650V N-Channel MOSFET

FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Device Marking and Package Information					
Device	Package	Marking			
CS5N65F	TO-220F	CS5N65F			
CS5N65P	TO-220	CS5N65P			
CS5N65U	TO-251	CS5N65U			
CS5N65D	TO-252	CS5N65D			



Parameter		Symbol	Value				
			TO-220F	TO-252	TO-251	TO-220	Unit
Drain-Source Voltage (V _{GS} = 0V)		V _{DSS}	650			V	
Continuous Drain Current		I _D	5				Α
Pulsed Drain Current	(note1)	I _{DM}	20				А
Gate-Source Voltage		V _{GSS}	±30				V
Single Pulse Avalanche Energy	(note2)	E _{AS}	115				mJ
Avalanche Current	(note1)	I _{AS}	4.8			Α	
Repetitive Avalanche Energy	(note1)	E _{AR}	69			mJ	
Power Dissipation (T _C = 25°C)		P_{D}	83 54			W	
Operating Junction and Storage Temperature Range		T _J , T _{stq}	-55~+150				°C

Thermal Resistance						
Parameter	Symbol	Value				11
		TO-220F	TO-252	TO-251	TO-220	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	2.3		1.5		°C/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	60		30/00	



Specifications $T_J = 25^{\circ}C$, unless otherwise noted								
Parameter	Symbol	Test Conditions	Value			Unit		
			Min.	Тур.	Max.			
Static			l	i				
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = 250 \mu A$	650			V		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μA		
Gate-Source Leakage	$I_{\rm GSS}$	$V_{GS} = \pm 30V$			±100	nA		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_D = 2.5A$		1.8	2.1	Ω		
Dynamic								
Input Capacitance	C _{iss}	V 0V		588		pF		
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$		61				
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		7				
Total Gate Charge	Q_g			13		nC		
Gate-Source Charge	Q_gs	$V_{DD} = 520V, I_{D} = 5A,$ $V_{GS} = 10V$		2				
Gate-Drain Charge	Q_{gd}	VGS — 10 V		7				
Turn-on Delay Time	t _{d(on)}			36		ns		
Turn-on Rise Time	t _r	$V_{DD} = 325V, I_{D} = 5A,$		16				
Turn-off Delay Time	t _{d(off)}	$R_G = 25 \Omega$		93				
Turn-off Fall Time	t _f			27				
Drain-Source Body Diode Character	istics							
Continuous Body Diode Current	I _s	T			5	A		
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			20			
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}\text{C}, I_{SD} = 2.5\text{A}, V_{GS} = 0\text{V}$			1.4	V		
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_{S} = 5A,$		677		ns		
Reverse Recovery Charge	Q _{rr}	$di_{F}/dt = 100A / \mu s$		1.9		μC		

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 1. Output Characteristics (T_J = 25°C)

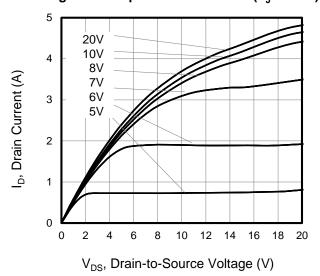


Figure 3. Drain Current vs. Temperature

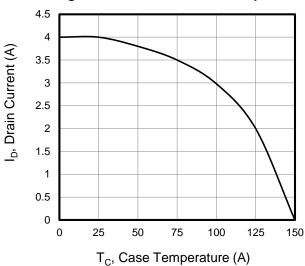


Figure 5. Transfer Characteristics

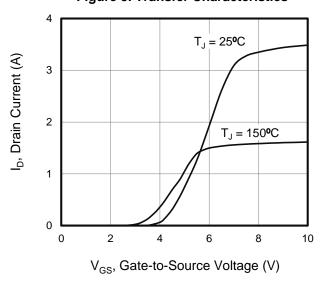


Figure 2. Body Diode Forward Voltage

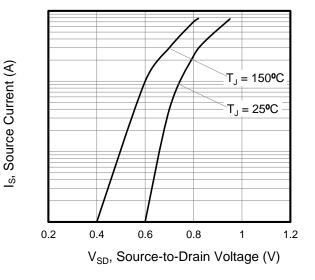


Figure 4. BV_{DSS} Variation vs. Temperature

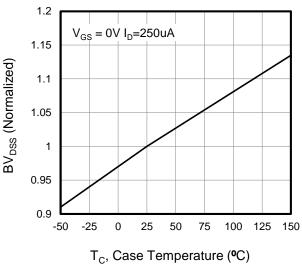
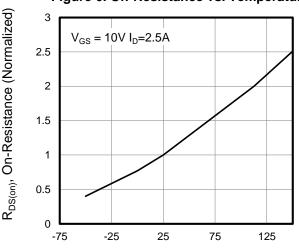


Figure 6. On-Resistance vs. Temperature



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

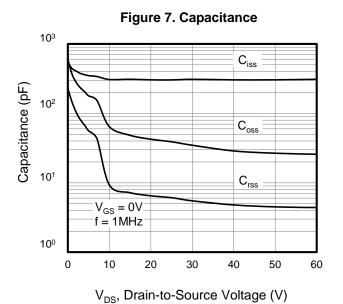


Figure 9. Transient Thermal Impedance TO-220,TO-251,TO-252

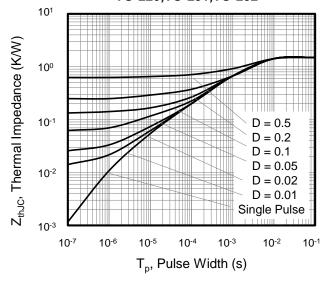


Figure 8. Gate Charge

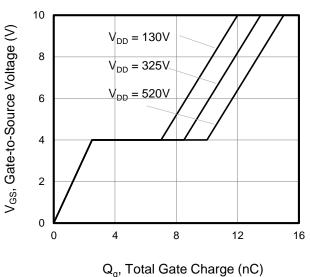


Figure 10. Transient Thermal Impedance

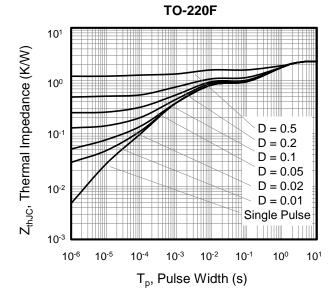


Figure A: Gate Charge Test Circuit and Waveform

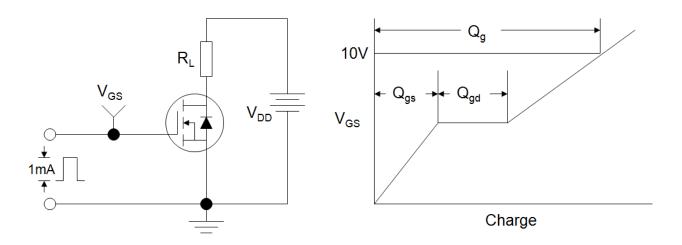


Figure B: Resistive Switching Test Circuit and Waveform

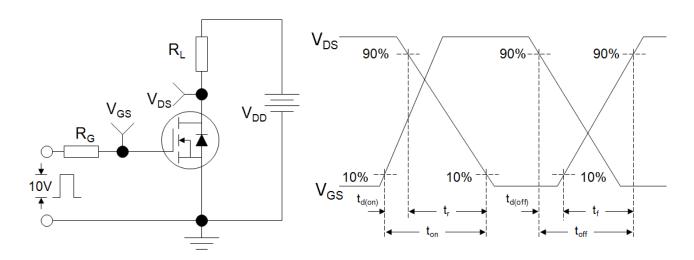
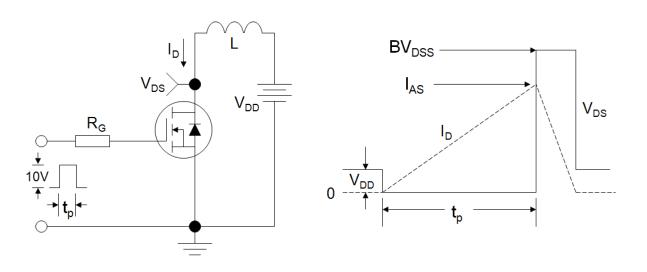
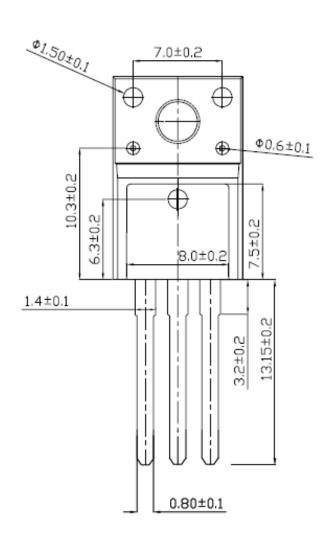
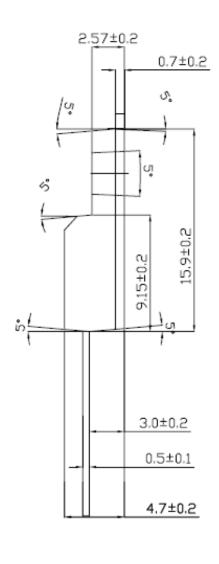


Figure C: Unclamped Inductive Switching Test Circuit and Waveform

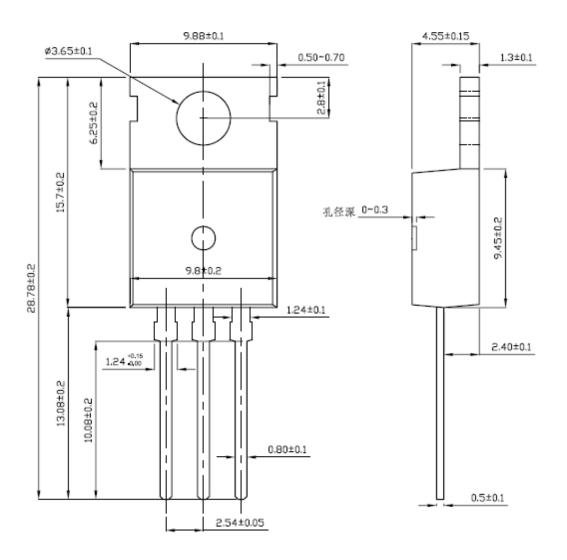


TO-220F

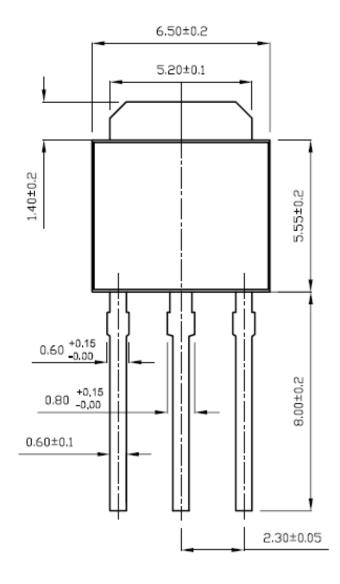


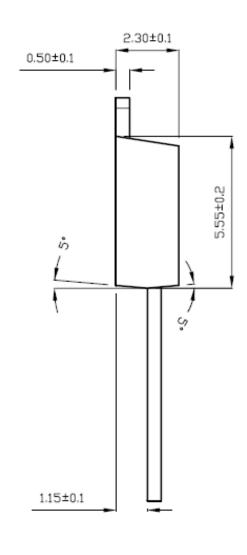


TO-220

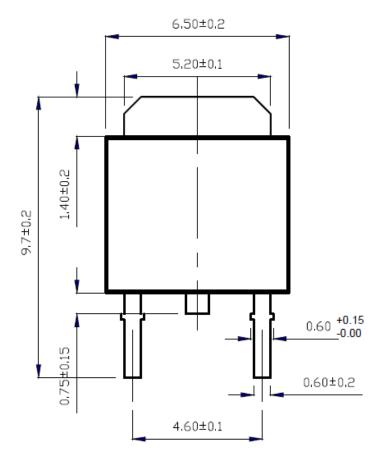


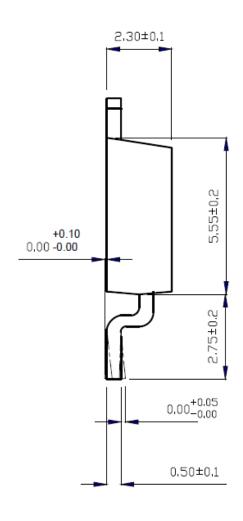
TO-251





TO-252







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