

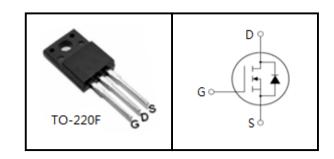
## **600V N-Channel MOSFET**

#### **FEATURES**

- Fast switching
- Integrate fast recovery diode
- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability

#### **APPLICATIONS**

- Switch Mode Power Supply (SMPS)
- Motor Controls
- Power Factor Correction (PFC)



Device Marking and Package Information					
Device	Package				
CSFR12N60F	TO-220F	CSFR12N60F			

<b>Absolute Maximum Ratings</b> $T_C = 25^{\circ}C$ , unless otherwise noted							
Parameter	Symbol	Value	Unit				
Drain-Source Voltage (V <sub>GS</sub> = 0V)	V <sub>DSS</sub>	600	V				
Continuous Drain Current	I <sub>D</sub>	12	Α				
Pulsed Drain Current (note1)	I <sub>DM</sub>	48	Α				
Gate-Source Voltage	V <sub>GSS</sub>	±30	V				
Single Pulse Avalanche Energy (note2)	E <sub>AS</sub>	480.2	mJ				
Avalanche Current (note1)	I <sub>AS</sub>	9.8	Α				
Repetitive Avalanche Energy (note1)	E <sub>AR</sub>	288.1	mJ				
Power Dissipation (T <sub>C</sub> = 25°C)	P <sub>D</sub>	70	W				
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55~+150	°C				

Thermal Resistance				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	$R_{thJC}$	1.78	K/W	
Thermal Resistance, Junction-to-Ambient	R <sub>thJA</sub>	62.5		



<b>Specifications</b> $T_J = 25^{\circ}$ C, unless otherwise noted								
Parameter	Comple al	Tool Conditions	Value			l lmit		
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	600			V		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 600V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ		
		V <sub>DS</sub> = 480V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C			100	μA		
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 30V$			±100	nA		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	R <sub>DS(on)</sub>	$V_{GS} = 10V, I_D = 6.0A$	-	0.63	0.75	Ω		
Dynamic								
Input Capacitance	C <sub>iss</sub>	V - 0V		1667		pF		
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0V,$ $V_{DS} = 25V,$		161				
Reverse Transfer Capacitance	$C_{rss}$	f = 1.0MHz	1	19	1			
Total Gate Charge	$Q_g$			48.7		nC		
Gate-Source Charge	$Q_{gs}$	$V_{DD} = 480V, I_{D} = 12A,$ $V_{GS} = 10V$		7.6				
Gate-Drain Charge	$Q_gd$		ł	25	-			
Turn-on Delay Time	t <sub>d(on)</sub>		1	45.5	-	ns		
Turn-on Rise Time	t <sub>r</sub>	$V_{DD} = 300V, I_{D} = 12A,$	1	26	1			
Turn-off Delay Time	t <sub>d(off)</sub>	$R_G = 25 \Omega$	1	195	1			
Turn-off Fall Time	t <sub>f</sub>		-	66				
Drain-Source Body Diode Character	istics							
Continuous Body Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			12	A		
Pulsed Diode Forward Current	I <sub>SM</sub>	1 <sub>C</sub> = 20 · O			48			
Body Diode Voltage	$V_{SD}$	$T_J = 25^{\circ}C$ , $I_{SD} = 6A$ , $V_{GS} = 0V$			1.4	V		
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0V, I_{S} = 12A,$		117		ns		
Reverse Recovery Charge	$Q_{rr}$	di <sub>F</sub> /dt =100A /µs	-	0.25	-	μC		

#### Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L=10mH,  $V_{DD}$  = 50V,  $R_G$  = 25 $\Omega$ , Starting  $T_J$  = 25 $^{\circ}$  C
- 3. Pulse Test: Pulse width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  1%



## **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted



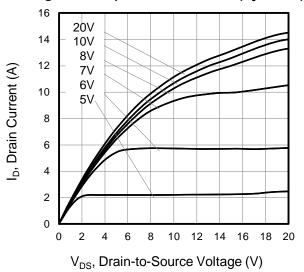


Figure 2. Body Diode Forward Voltage

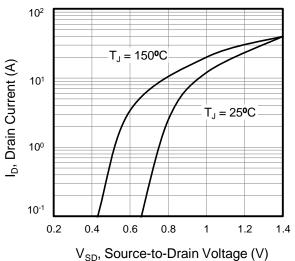


Figure 3. Drain Current vs. Temperature

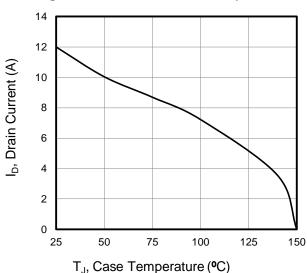


Figure 4. BV<sub>DSS</sub> Variation vs. Temperature

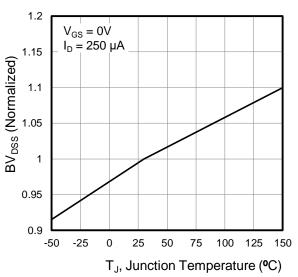


Figure 5. Transfer Characteristics

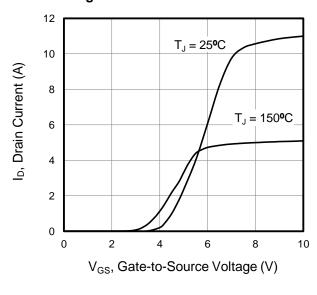
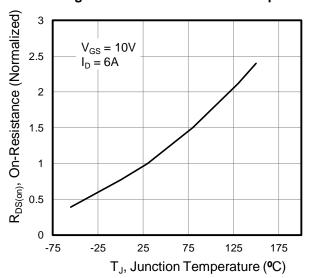


Figure 6. On-Resistance vs. Temperature





## **Typical Characteristics** $T_J = 25^{\circ}\text{C}$ , unless otherwise noted

Figure 7. Capacitance

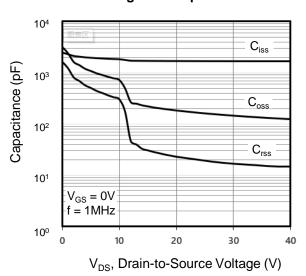


Figure 8. Gate Charge

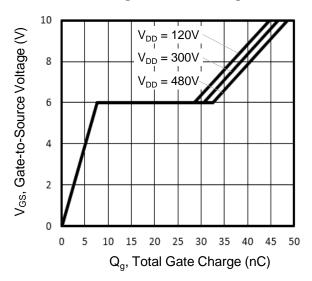


Figure 9. Transient Thermal Impedance TO-220F

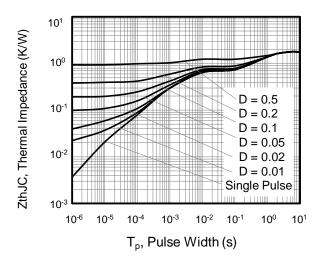




Figure A: Gate Charge Test Circuit and Waveform

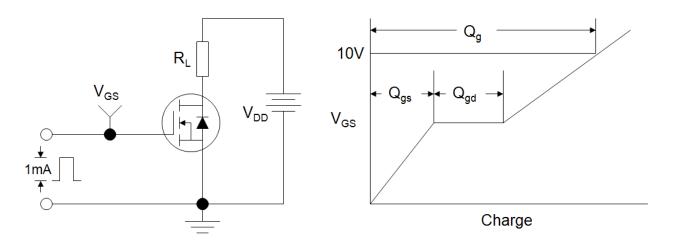


Figure B: Resistive Switching Test Circuit and Waveform

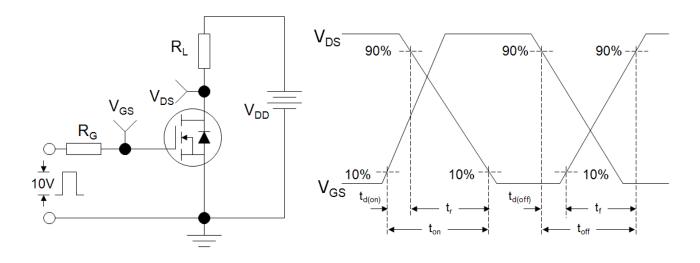
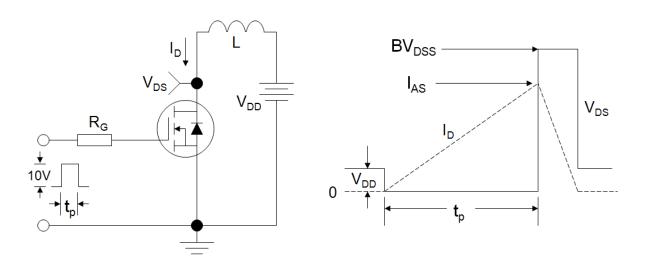
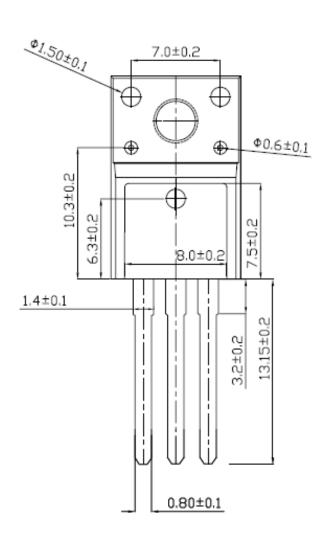


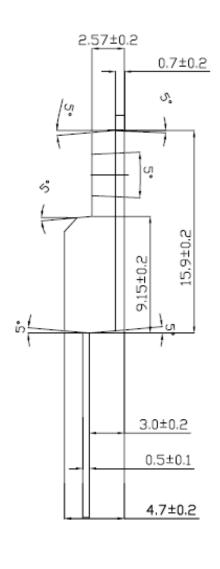
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





# **TO-220F**







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