## 100V N-Channel Trench MOSFET

## FEATURES

- Super Low Gate Charge
- 100\% EAS Guaranteed
- RoHS compliant
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology


## APPLICATIONS

- Power switching application
- LED TV Backlight Module
- LCD Application System

| Device Marking and Package Information |
| :--- | :--- |


| Device | Package | Marking |
| :--- | :--- | :--- |
| CTD10N100 | TO-252 | CTD10N100 |

Absolute Maximum Ratings at $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ unless otherwise noted

| Parameter | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| Drain-Source Voltage ( $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}$ ) | $V_{\text {DSS }}$ | 100 | V |
| Drain Current-Continuous(Tc = $25^{\circ} \mathrm{C}$ ) (note1) | $I_{\text {D }}$ | 17 | A |
| Drain Current-Continuous $\left(\mathrm{Tc}=100^{\circ} \mathrm{C}\right.$ ) (note1) |  | 13 |  |
| Pulsed Drain Current (note2) | $\mathrm{I}_{\mathrm{DM}}$ | 25 | A |
| Gate Source Voltage | $\mathrm{V}_{\text {GSS }}$ | $\pm 20$ | V |
| Power Dissipation $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ (note4) | $\mathrm{P}_{\mathrm{D}}$ | 30 | W |
| Single Pulse Avalanche Energy (note3) | $\mathrm{E}_{\text {AS }}$ | 0.8 | mJ |
| Operating Junction and Storage Temperature Range | $\mathrm{T}_{\mathrm{J}}, \mathrm{T}_{\text {stg }}$ | $-55 \sim+175$ | ${ }^{\circ} \mathrm{C}$ |


| Thermal Characteristics |  |  |  |  |  |  |  |
| :--- | :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Parameter | Symbol | Value | Unit |  |  |  |  |
| Thermal Resistance Junction-to-ambient | (note1) | $\mathrm{R}_{\text {өJA }}$ | 62 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |  |  |  |
| Thermal Resistance Junction-Case | (note1) | $\mathrm{R}_{\text {өJc }}$ | 3.6 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |  |  |  |

Electrical Characteristics $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Parameter | Symbol | Test Conditions | Value |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Typ. | Max. |  |
| Static |  |  |  |  |  |  |
| Drain-Source Breakdown Voltage | $\mathrm{V}_{\text {(BR)DSs }}$ | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=250 \mu \mathrm{~A}$ | 100 | -- | -- | V |
| Zero Gate Voltage Drain Current | $\mathrm{I}_{\text {DSS }}$ | $\mathrm{V}_{\mathrm{DS}}=80 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ | -- | -- | 1 | uA |
| Zero Gate Voltage Drain Current |  | $\mathrm{V}_{\mathrm{DS}}=80 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=55^{\circ} \mathrm{C}$ | -- | -- | 5 |  |
| Gate-Source Leakage | $\mathrm{I}_{\text {GSS }}$ | $\mathrm{V}_{\mathrm{GS}}= \pm 20 \mathrm{~V}$ | -- | -- | $\pm 100$ | nA |
| Gate-Source Threshold Voltage | $\mathrm{V}_{\text {GS(th) }}$ | $\mathrm{V}_{\mathrm{DS}}=\mathrm{V}_{\mathrm{GS}}, \mathrm{I}_{\mathrm{D}}=250 \mu \mathrm{~A}$ | 1.2 | -- | 2.9 | V |
| Drain-Source On-Resistance (note2) | $\mathrm{R}_{\mathrm{DS} \text { (on) }}$ | $\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=10 \mathrm{~A}$ | -- | -- | 100 | $m \Omega$ |
|  |  | $\mathrm{V}_{\mathrm{GS}}=4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=8 \mathrm{~A}$ | -- | -- | 110 | $\mathrm{m} \Omega$ |

## Dynamic

| Input Capacitance | $\mathrm{C}_{\text {iss }}$ | $\begin{gathered} V_{G S}=0 \mathrm{~V}, \\ V_{D S}=50 \mathrm{~V}, \\ f=1.0 \mathrm{MHz} \end{gathered}$ | -- | 450 | -- | pF |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output Capacitance | $\mathrm{C}_{\text {oss }}$ |  | -- | 55 | -- |  |
| Reverse Transfer Capacitance | $\mathrm{C}_{\text {rss }}$ |  | -- | 16 | -- |  |
| Total Gate Charge (4.5V) | $Q_{g}$ | $\begin{gathered} \mathrm{V}_{\mathrm{DS}}=50 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=15 \mathrm{~A}, \\ \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V} \end{gathered}$ | -- | 11.9 | -- | nC |
| Gate-Source Charge | $\mathrm{Q}_{\mathrm{gs}}$ |  | -- | 2.8 | -- |  |
| Gate-Drain Charge | $\mathrm{Q}_{\mathrm{gd}}$ |  | -- | 1.7 | -- |  |
| Turn-on Delay Time | $\mathrm{t}_{\text {d(on) }}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{DS}}=50 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=15 \mathrm{~A} \\ & \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{R}_{\mathrm{G}}=3 \Omega \end{aligned}$ | -- | 3.8 | -- | ns |
| Turn-on Rise Time | $\mathrm{t}_{\mathrm{r}}$ |  | -- | 25.8 | -- |  |
| Turn-off Delay Time | $\mathrm{t}_{\text {d(off) }}$ |  | -- | 16 | -- |  |
| Turn-off Fall Time | $\mathrm{t}_{\mathrm{f}}$ |  | -- | 8.8 | -- |  |
| Body Diode Characteristics |  |  |  |  |  |  |
| Source-Drain Current(Body Diode) | $\mathrm{I}_{\text {SD }}$ |  | -- | -- | 17 | A |
| Pulsed <br> Diode) | $\mathrm{I}_{\text {SDM }}$ |  | -- | -- | 25 | A |
| Body Diode Voltage | $\mathrm{V}_{\text {SD }}$ | $\mathrm{I}_{\mathrm{SD}}=22 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ | -- | -- | 1.2 | V |

## Notes

1. The data tested by surface mounted on a 1 inch2 FR-4 board with $2 O Z$ copper.
2. The data tested by pulsed, pulse width $\leqq 300$ us , duty cycle $\leq 2 \%$
3. The EAS data shows Max. rating. The test condition is VDD $=25 \mathrm{~V}, \mathrm{VGS}=10 \mathrm{~V}, \mathrm{~L}=0.1 \mathrm{mH}$
4. The power dissipation is limited by $175^{\circ} \mathrm{C}$ junction temperature
5. The data is theoretically the same as ID and IDM , in real applications, should be limited by total power dissipation.

Typical Characteristics $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$, unless otherwise noted


Fig. 1 Typical Output Characteristics



Fig. 2 On-Resistance vs. G-S Voltage


Fig. 3 Source Drain Forward Characteristics Fig. 4 Gate-Charge Characteristics



Fig. 5 Normalized VGS(th) vs. TJ Fig. 6 Normalized RDSON vs. TJ

CTD10N100

Typical Characteristics $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$, unless otherwise noted


Fig. 7 Capacitance


Fig. 9 Normalized Maximum Transient Thermal Impedance

Figure A: Gate Charge Test Circuit and Waveform


Figure B: Resistive Switching Test Circuit and Waveform


Figure C: Unclamped Inductive Switching Test Circuit and Waveform


## TO-252



| Unit: mm |  |  |
| :---: | :--- | :---: |
| Symbol | Min. | Max. |
| A | 2.20 | 2.40 |
| A1 | 0.00 | 0.20 |
| A2 | 0.97 | 1.17 |
| b | 0.68 | 0.90 |
| b3 | 5.20 | 5.50 |
| c | 0.43 | 0.63 |
| D | 5.98 | 6.22 |
| D1 | 5.30 REF |  |
| E | 6.40 | 6.80 |
| E1 | 4.63 | - |


| Unit: mm |  |  |
| :---: | :---: | :---: |
| Symbol | Min. | Max. |
| e | 2.286 BSC |  |
| H | 9.40 | 10.50 |
| L | 1.38 | 1.75 |
| L1 | 2.90 REF |  |
| L2 | 0.51 BSC |  |
| L3 | 0.88 | 1.28 |
| L4 | - | 1.00 |
| L5 | 1.65 | 1.95 |
| $\theta$ | $0^{\circ}$ | $8^{\circ}$ |

CTD10N100

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