



FEATURES

Small size - High voltage – High Capacitance

APPLICATIONS

UPS – DC link – AC/DC motor controls – Power inverters

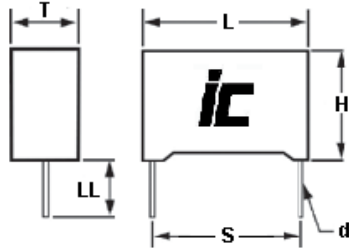
Operating Temperature Range		-40°C to +100°C Above +85°C applied voltage must be de-rated by 1.5%/°C								
Capacitance Tolerance		±10% at 1 kHz, 25°C ±5% optional								
Surge Voltage	VDC	Code	A35	A40	A45	A50	A55	A60	A70	
		85°C	575	700	800	900	1000	1100	1275	
		70°C	630	770	880	1000	1100	1200	1400	
	Repetitive SVDC	85°C	660	805	920	1035	1150	1265	1465	
		70°C	720	885	1010	1150	1265	1380	1610	
	Non-repetitive SVDC	85°C	750	910	1040	1170	1300	1430	1655	
70°C		815	1000	1140	1300	1430	1560	1820		
Repetitive SVDC: Maximum repetitive voltage for 1 hour Max/Day										
AC Voltage	Code		A35	A40	A45	A50	A55	A60	A70	
	VDC		575	700	800	900	1000	1100	1275	
	VAC	85°C	240	285	315	350	400	420	440	
		70°C	250	300	330	370	420	440	460	
Dissipation Factor (MAX) Tan δ (%) at 1 kHz and 25°C			C ≤ 4μF	4μF < C ≤ 12μF	12μF < C ≤ 20μF	20μF < C ≤ 40μF	40μF < C ≤ 75μF	C > 75μF		
			.06	.08	.11	.14	.17	.2		
ESR (mΩ)			Lead spacing (mm)		Frequency range		ESR Value			
			27.5		10kHz - 60kHz		See standard part listing			
			37.5		10kHz - 45 kHz		See standard part listing			
			52.5		10kHz - 30 kHz		See standard part listing			
RMS Current			No RMS current is to be applied at temperatures > +95°C							
Ripple Current			See standard part listing, No ripple current is to be applied when the ambient temperature is ≥ +95°C							
Insulation Resistance			3000 MΩxμF (10000 MΩxμF typical) "Not to exceed 3GΩ" After 1 minute of 100VDC applied between the terminals at 25°C							
Self Inductance			<1 nano-Henry per mm of lead spacing and lead length							
Long Term Stability			Capacitance variation <1% MAX after 2 years							
Dielectric Strength			Terminal to Terminal				Terminal to case			
			150% of VDC or 150% of VAC applied for 10 Seconds and 25°C				3KVAC at 50/60 Hz applied between the terminals and case for 60 Seconds and 25°C			



Life Expectancy	≥ 60000 with VAC, ≥ 200000 With WVDC applied at $+70^{\circ}\text{C}$ ≥ 30000 with VAC, ≥ 100000 With WVDC applied at $+85^{\circ}\text{C}$ ≥ 10000 with $0.8 \times \text{VAC}$, ≥ 30000 With $0.8 \times \text{WVDC}$ applied at $+100^{\circ}\text{C}$		
Failure Quota	300/billion component hours		
Damp Heat Test	Test Condition	Performance requirements	
	Temperature= $+40^{\circ}\text{C} \pm 2^{\circ}\text{C}$	Max Capacitance change	$\leq \pm 3\%$ of initially measured valued
	Relative Humidity= $93 \pm 2\%$	DF Change	$\leq 200\%$ of initially specified valued
	Test length= 56 days	Insulation resistance	$\geq 50\%$ of minimum specified value
Resistance to soldering Heat Solder bath temperature: $+260^{\circ}\text{C} \pm 5^{\circ}\text{C}$ Exposure time: 10 seconds ± 1 second	Max Capacitance change	$\leq 1\%$ of initially measured value	
	DF Change	$\leq .1\%$ at 1kHz	
	Insulation resistance	$\geq 50\%$ of minimum specified value	
Construction	Metallized Polypropylene film		
Electrodes	Vacuum deposited Metal layers		
Coating	Flame retardant Solvent resistant plastic case with epoxy end fill		
Lead Terminations	Lead free tinned copper leads		

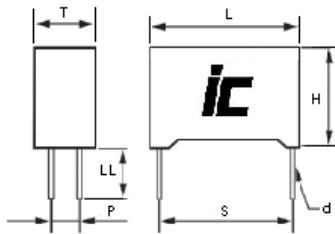


Lead styles



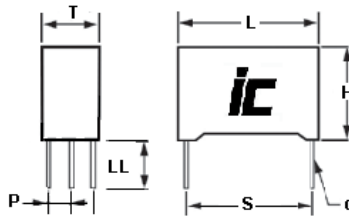
2 leads

Length (L)	32	41.5	42.5	57.5
Lead spacing (S)	27.5	37.5	37.5	52.5
Lead Length (LL)	5mm +/-1 mm			



4 leads

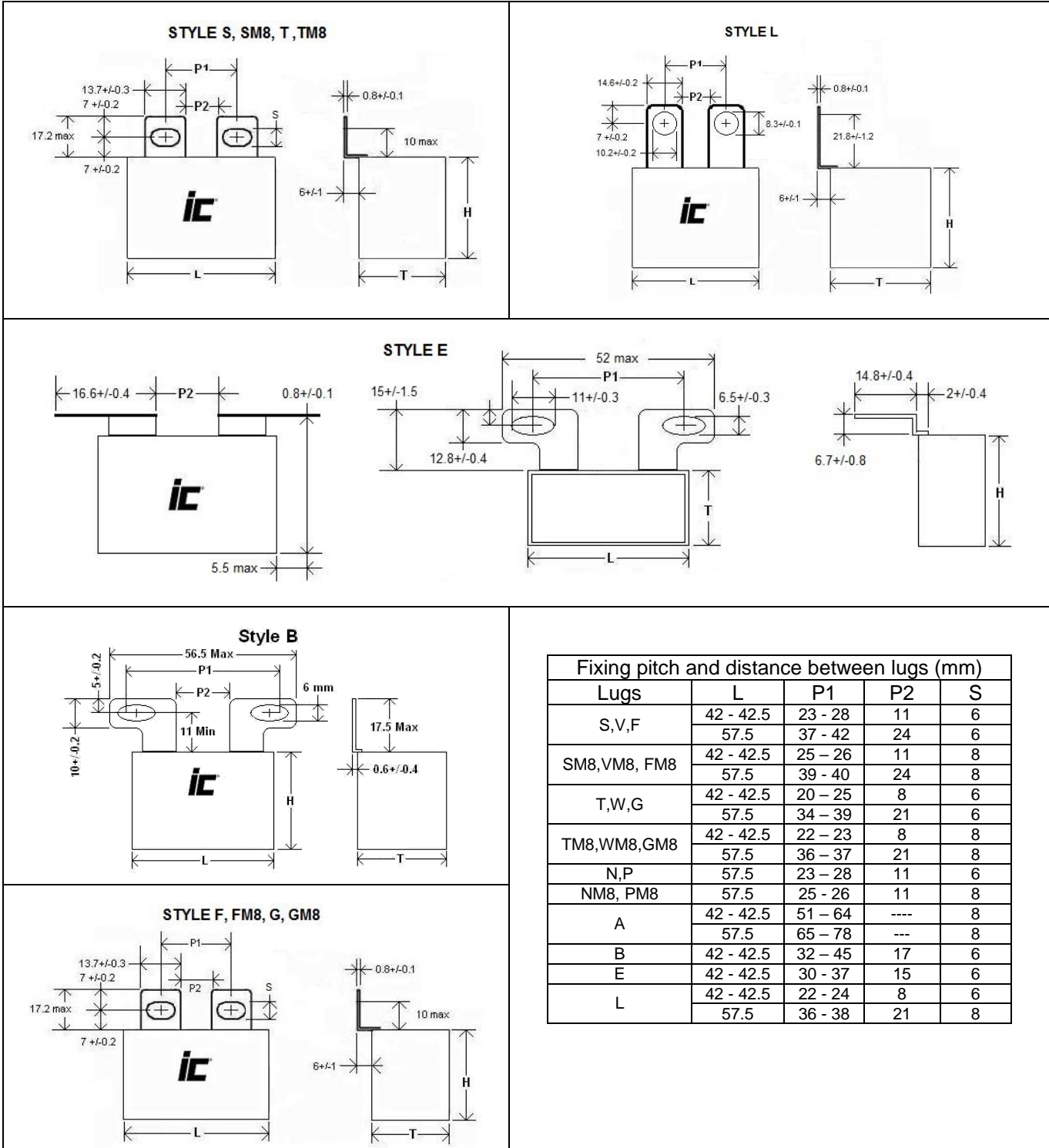
Length (L)	32	41.5	42.5	57.5
Lead spacing (S)	27.5	37.5	37.5	52.5
Lead Spacing (P)	See part listing			
Lead Length (LL)	5mm +/-1 mm			



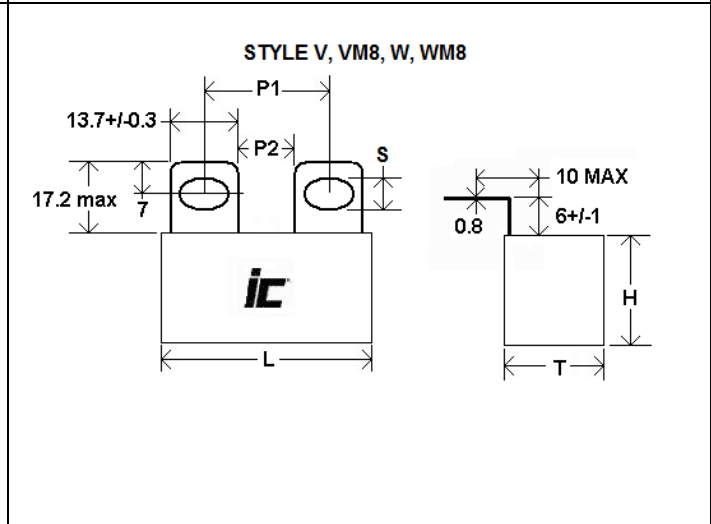
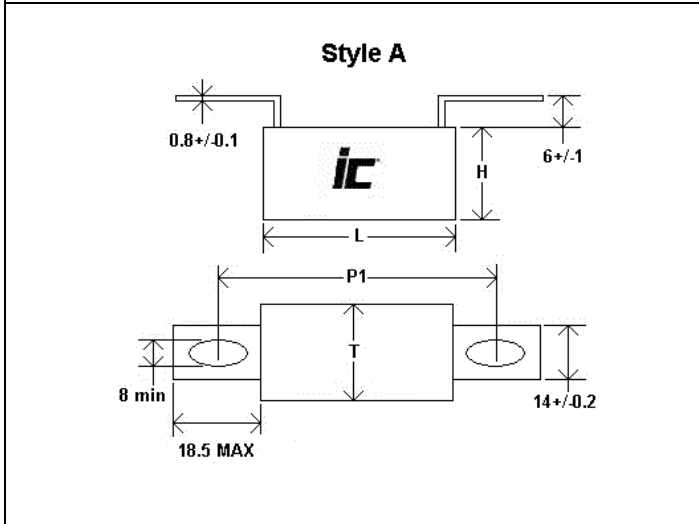
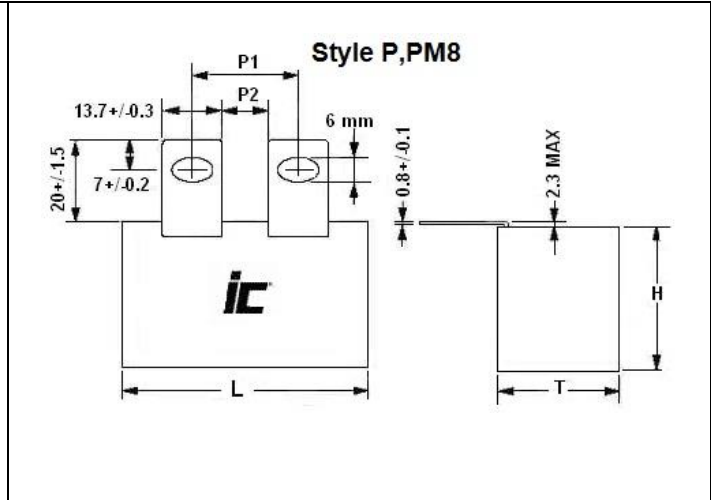
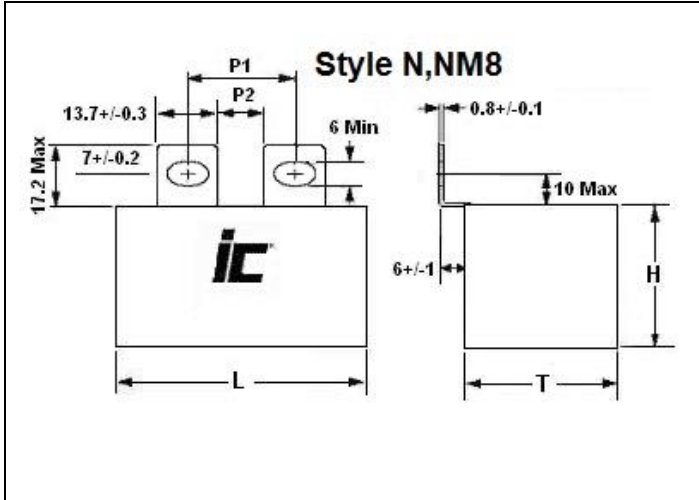
6 leads

Length (L)	32	41.5	42.5	57.5
Lead spacing (S)	27.5	37.5	37.5	52.5
Lead Spacing (P)	See part listing			
Lead Length (LL)	5mm +/-1 mm			

Dimensions in mm



Dimensions in mm



Dimensions in mm

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