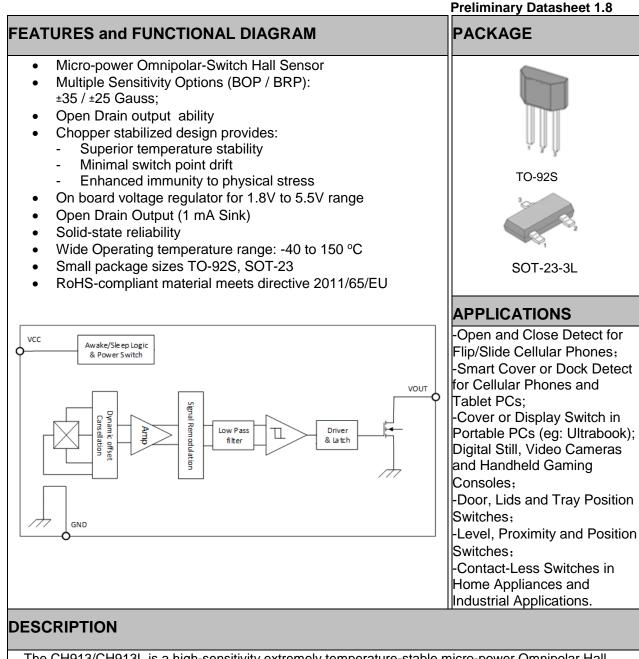


CH913/CH913L

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The CH913/CH913L is a high-sensitivity extremely temperature-stable micro-power Omnipolar Hall effect switch IC with internal pull down capability. Designed for portable and battery powered consumer equipment such as cellular phones and portable PCs to office equipment, home appliances and industrial applications, the average supply current is only 1.62µA at 3.3V for CH913 and 0.92uA at 3.3V for CH913L. To support potable equipment the CH913/CH913L can operate over the supply range of 1.8V to 5.5V and uses a hibernating clocking system to minimize the power consumption.

The device includes a clocking system, a Hall-voltage generator, a small-signal amplifier, a chopper stabilization, two Schmitt trigger, and an output driver controller.

The output is activated with either a north or south pole of sufficient magnetic field strength. When the magnetic flux density (B) perpendicular to the package is larger than operate point (Bop), the output will be turned on (pulled low) and held until B is lower than release point (Brp).

Two package styles provide a magnetically optimized package for most applications, SO-T23 and TO-92S. Each package type is lead (Pb) free (suffix, –T), with a 100% matte-tin-plated leadframe.



CH913/CH913L

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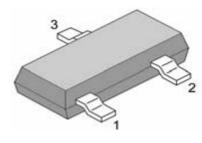
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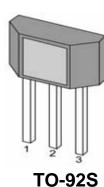
1. Product Family Members

Part Number	Marking ID	Description
CH913SR	C913	Micro-power Omnipolar-Switch, Hall-effect digital sensor IC, SOT-23-3L package, tape and reel packing (3000 units per reel)
CH913TB	C913	Micro-power Omnipolar-Switch, Hall-effect digital sensor IC, flat, TO-92S package, bulk packing (1000 units per bag)
CH913LSR	913L	Micro-power Omnipolar-Switch, Hall-effect digital sensor IC, SOT-23-3L package, tape and reel packing (3000 units per reel)
CH913LTB	913L	Micro-power Omnipolar-Switch, Hall-effect digital sensor IC, flat, TO-92S package, bulk packing (1000 units per bag)

2. Pin Definitions and Descriptions

SOT-23-3L (S)	TO-92S (T)	Name	Туре	Function
1	1	VDD	Supply	Supply Voltage pin
2	3	OUT	Output	Open Drain Output pin
3	2	GND	Ground	Ground pin





SOT-23-3L

3. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Units
Supply Voltage	V _{DD}	-	6	V
VDD Reverse Voltage VDD	V _{RDD}	-0.3		V
Supply Current	I _{DD}	-	2	mA
Output Voltage	V _{OUT}	-0.3	VDD+0.3	V
Output Current	IOUT	-	3	mA
Operating Ambient Temperature	T _A	-40	150	°C
Storage Temperature	Ts	-50	150	°C
Junction temperature	TJ	-50	165	°C
Magnetic Flux	В	No Limit Gau		Gauss

Note: Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolutemaximum- rated conditions for extended periods may affect device reliability.



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4. ESD Protections

Parameter	Value	Unit
All pins ¹⁾	+/-8000	V
All pins ²⁾	+/-400	V
All pins ³⁾	+/-750	V

1) HBM (Human Body Mode) according to AEC-Q100-002

2) MM (Machine Mode) according to AEC-Q100-003

3) CDM (charged device mode) according to AEC-Q100-011

5. Function Description

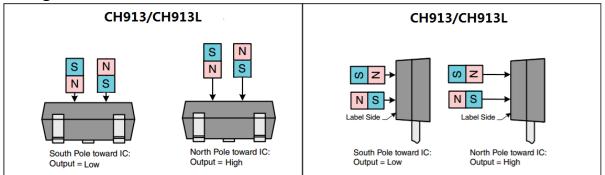
The CH913/CH913L exhibits Micro-power digital Omnipolar switching characteristics.

Therefore, it requires only south poles or north poles to operate properly.

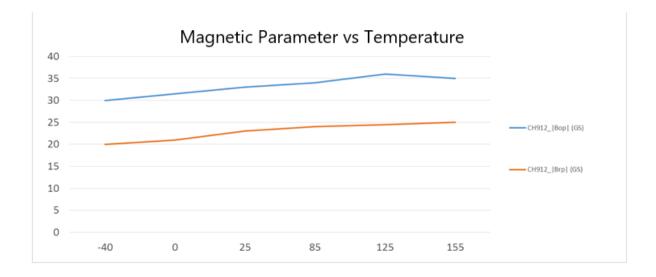
When the applied magnetic flux density exceeds the BOP threshold, the chip push-pull output goes low. The output stays low until the field decreases to less than BRP, and then the output goes to high.

A magnetic hysteresis BHYST keeps BOP and BRP separated by a minimal value. This hysteresis prevents output oscillation near the switching point.

6. Magnetic Activation



7. Temperature Characteristics





CH913/CH913L

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8. Parameters Specification (VCC=3V supply, TA= -40 °C to 150 °C except where otherwise specified.)

Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
Vcc	Supply Voltage	Operating, TJ < 165°C	1.8		5.25	V
	CH913 Supply Current	During awake period, TA =	-	1.1	1.6	mA
ICC(awake)	C(awake) CH913L Supply Current 25°C, VCC=3.3V		_	1.1	1.6	mA
	CH913 Supply Current	During sleep period, TA =	-	0.7	_	uA
CC(sleep)	CH913L Supply Current	25°C, VCC=3.3V	_	0.7	_	uA
		TA = 25°C, VDD = 1.8V		1.09		uA
	current	TA = 25°C, VDD = 3.3V		1.62		uA
ICC(avg)	CH913L Average supply	TA = 25°C, VDD = 1.8V		0.52		uA
	current	TA = 25°C, VDD = 3.3V		0.92		uA
Vol	Output low voltage(on)	I _{OUT} =1 mA	-	0.1	0.2	V
Vон	Output high voltage(off)	I _{OUT} = -1mA	V _{CC} - 0.2	V _{cc} - 0.1	_	V
т.	CH913 Awake time	(note4)	I	40	60	us
Tawake	CH913L Awake time	(note4)		40	60	us
т	CH913 Period	(note4)	-	50	75	ms
T _{period}	CH913L Period	(note4)	-	190	280	mS
D.C.	Duty cycle CH913	_	-	0.08	_	%
D.C.	Duty cycle CH913L	_	-	0.02	_	%
fc	Chopping Frequency		Ι	500	_	kHz
I _{OFF}	Output Leakage Current	VOUT = 5.5 V; Switch state = Off	-	_	1	μA
Вор	Operate point	VDD = 1.8V to 5.25V TA = -40°C to 150°C	±20	±35	±55	Gauss
Brp	Release point	VDD = 1.8V to 5.25V TA = -40°C to 150°C	±10	±25	±50	Gauss
Hys	Hysteresis	VDD = 1.8V to 5.25V TA = -40°C to 150°C	_	10	_	Gauss

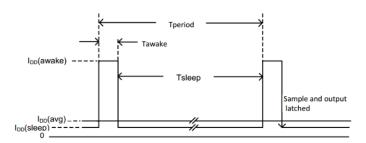
1. 1G (gauss) = 0.1 mT (millitesla).

2. Output Rise Time is governed by external circuits tied to VOUT.

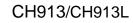
3. Measured from 10% to 90% of the steady state output.

4. When power is initially turned on, the operating VCC (1.8V to 5.5V) must be applied to guaranteed the output sampling. The output state is valid after the second operating cycle (typical 100ms).

NOTICE: The magnetic field strength (Gauss) required to cause the switch to change state (operate and release) will be as specified in the magnetic characteristics. To test the switch against the specified magnetic characteristics, the switch must be placed in a uniform magnetic field.







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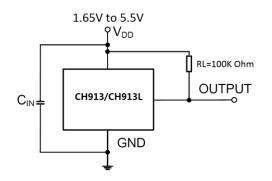
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9. Application Information

9.1 Typical Application

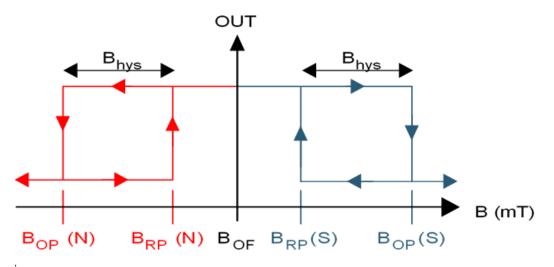
It is recommended that an external capacitor C_{IN} is connected to the supply. This can reduce the noise injected into the device. Normal 0.1uF is suggested.



Typical Application Circuit

9.2 Device Output

If the device is powered on with a magnetic field strength between BRP and BOP, then the device output is indeterminate and can either be high or Low. If the field strength is greater than BOP, then the output is pulled low. If the field strength is less than BRP, then the output is pulled high.



9.3 Overcurrent Protection (OCP)

An analog current-limit circuit limits the current through the FET. The driver current is clamped to IOCP. During this clamping, the rDS(on) of the output FET is increased from the nominal value.

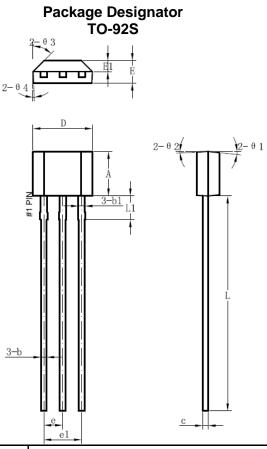


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10. Package Information:



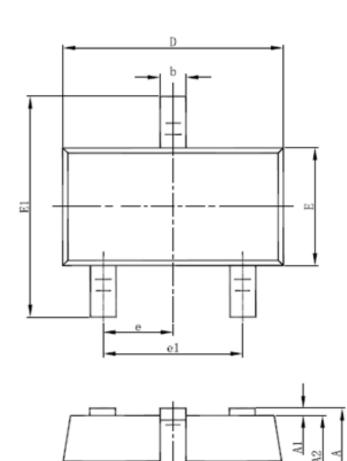
Symbol	Dimensions in Millimeters				
Symbol	Min.	Тур.	Max.		
A	2.9	3	3.1		
b	0.35	0.39	0.56		
b1		0.44			
С	0.36	0.38	0.51		
D	3.9	4	4.1		
E	1.42	1.52	1.62		
E1		0.75			
е		1.27			
e1		2.54			
L	13.5	14.5	15.5		
L1		1.6			
θ1		6°			
θ2		3°			
θ3		45°			
θ4		3°			

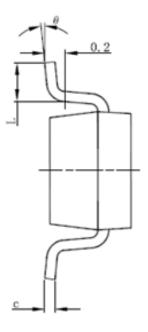


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PACKAGE DESIGNATOR SOT-23-3L







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Sumbal	Dimensions Ir	n Millimeters	Dimensions	In Inches
Symbol	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
е	0.950	(BSC)	0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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