

**Features**

- Uses CRM(CQ) advanced SkyMOS1 technology
- Extremely low on-resistance RDS(on)
- Excellent Qg×RDS(on) product(FOM)
- Qualified according to JEDEC criteria

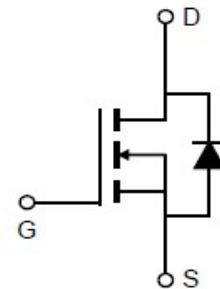
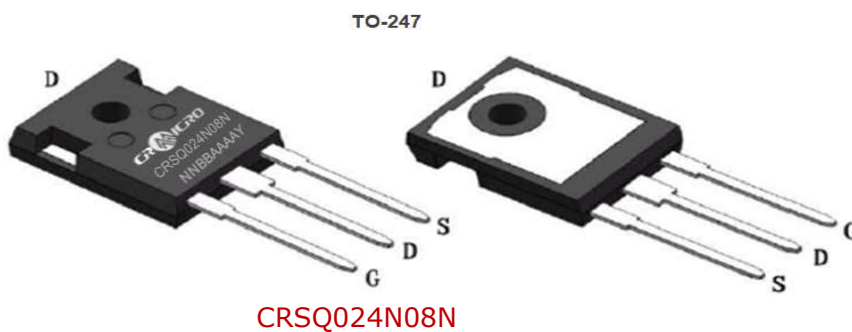
**Applications**

- Motor control and drive
- Battery management
- UPS (Uninterruptible Power Supplies)

**Product Summary**

V <sub>DS</sub>	80V
R <sub>DS(on)</sub>	2mΩ
I <sub>D</sub>	180A

**100% Avalanche Tested**  
**100% DVDS Tested**


**Package Marking and Ordering Information**

MARKING	流通码	Package	Packing	Reel Size	Tape Width	Qty
CRSQ024N08N	-	TO-247	Tube	N/A	N/A	30pcs

**Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit
Drain-source voltage	V <sub>DS</sub>	80	V
Continuous drain current	I <sub>D</sub>	272	A
T <sub>C</sub> = 25°C (Silicon limit)		180	
T <sub>C</sub> = 25°C (Package limit)		172	
T <sub>C</sub> = 100°C (Silicon limit)			
Pulsed drain current (T <sub>C</sub> = 25°C, t <sub>p</sub> limited by T <sub>jmax</sub> )	I <sub>D pulse</sub>	720	A
Avalanche energy, single pulse (L=0.5mH, Rg=25Ω) <sup>[1]</sup>	E <sub>AS</sub>	600	mJ
Gate-Source voltage	V <sub>GS</sub>	±20	V
Power dissipation (T <sub>C</sub> = 25°C)	P <sub>tot</sub>	291	W
Operating junction and storage temperature	T <sub>j</sub> , T <sub>stg</sub>	-55...+150	°C

※. Notes: 1.EAS is tested at starting T<sub>j</sub> = 25°C, L = 0.5mH, IAS = 49A, Vgs=10V.

**Thermal Resistance**

Parameter	Symbol	Max	Unit
Thermal resistance, junction – case.	$R_{thJC}$	0.43	°C/W
Thermal resistance, junction – ambient(min. footprint)	$R_{thJA}$	62	

**Electrical Characteristic (at  $T_j = 25\text{ }^\circ\text{C}$ , unless otherwise specified)**

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

**Static Characteristic**

Drain-source breakdown voltage	$BV_{DSS}$	80	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	2	3	4	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Zero gate voltage drain current	$I_{DSS}$	-	-	1	$\mu A$	$V_{DS}=80V, V_{GS}=0V$ $T_j=25^\circ C$ $T_j=150^\circ C$
Gate-source leakage current	$I_{GSS}$	-	$\pm 10$	$\pm 100$	nA	$V_{GS}=\pm 20V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	2.0	2.4	mΩ	$V_{GS}=10V, I_D=60A$
Transconductance	$g_{fs}$	-	187.1	-	S	$V_{DS}=5V, I_D=60A$

**Dynamic Characteristic**

Input Capacitance	$C_{iss}$	-	12796	-	pF	$V_{GS}=0V, V_{DS}=40V,$ $f=1MHz$
Output Capacitance	$C_{oss}$	-	2815	-		
Reverse Transfer Capacitance	$C_{rss}$	-	35	-		
Gate Total Charge	$Q_G$	-	206.2	-	nC	$V_{GS}=10V, V_{DS}=40V,$ $I_D=60A, f=1MHz$
Gate-Source charge	$Q_{gs}$	-	68.5	-		
Gate-Drain charge	$Q_{gd}$	-	51.4	-		
Turn-on delay time	$t_{d(on)}$	-	39.5	-	ns	$V_{GS}=10V, V_{DD}=40V,$ $R_{G\_ext}=2.7\Omega$
Rise time	$t_r$	-	120.5	-		
Turn-off delay time	$t_{d(off)}$	-	109	-		
Fall time	$t_f$	-	125.2	-		
Gate resistance	$R_G$	-	1.8	-	Ω	$V_{GS}=0V, V_{DS}=0V,$ $f=1MHz$

**Body Diode Characteristic**

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	$V_{SD}$	-	0.91	1.3	V	$V_{GS}=0V, I_{SD}=60A$
Body Diode Reverse Recovery Time	$t_{rr}$	-	93.4	-	ns	$I_F=60A,$ $dI/dt=100A/us$
Body Diode Reverse Recovery Charge	$Q_{rr}$	-	162.9	-	nC	

### Typical Performance Characteristics

Fig 1: Output Characteristics

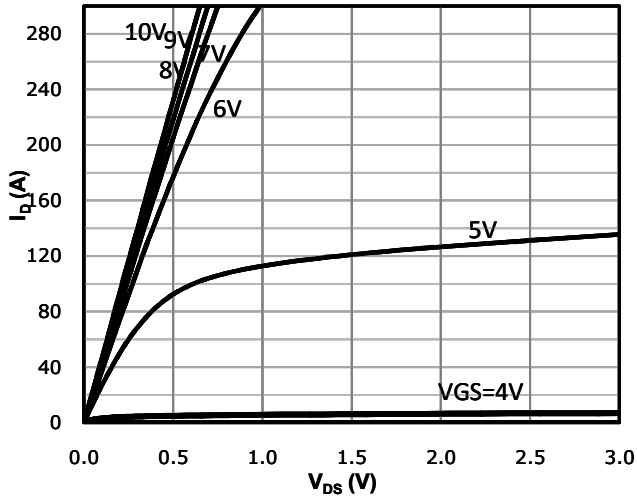


Fig 2: Transfer Characteristics

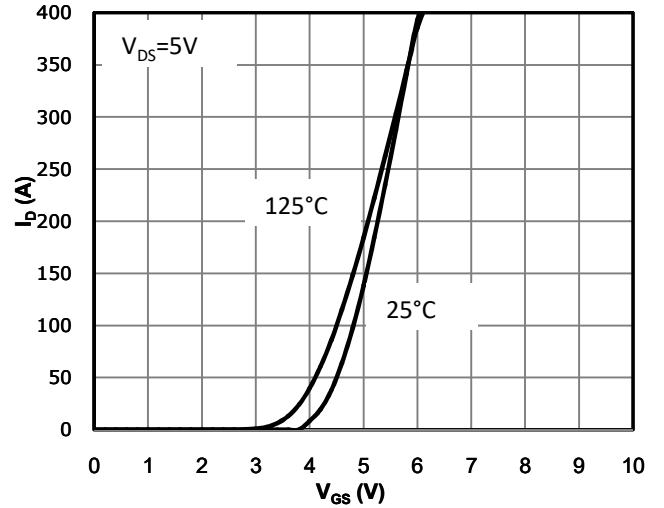


Fig 3: Rds(on) vs Drain Current and Gate Voltage

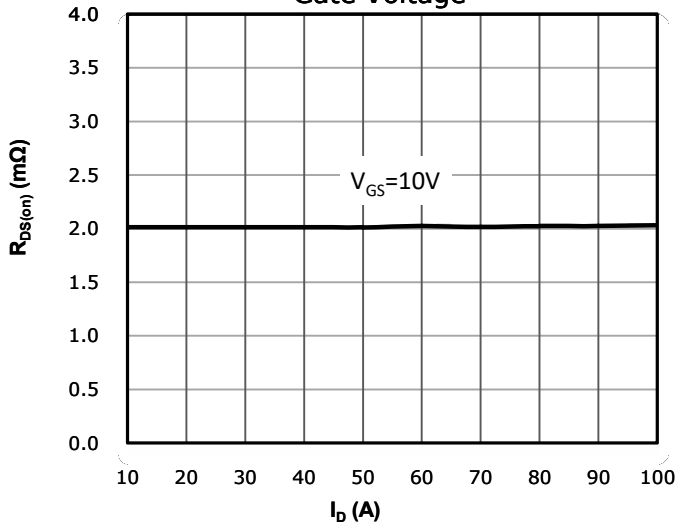


Fig 4: Rds(on) vs Gate Voltage

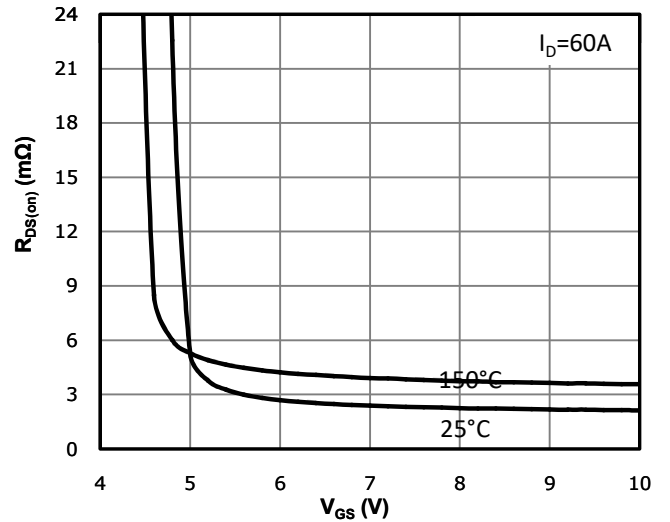


Fig 5: Rds(on) vs. Temperature

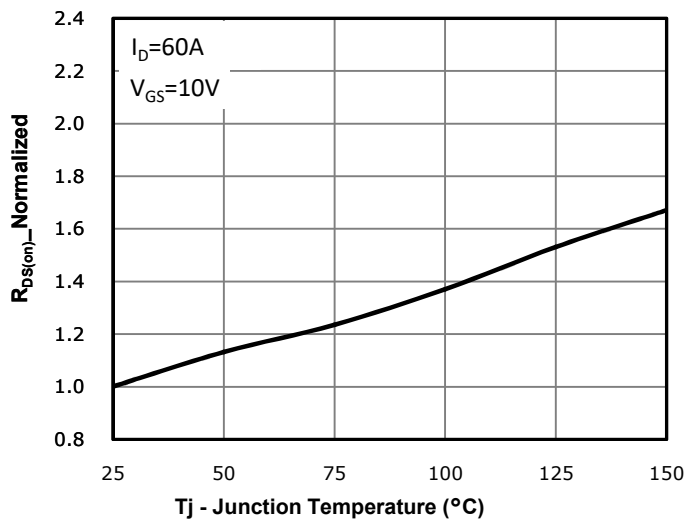


Fig 6: Capacitance Characteristics

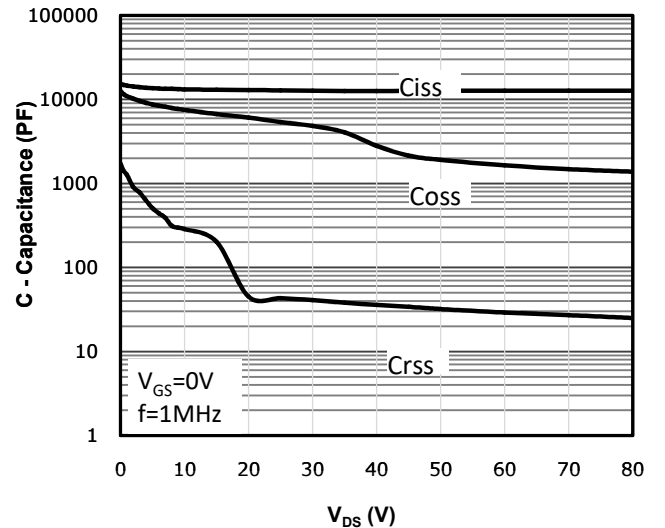


Fig 7: Gate Charge Characteristics

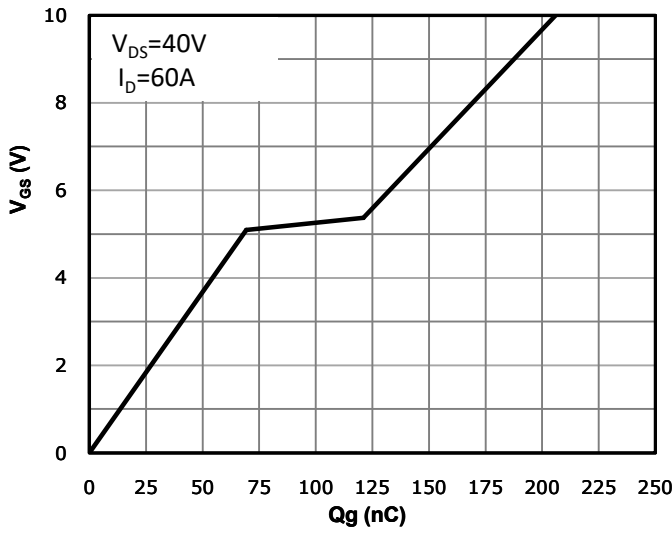


Fig 8: Body-diode Forward Characteristics

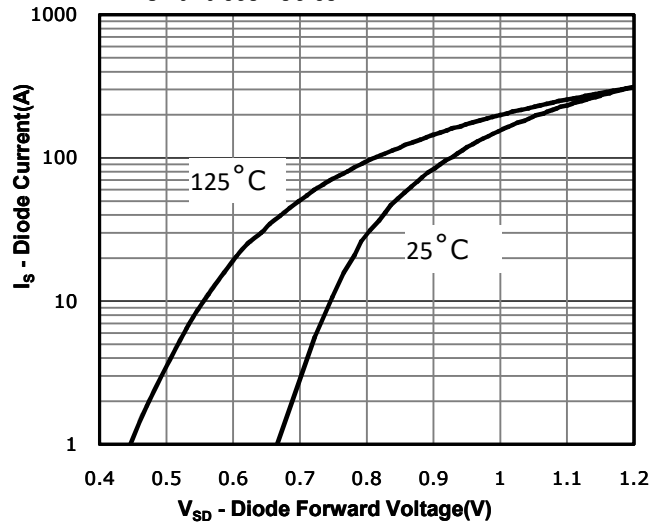


Fig 9: Power Dissipation

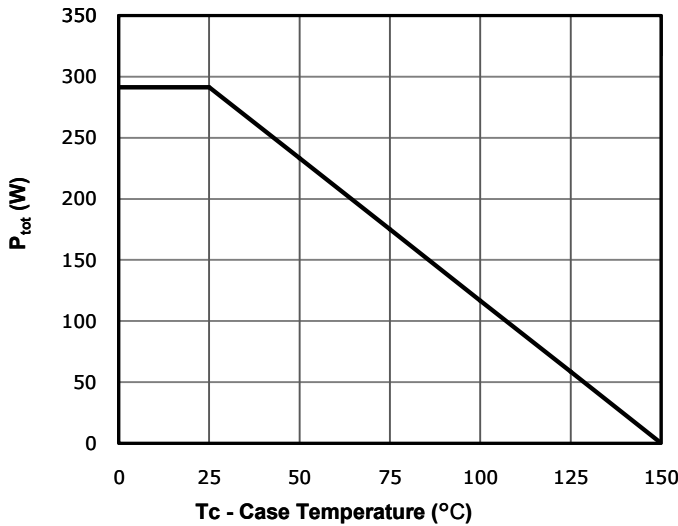


Fig 10: Drain Current Derating

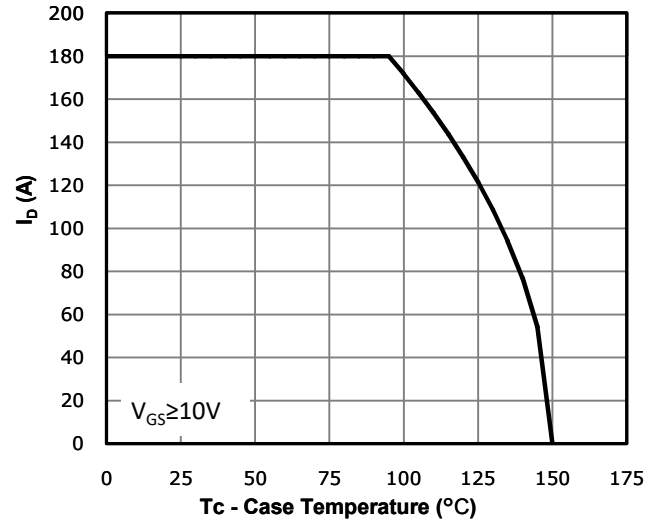


Fig 11: Safe Operating Area

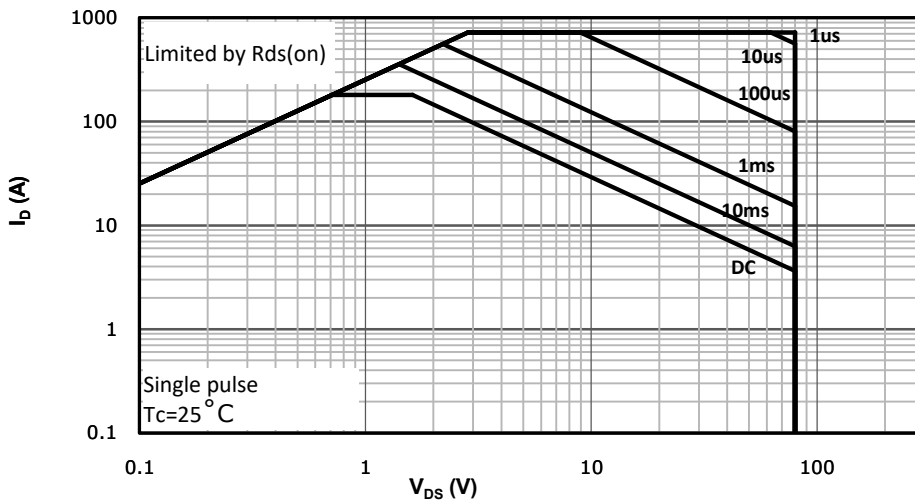
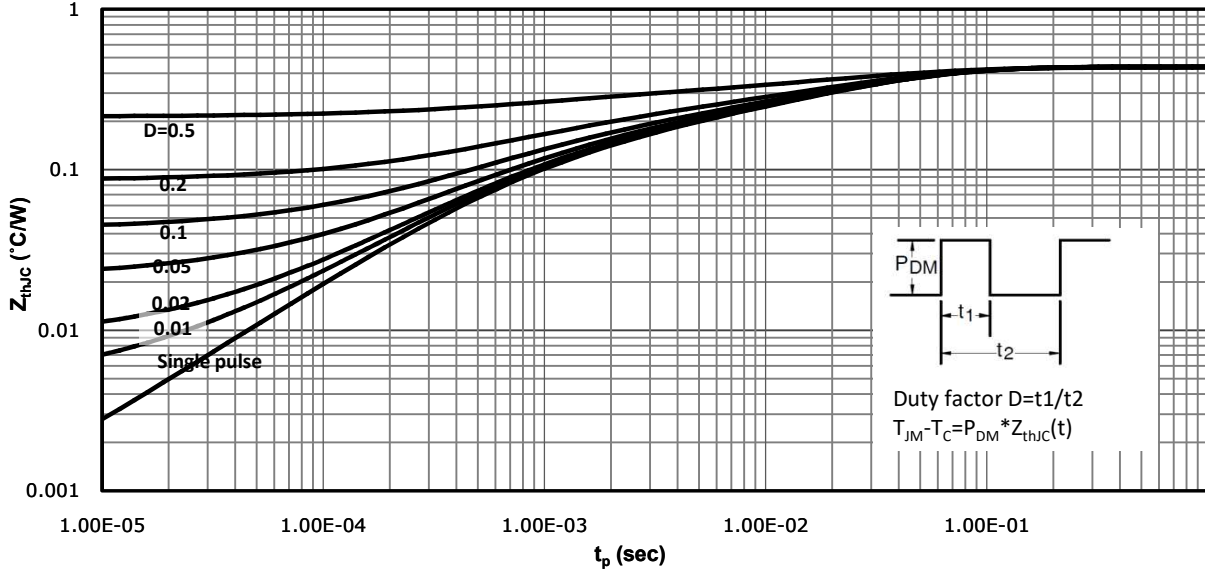
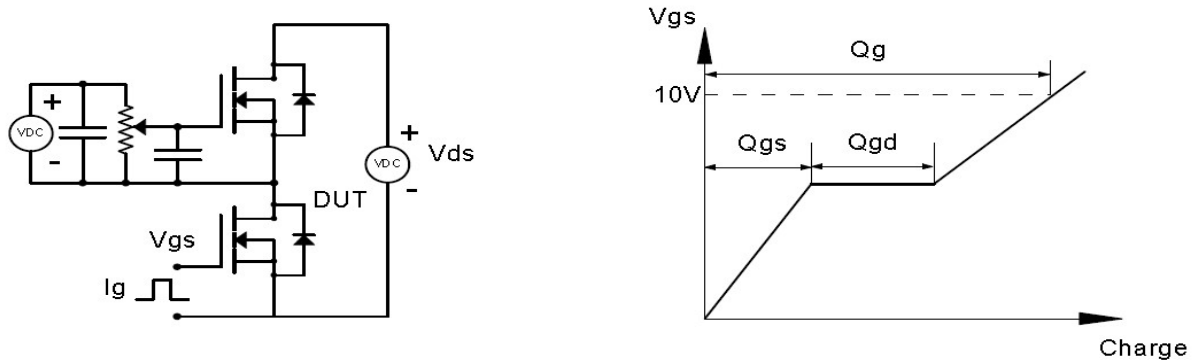


Fig 12: Max. Transient Thermal Impedance

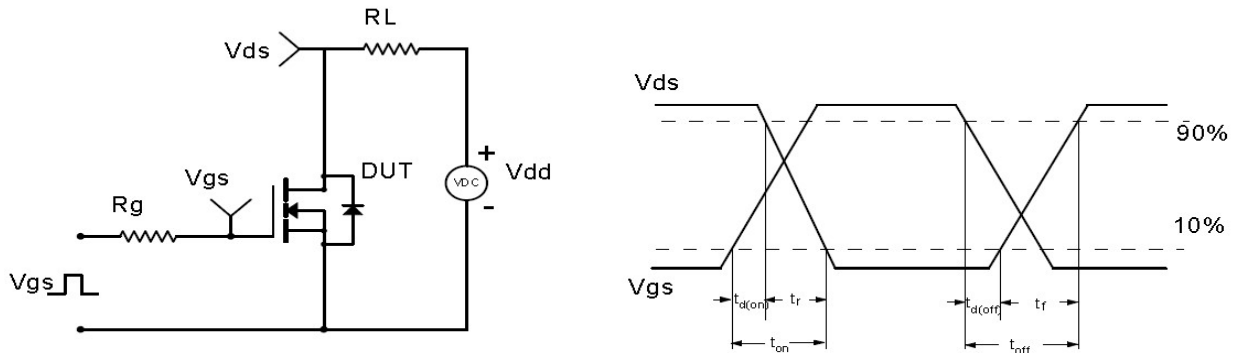


### Test Circuit & Waveform

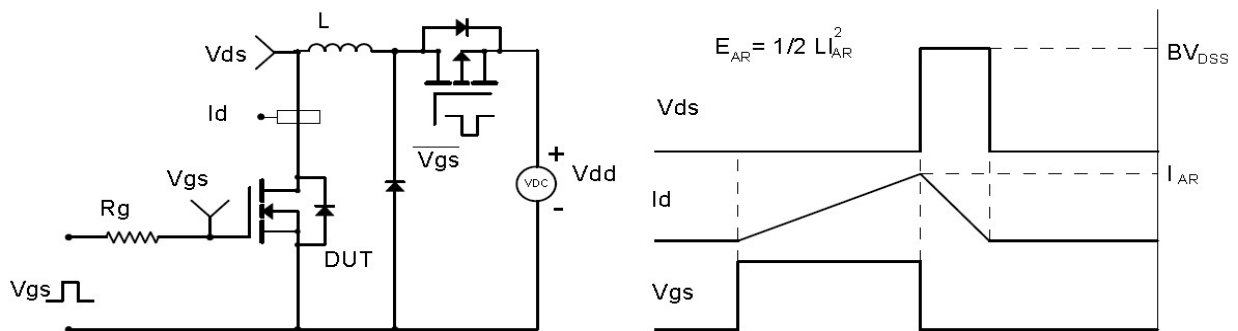
Gate Charge Test Circuit & Waveform



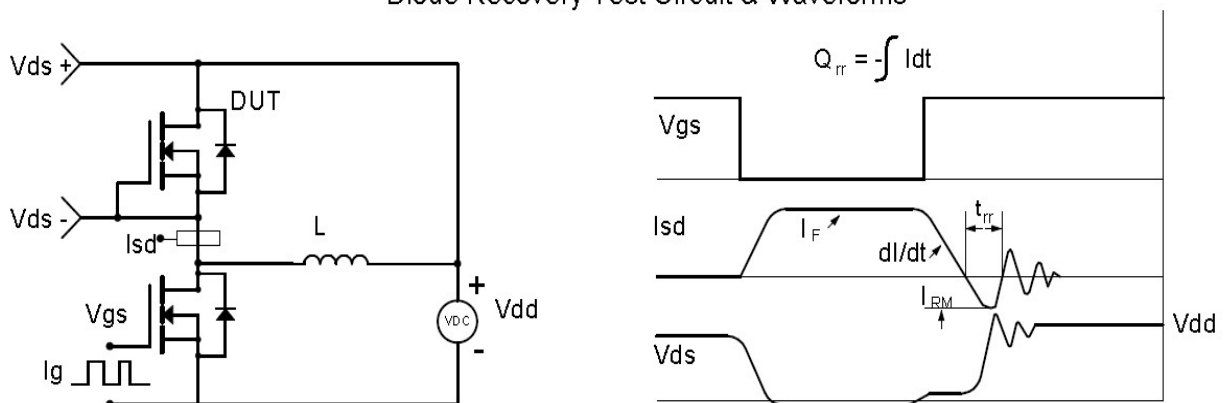
Resistive Switching Test Circuit & Waveforms

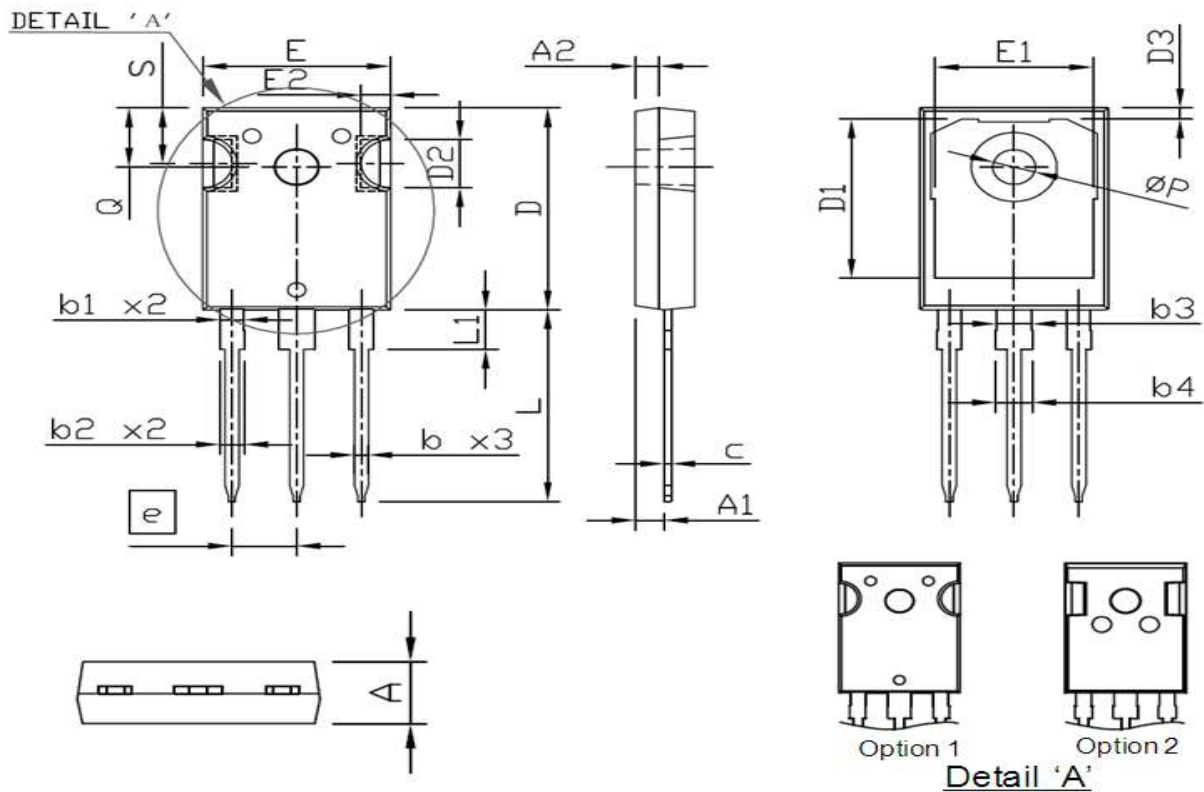


Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



**Package Outline: TO-247**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.85	5.15	0.191	0.203
A1	2.27	2.54	0.089	0.100
A2	1.90	2.10	0.075	0.083
b	1.07	1.33	0.042	0.052
b1	1.90	2.16	0.075	0.085
b2	2.00	2.21	0.079	0.087
b3	2.87	3.13	0.113	0.123
b4	2.87	3.20	0.113	0.126
c	0.55	0.68	0.022	0.027
D	20.80	21.10	0.819	0.831
D1	16.25	17.65	0.640	0.695
D2	3.68	5.10	0.145	0.201
D3	0.95	1.35	0.037	0.053
e	5.44 BSC.		0.214 BSC.	
E	15.70	16.13	0.618	0.635
E1	13.03	14.15	0.513	0.557
E2	2.20	2.60	0.087	0.102
L	19.72	20.32	0.776	0.800
L1	4.00	4.47	0.157	0.176
Q	6.04	6.30	0.238	0.248
P	3.50	3.70	0.138	0.146
S	5.49	6.00	0.216	0.236



## Revision History

Revision	Date	Major changes
1.0	2020-9-25	Release of Formal version.

## Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM(CQ) reserves the right to improve product design, function and reliability without notice.

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