CROCUS TECHNOLOGY
Intelligence in Sensing™

CT450

XtremeSense® TMR 1 MHz Bandwidth Contactless Current Sensor with <1% Total Error

Features

- Integrated Contactless Current/Field Sensing Ranges:
  - 0 mT to +6 mT
  - -6 mT to +6 mT
  - 0 mT to +12 mT
  - -12 mT to +12 mT
  - 0 mT to +24 mT
  - -24 mT to +24 mT
- Linear Analog Output Voltage
- Total Error Output ≤ ±1.0%, -40°C to +125°C
- 1 MHz Bandwidth
- Response Time < 300 ns
- Reference Voltage Output for Unipolar/Bipolar Field Measurements
- VOUT – VREF Error < 1.0% FS (Max.)
- Supply Voltage: 4.75 V to 5.50 V
- Low Noise Performance
- Filter Pin to Reduce Noise on Output
- AEC-Q100 Grade 1
- Package Options:
  - 8-lead SOIC
  - 8-lead TSSOP Package

Applications

- Solar/Power Inverters
- Battery Management Systems
- Smart Fuse Over-Current Detection
- Industrial Equipment
- Power Utility Meters
- Power Conditioner
- DC/DC Converters

Product Description

The CT450 is a high bandwidth and low noise integrated contactless current sensor that uses Crocus Technology's patented XtremeSense TMR technology to enable high accuracy current measurements for many consumer, enterprise, and industrial applications. It supports six (6) field ranges where the CT450 senses and translates the magnetic field into a linear analog output voltage. It achieves a total error output of less than ±1.0% over voltage and temperature.

It has less than 300 ns output response time while the current consumption is about 6.0 mA. The CT450 is equipped with a filter function to reduce the noise on the output pin.

The CT450 is housed in an 8-lead SOIC package and a very low profile, industry standard 8-lead TSSOP package that are both "green" and RoHS compliant.

Part Ordering Information

Part Number	Auto Grade	Field Range	Operating Temperature Range	Package	Packing Method
CT450-H06DRSN08	-	0 mT to +6 mT	-40°C to +125°C	8-lead SOIC 4.89 x 6.00 x 1.62 mm	Tape & Reel
CT450-A06DRSN08	Grade 1				
CT450-H06DRTS08	-			8-lead TSSOP 3.00 x 6.40 x 1.10 mm	
CT450-A06DRTS08	Grade 1				
CT450-H06MRSN08	-	-6 mT to +6 mT		8-lead SOIC 4.89 x 6.00 x 1.62 mm	
CT450-A06MRSN08	Grade 1				
CT450-H06MRTS08	-			8-lead TSSOP 3.00 x 6.40 x 1.10 mm	
CT450-A06MRTS08	Grade 1				
CT450-H12DRSN08	-	0 mT to +12 mT		8-lead SOIC 4.89 x 6.00 x 1.62 mm	
CT450-A12DRSN08	Grade 1				
CT450-H12DRTS08	-			8-lead TSSOP 3.00 x 6.40 x 1.10 mm	
CT450-A12DRTS08	Grade 1				
CT450-H12MRSN08	-	-12 mT to +12 mT		8-lead SOIC 4.89 x 6.00 x 1.62 mm	
CT450-A12MRSN08	Grade 1				
CT450-H12MRTS08	-			8-lead TSSOP 3.00 x 6.40 x 1.10 mm	
CT450-A12MRTS08	Grade 1				
CT450-H24DRSN08	-	0 mT to +24 mT		8-lead SOIC 4.89 x 6.00 x 1.62 mm	
CT450-A24DRSN08	Grade 1				
CT450-H24DRTS08	-			8-lead TSSOP 3.00 x 6.40 x 1.10 mm	
CT450-A24DRTS08	Grade 1				
CT450-H24MRSN08	-	-24 mT to +24 mT		8-lead SOIC 4.89 x 6.00 x 1.62 mm	
CT450-A24MRSN08	Grade 1				
CT450-H24MRTS08	-			8-lead TSSOP 3.00 x 6.40 x 1.10 mm	
CT450-A24MRTS08	Grade 1				

Evaluation Board Ordering Information

Part Number	Magnetic Field Range	Operating Temperature Range	Package
CTD450-06U	0 mT to +6 mT	-40°C to +125°C	8-lead SOIC 4.89 x 6.00 x 1.62 mm
CTD450-06B	-6 mT to +6 mT		
CTD450-12U	0 mT to +12 mT		
CTD450-12B	-12 mT to +12 mT		
CTD450-24U	0 mT to +24 mT		
CTD450-24B	-24 mT to +24 mT		

Block Diagram

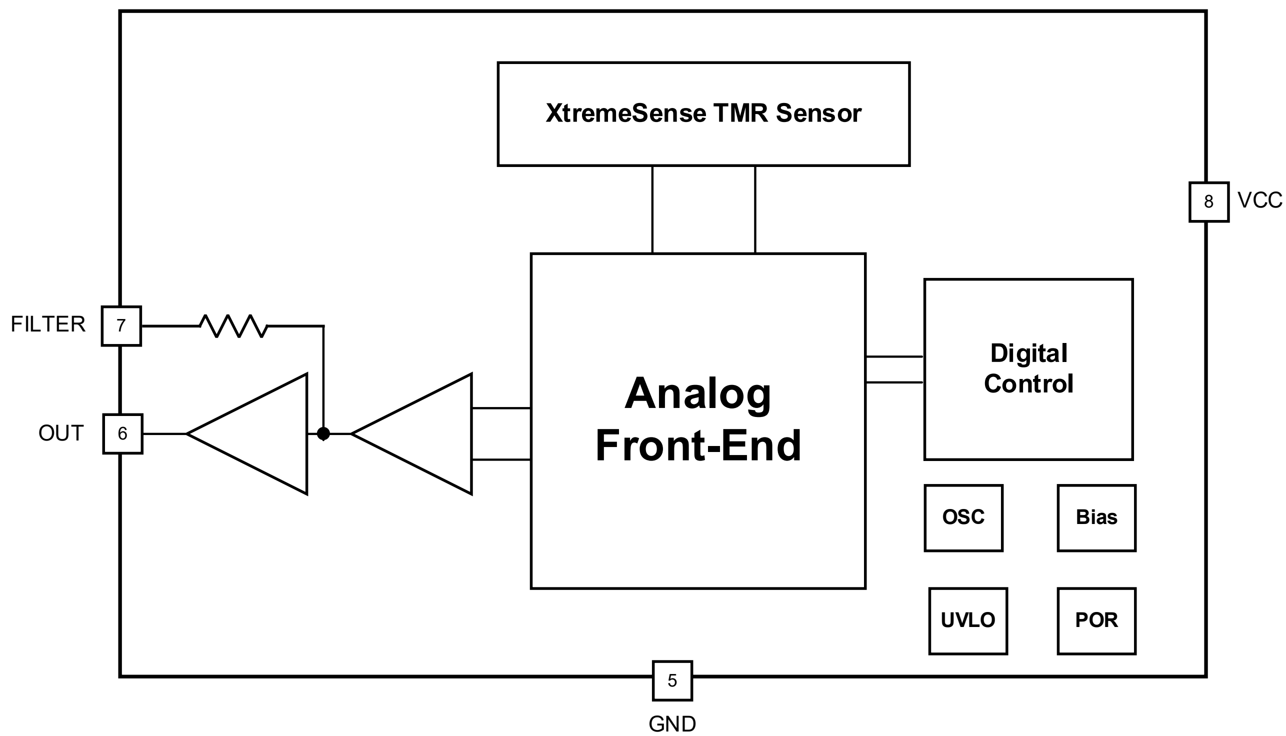


Figure 1. CT450 Functional Block Diagram for SOIC-8

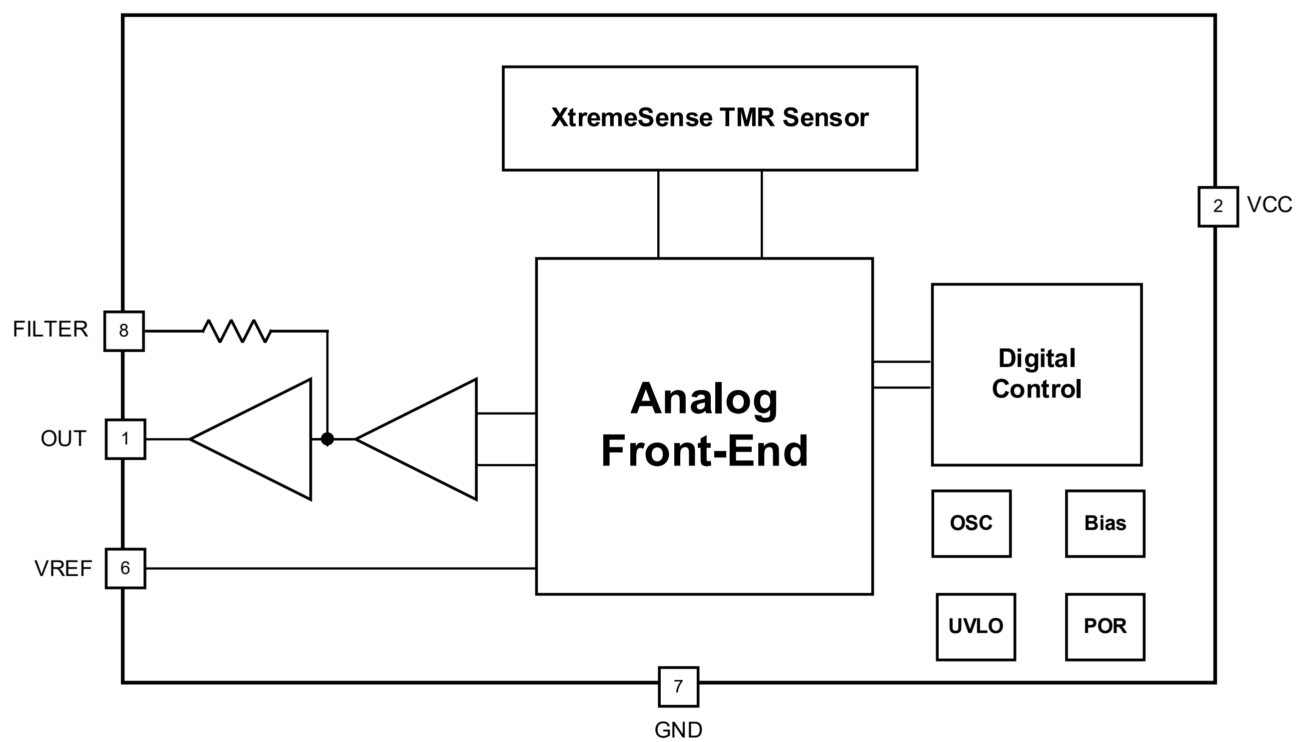


Figure 2. CT450 Functional Block Diagram for TSSOP-8

CT450 Application Diagram

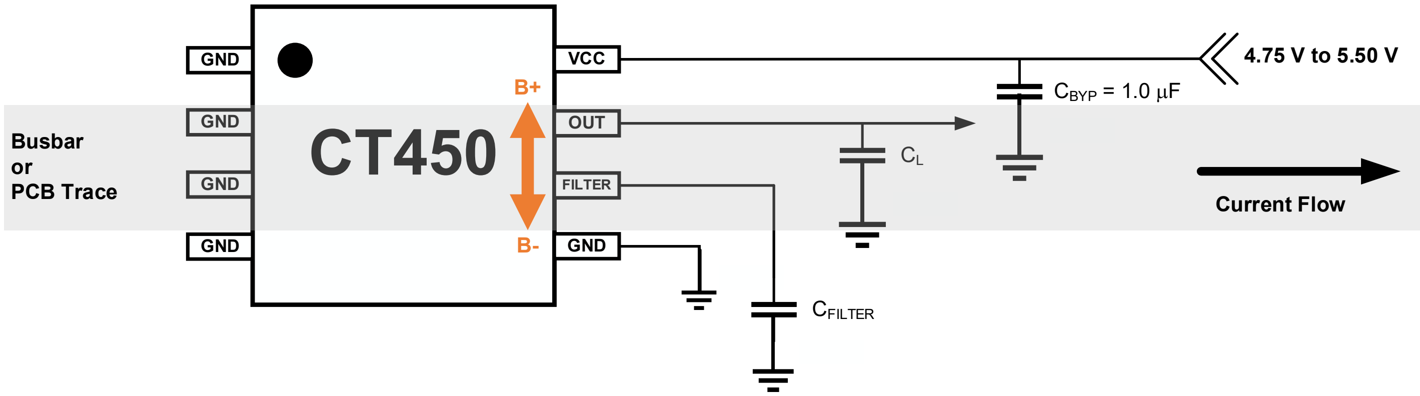


Figure 3. CT450 Application Diagram for SOIC-8

Table 1. Recommended External Components

Component	Description	Vendor & Part Number	Parameter	Min.	Typ.	Max.	Unit
C_{BYP}	1.0 µF, X5R or Better	Murata GRM155C81A105KA12	C1		1.0		µF
C_{FILTER}	Various, X5R or Better	Murata	C2		Table 3		pF

CT450 Application Diagram

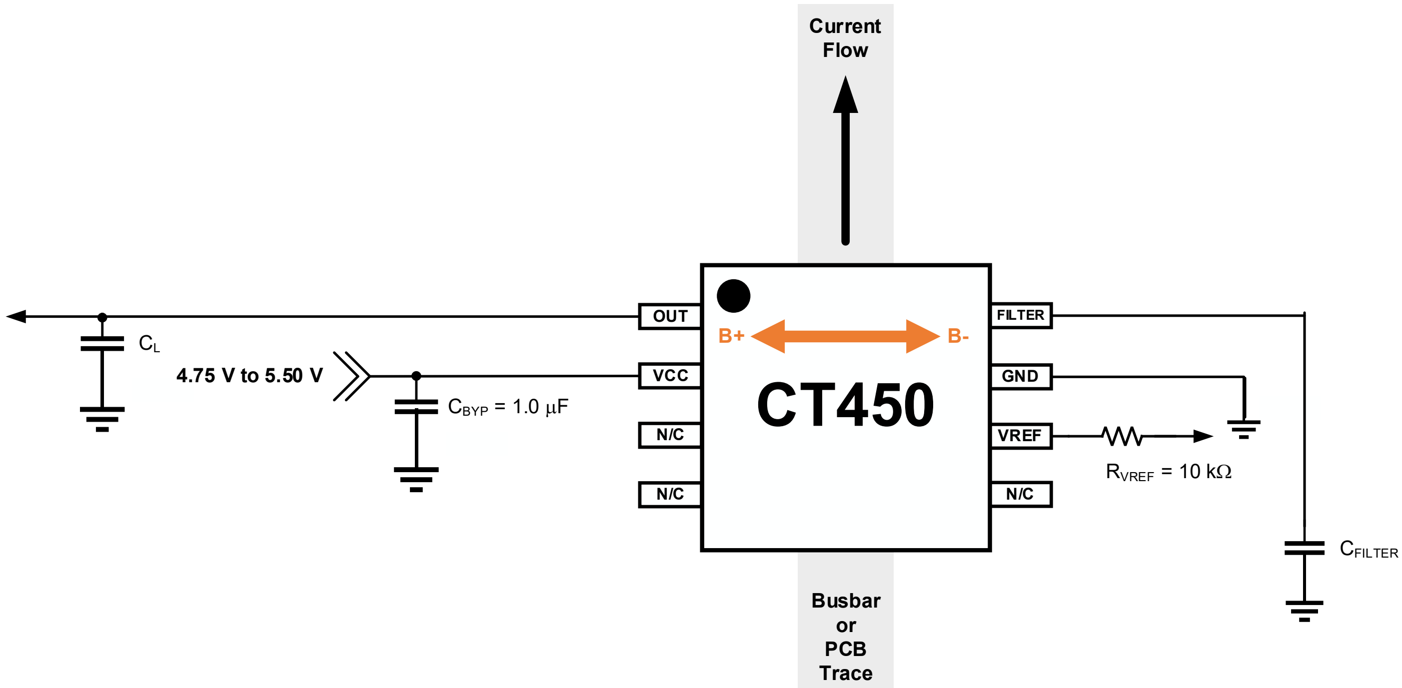


Figure 4. CT450 Application Diagram for TSSOP-8

Table 2. Recommended External Components

Component	Description	Vendor & Part Number	Parameter	Min.	Typ.	Max.	Unit
C_{BYP}	1.0 µF, X5R or Better	Murata GRM155C81A105KA12	C1		1.0		µF
C_{FILTER}	Various, X5R or Better	Murata	C2		Table 3		pF
R_{VREF}	10 kΩ Resistor	Various	R2		10		kΩ

CT450 SOIC-8 Pin Configuration

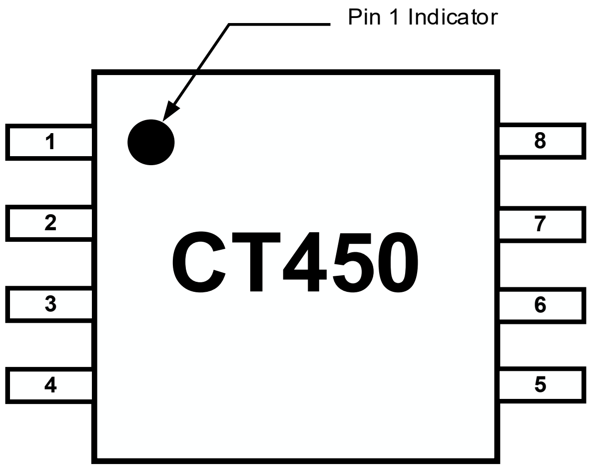


Figure 5. CT450 Pin-out Diagram for 8-lead SOIC Package (Top-Down View)

Pin Definition

Pin #	Pin Name	Pin Description
1	GND	Ground.
2		
3		
4		
5		
6	FILTER	Filter pin to improve noise performance by connecting an external capacitor to set the cut-off frequency. No connect if the FILTER pin is not used.
7	OUT	Analog output voltage that represents the measured current/field.
8	VCC	Supply voltage.

CT450 TSSOP-8 Pin Configuration

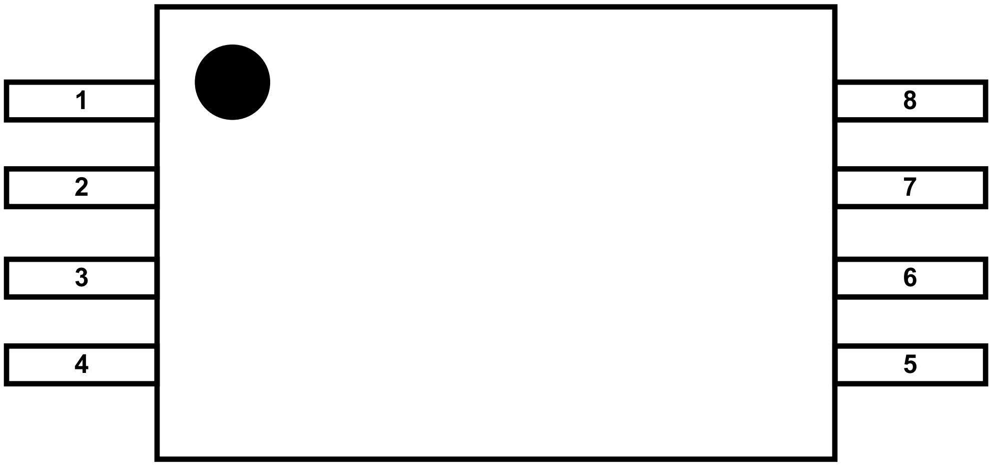


Figure 6. CT450 Pin-out Diagram for 8-lead TSSOP Package (Top-Down View)

Pin Definition

Pin #	Pin Name	Pin Description
1	OUT	Analog output voltage that represents the measured current/field.
2	VCC	Supply voltage.
3	N/C	No connect (Do Not Use).
4		
5		
6	VREF	Reference voltage output. If not used, then do not connect.
7	GND	Ground.
8	FILTER	Filter pin to improve noise performance by connecting an external capacitor to set the cut-off frequency. No connect if the FILTER pin is not used.

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the CT450 and may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter		Min.	Max.	Unit
V_{CC}	Supply Voltage		-0.3	6.0	V
$V_{I/O}$	Analog Input/Output Pins Maximum Voltage		-0.3	V_{CC} + 0.3*	V
ESD	Electrostatic Discharge Protection Level	Human Body Model (HBM) per JESD22-A114	2.0		kV
		Charged Device Model (CDM) per JESD22-C101	0.5		
T_J	Junction Temperature		-40	+150	°C
T_{STG}	Storage Temperature		-65	+155	°C
T_L	Lead Soldering Temperature, 10 Seconds			+260	°C

*The lower of V_{CC} + 0.3 V or 6.0 V.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual operation of the CT450. Recommended operating conditions are specified to ensure optimal performance to the specifications. Crocus Technology does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter		Min.	Typ.	Max.	Unit
V_{CC}	Supply Voltage Range		4.75	5.00	5.50	V
V_{OUT}	OUT Voltage Range		0		V_{CC}	V
I_{OUT}	OUT Current				±1.0	mA
T_A	Operating Ambient Temperature	Industrial	-40	+25	+85	°C
		Extended Industrial	-40	+25	+125	

Electrical Specifications

General Parameters

Unless otherwise specified: V_{CC} = 4.75 V to 5.50 V, T_A = -40°C to +125°C, C_{BYP} = 1.0 µF. Typical values are V_{CC} = 5.00 V and T_A = +25°C.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Power Supplies						
I_{CC}	Supply Current	f_{BW} = 1 MHz No load, B_{OP} = 0 mT		6.0	9.0	mA
I_{OUT}	OUT Maximum Drive Capability	OUT covers 10% to 90% of V_{CC} span.	-1.0		+1.0	mA
C_{L_OUT}	OUT Capacitive Load				100	pF
R_{L_OUT}	OUT Resistive Load			100		kΩ
R_{FILTER}	Internal Filter Resistance (1)			15		kΩ
I_{VREF}	VREF Maximum Drive Capability	TSSOP-8 only	-50		+50	µA
C_{L_VREF}	VREF Capacitive Load	TSSOP-8 only			10	pF
R_{L_VREF}	VREF Resistive Load	TSSOP-8 only		100		kΩ
Analog Output (OUT)						
V_{OUT}	OUT Voltage Linear Range	V_{SIG_AC} = ±2.00 V V_{SIG_DC} = +4.00 V	0.50		4.50	V
V_{OUT_SAT}	Output High Saturation Voltage	V_{OUT}, T_A = +25°C	V_{CC} – 0.30	V_{CC} – 0.25		V
Reference Voltage (VREF) for TSSOP-8 Only						
V_{REF}	Reference Voltage	Unipolar Field		0.50		V
		Bipolar Field		2.50		
Fault Output ($\overline{FLT}$) for TSSOP-8 Only						
$V_{FLT\#\_OL}$	$\overline{FLT}$ Voltage LOW	$I_{FLT\#\_OUT} \leq$ 20 mA	0		0.5	V
$I_{LEAK\_FLT\#}$	High Impedance Output Leakage Current	$V_{FLT\#\_OH}$ = V_{CC}		5		µA
RPU	$\overline{FLT}$ Pull-up Resistor			100		kΩ
Timings						
t_{ON}	Power-On Time	$V_{CC} \geq$ 4.0 V		100	200	µs
t_{RISE}	Rise Time (1)	B_{OP} = $B_{OP(MAX)}$, T_A = +25°C, C_L = 100 pF		200		ns
$t_{RESPONSE}$	Response Time (1)			300		ns
t_{DELAY}	Propagation Delay (1)			250		ns
Protection						
V_{UVLO}	Under-Voltage Lockout	Rising V_{CC}		2.50		V
		Falling V_{CC}		2.45		V
V_{UV_HYS}	UVLO Hysteresis			50		mV

(1) Guaranteed by design and characterization; not tested in production.

Electrical Characteristics

V_{CC} = 5.00 V and T_A = +25°C and C_{BYP} = 1.0 µF (unless otherwise specified)

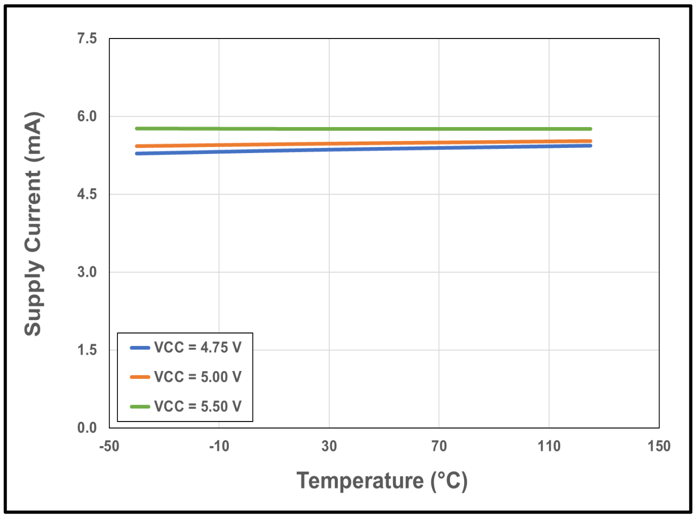


Figure 7. CT450 Supply Current vs. Temperature vs. Supply Voltage

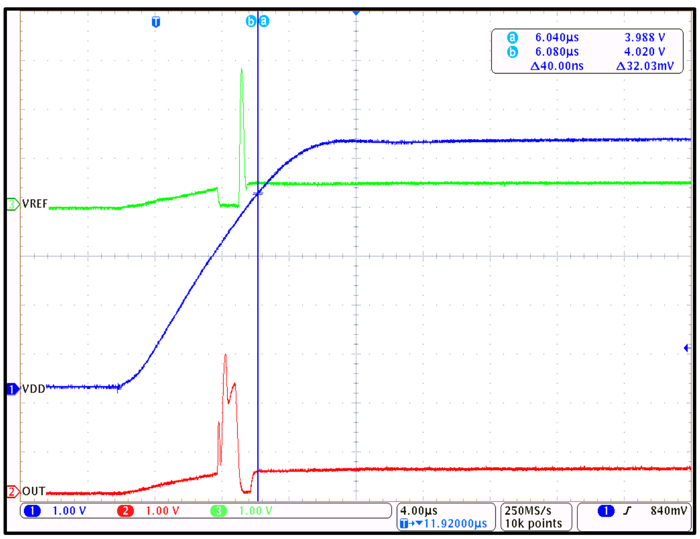


Figure 8. CT450 Startup Waveforms for V_{OQ} = 0.50 V (Unipolar Field)

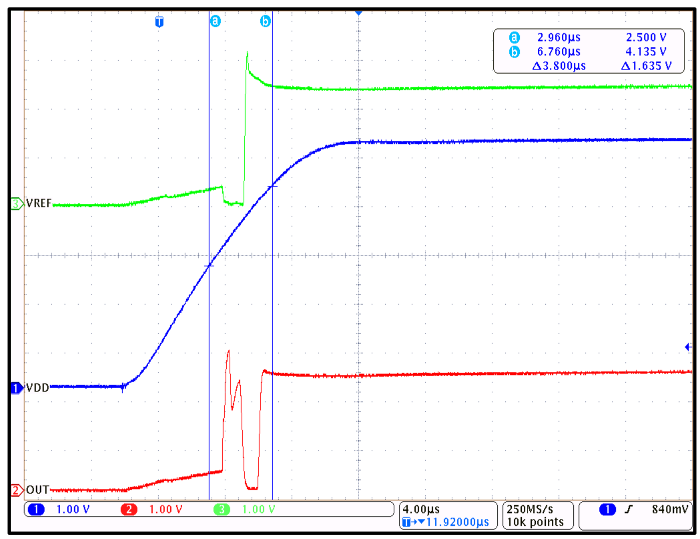


Figure 9. CT450 Startup Waveforms for V_{OQ} = 2.50 V (Bipolar Field)

Electrical Characteristics (continued)

V_{CC} = 5.00 V and T_A = +25°C and C_{BYP} = 1.0 µF (unless otherwise specified)

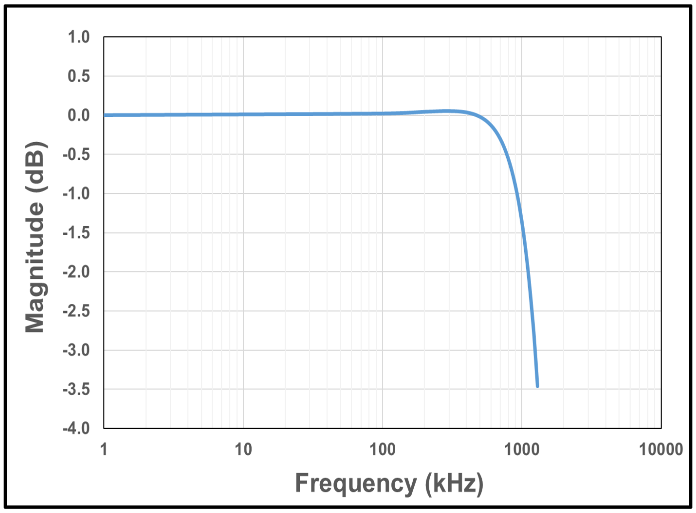


Figure 10. CT450 Bandwidth

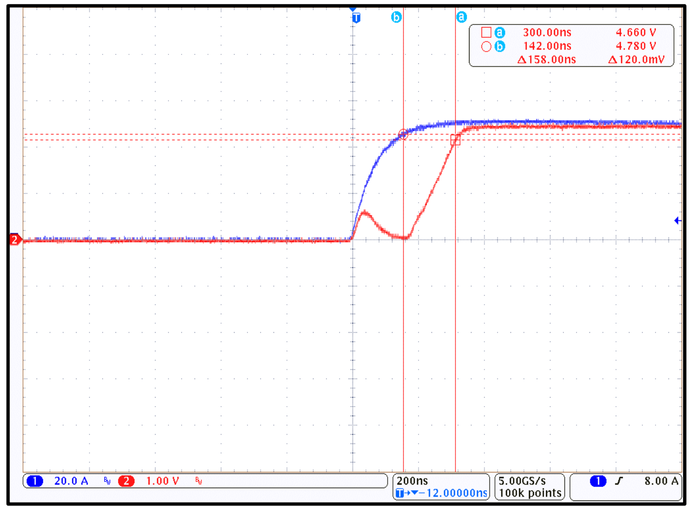


Figure 11. CT450 Response Time; B_{OP} = 12 mT and C_L = 220 pF (Blue = I_{CCC}, Red = V_{OUT})

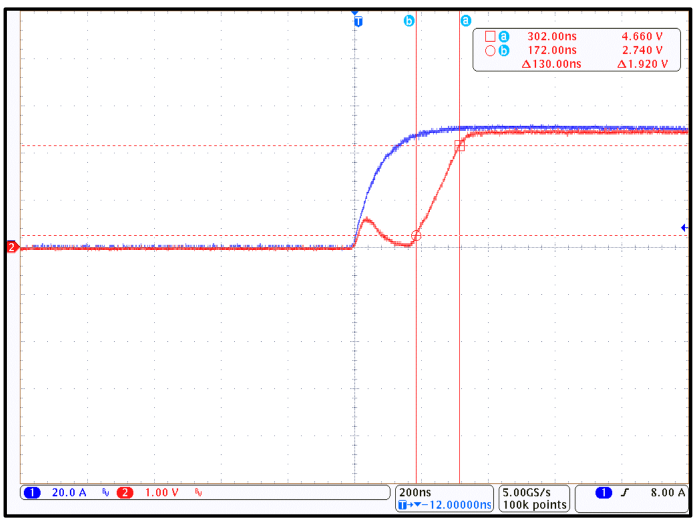


Figure 12. CT450 Rise Time; B_{OP} = 12 mT and C_L = 220 pF (Blue = I_{CCC}, Red = V_{OUT})

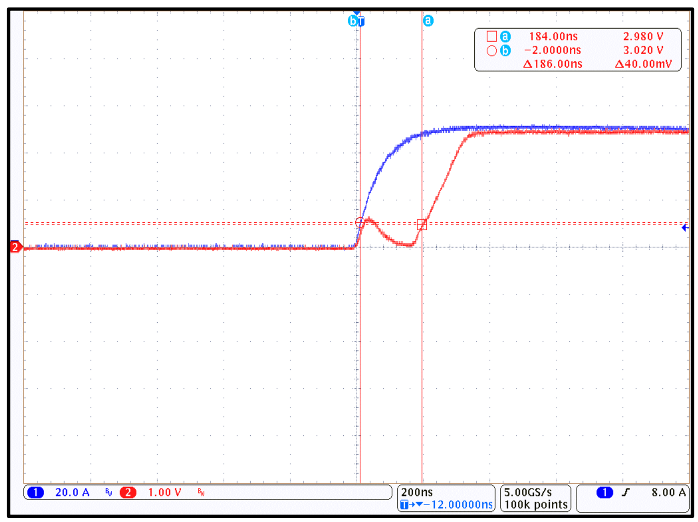


Figure 13. CT450 Propagation Delay; B_{OP} = 12 mT and C_L = 220 pF (Blue = I_{CCC}, Red = V_{OUT})

CT450-x08DR: 0 mT to +6 mT

Unless otherwise specified: V_{CC} = 4.75 V to 5.50 V, T_A = -40°C to +125°C, C_{BYP} = 1.0 µF. Typical values are V_{CC} = 5.00 V and T_A = +25°C.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B_{RNG}	Magnetic Field Range		0		+6	mT
V_{OQ}	Voltage Output Quiescent	T_A = +25°C, B_{OP} = 0 mT	0.495	0.500	0.505	V
S	Sensitivity	$B_{RNG(MIN)} < B_{OP} < B_{RNG(MAX)}$		666.7		mV/mT
f_{BW}	Bandwidth [1]	Small Signal = -3 dB		1.0		MHz
e_N	Noise [1]	T_A = +25°C, f_{BW} = 100 kHz		1.63		mV_{RMS}
				2.45		μT_{RMS}
OUT Accuracy Performance						
E_{OUT}	Total Output Error	$B_{OP} = B_{RNG(MAX)}$		±0.7	±1.0	% FS
E_{LIN}	Non-Linearity Error [1]	$B_{OP} = B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.2		% FS
E_{SENS}	Sensitivity Error [1]	$B_{OP} = B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.3		% FS
V_{OFFSET}	Offset Voltage [1]	B_{OP} = 0 mT, T_A = -40°C to +125°C		±10.0		mV
				±0.3		% FS
$V_{OUT} – V_{REF}$ Accuracy Performance (TSSOP-8 only)						
$E_{OUT\text{-}VREF}$	VOUT – VREF Error	$B_{OP} = B_{RNG(MAX)}$, V_{CC} = 5.0 V T_A = -40°C to +125°C			±1.0	% FS
V_{OUT} - V_{REF}	OUT – VREF Offset Voltage	V_{CC} = 5.0 V T_A = -40°C to +125°C		±5.0		mV
Lifetime Drift						
E_{TOT_DRIFT}	Total Output Error Lifetime Drift [1]	$B_{OP} = B_{OP(MAX)}$		±1.0		% FS

(1) Guaranteed by design and characterization; not tested in production.

Electrical Characteristics for CT450-x06DR

V_{CC} = 5.0 V and T_A = +25°C and C_{BYP} = 1.0 µF (unless otherwise specified)

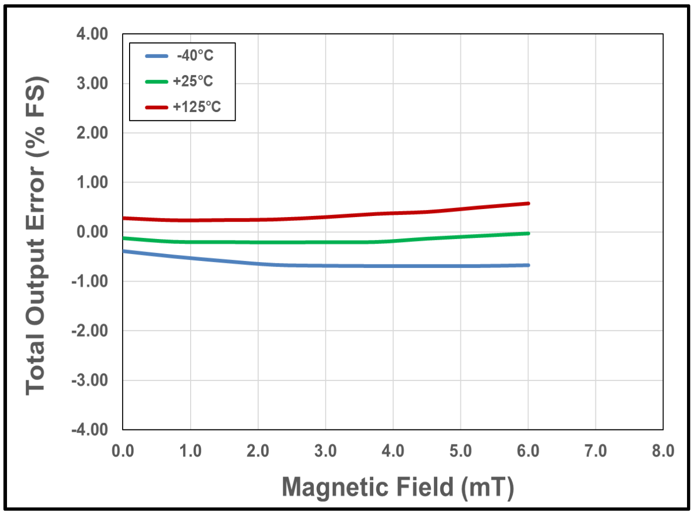


Figure 14. Total Output Error vs. Current vs. Temperature

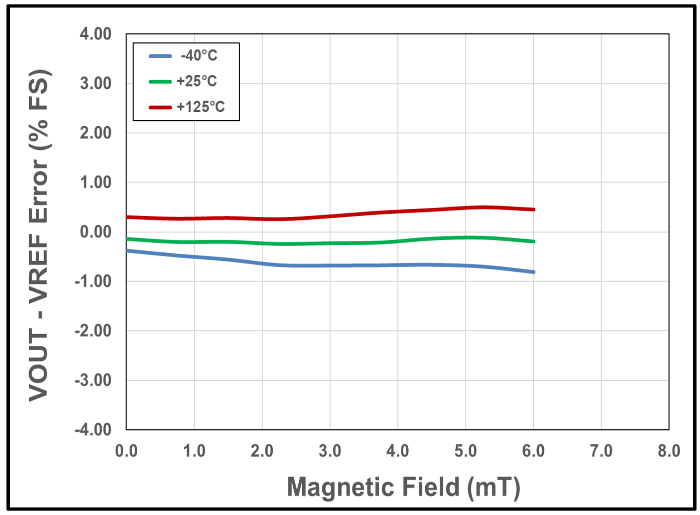


Figure 15. VOUT – VREF Error vs. Current vs. Temperature for TSSOP-8 Variant

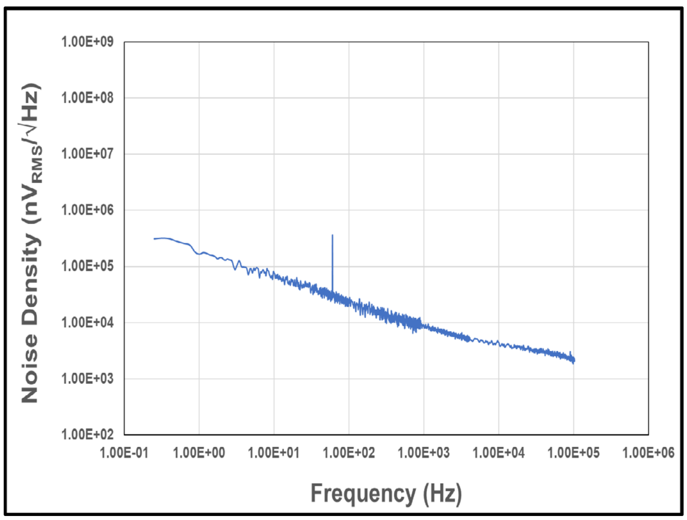


Figure 16. Noise Density vs. Frequency

CT450-x06MR: -6 mT to +6 mT

Unless otherwise specified: V_{CC} = 4.75 V to 5.50 V, T_A = -40°C to +125°C, C_{BYP} = 1.0 µF. Typical values are V_{CC} = 5.00 V and T_A = +25°C.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B_{RNG}	Magnetic Field Range		-6		+6	mT
V_{OQ}	Voltage Output Quiescent	T_A = +25°C, B_{OP} = 0 mT	2.490	2.500	2.510	V
S	Sensitivity	$B_{RNG(MIN)} < B_{OP} < B_{RNG(MAX)}$		333.3		mV/mT
f_{BW}	Bandwidth (1)	Small Signal = -3 dB		1.0		MHz
e_N	Noise (1)	T_A = +25°C, f_{BW} = 100 kHz		0.92		mV_{RMS}
				2.77		μT_{RMS}
OUT Accuracy Performance						
E_{OUT}	Total Output Error	$B_{OP} = B_{RNG(MAX)}$		±0.5	±1.0	% FS
E_{LIN}	Non-Linearity Error (1)	$B_{OP} = B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.1		% FS
E_{SENS}	Sensitivity Error (1)	$B_{OP} = B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.3		% FS
V_{OFFSET}	Offset Voltage (1)	B_{OP} = 0 mT, T_A = -40°C to +125°C		±8.0		mV
				±0.2		% FS
V_{OUT} – V_{REF} Accuracy Performance (TSSOP-8 only)						
$E_{OUT-VREF}$	VOUT – VREF Error	$B_{OP} = B_{RNG(MAX)}$, V_{CC} = 5.0 V T_A = -40°C to +125°C			±1.0	% FS
V_{OUT} - V_{REF}	OUT – VREF Offset Voltage	V_{CC} = 5.0 V T_A = -40°C to +125°C		±5.0		mV
Lifetime Drift						
E_{TOT_DRIFT}	Total Output Error Lifetime Drift (1)	$B_{OP} = B_{OP(MAX)}$		±1.0		% FS

(1) Guaranteed by design and characterization; not tested in production.

Electrical Characteristics for CT450-x06MR

V_{CC} = 5.0 V and T_A = +25°C and C_{BYP} = 1.0 µF (unless otherwise specified)

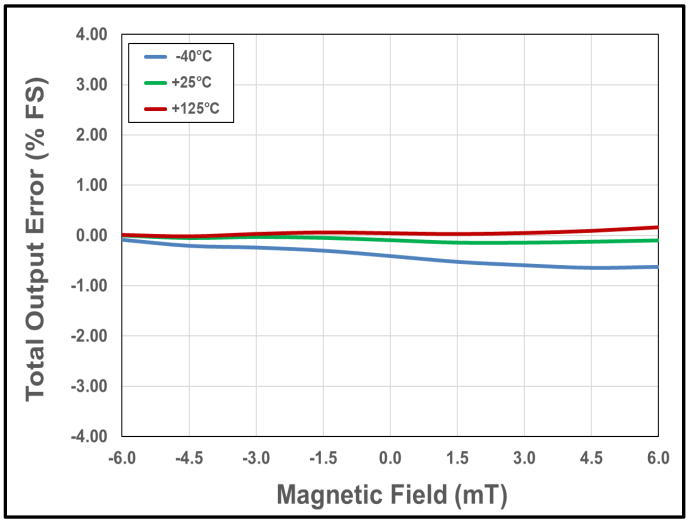


Figure 17. Total Output Error vs. Current vs. Temperature

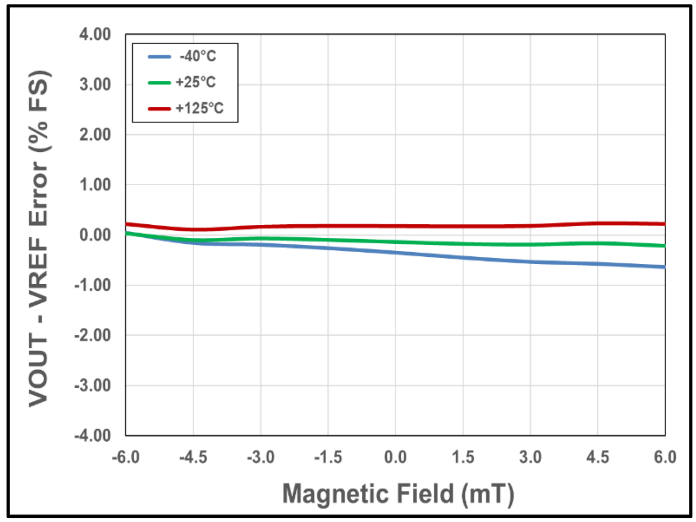


Figure 18. VOUT – VREF Error vs. Current vs. Temperature for TSSOP-8 Variant

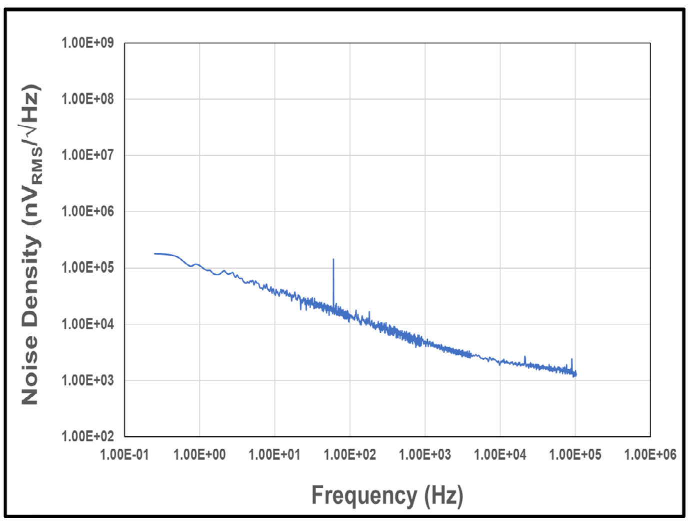


Figure 19. Noise Density vs. Frequency

CT450-x12DR: 0 mT to +12 mT

Unless otherwise specified: V_{CC} = 4.75 V to 5.50 V, T_A = -40°C to +125°C, C_{BYP} = 1.0 µF. Typical values are V_{CC} = 5.00 V and T_A = +25°C.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B_{RNG}	Magnetic Field Range		0		+12	mT
V_{OQ}	Voltage Output Quiescent	T_A = +25°C, B_{OP} = 0 mT	0.495	0.500	0.505	V
S	Sensitivity	$B_{RNG(MIN)} < B_{OP} < B_{RNG(MAX)}$		333.3		mV/mT
f_{BW}	Bandwidth (1)	Small Signal = -3 dB		1.0		MHz
e_N	Noise (1)	T_A = +25°C, f_{BW} = 100 kHz		1.30		mV_{RMS}
				3.90		μT_{RMS}
OUT Accuracy Performance						
E_{OUT}	Total Output Error	B_{OP} = $B_{RNG(MAX)}$		±0.7	±1.0	% FS
E_{LIN}	Non-Linearity Error (1)	B_{OP} = $B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.2		% FS
E_{SENS}	Sensitivity Error (1)	B_{OP} = $B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.3		% FS
V_{OFFSET}	Offset Voltage (1)	B_{OP} = 0 mT, T_A = -40°C to +125°C		±9.0		mV
				±0.2		% FS
V_{OUT} – V_{REF} Accuracy Performance (TSSOP-8 only)						
$E_{OUT-VREF}$	VOUT – VREF Error	B_{OP} = $B_{RNG(MAX)}$, V_{CC} = 5.0 V T_A = -40°C to +125°C			±1.0	% FS
V_{OUT} - V_{REF}	OUT – VREF Offset Voltage	V_{CC} = 5.0 V T_A = -40°C to +125°C		±5.0		mV
Lifetime Drift						
E_{TOT_DRIFT}	Total Output Error Lifetime Drift (1)	B_{OP} = $B_{OP(MAX)}$		±1.0		% FS

(1) Guaranteed by design and characterization; not tested in production.

Electrical Characteristics for CT450-x12DR

V_{CC} = 5.0 V and T_A = +25°C and C_{BYP} = 1.0 μF (unless otherwise specified)

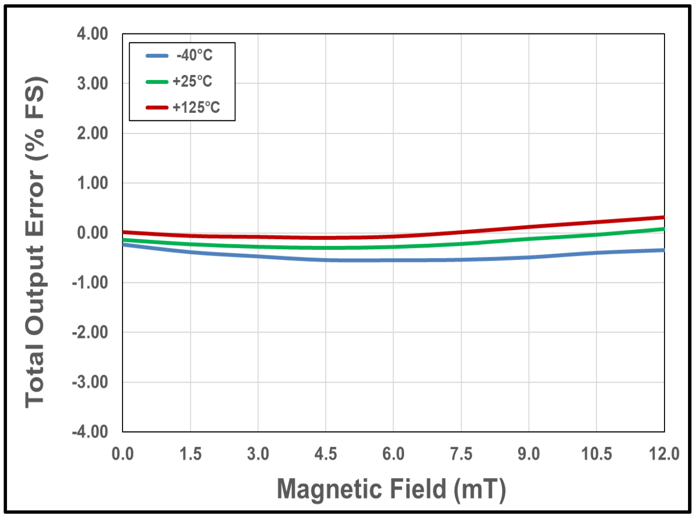


Figure 20. Total Output Error vs. Current vs. Temperature

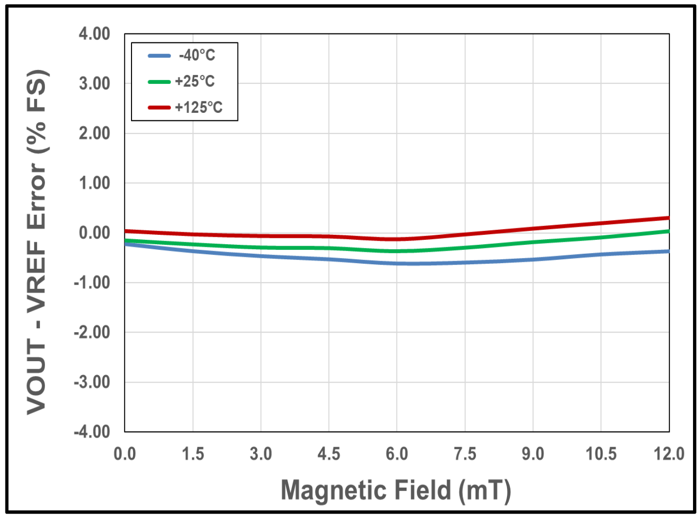


Figure 21. VOUT – VREF Error vs. Current vs. Temperature for TSSOP-8 Variant

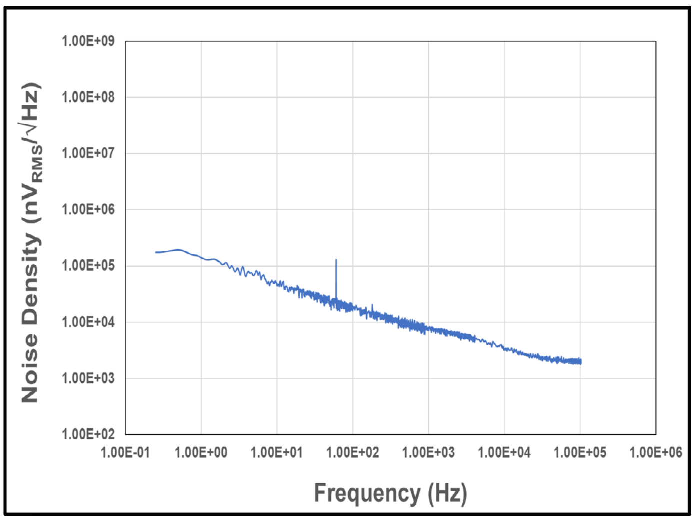


Figure 22. Noise Density vs. Frequency

CT450-x12MR: -12 mT to +12 mT

Unless otherwise specified: V_{CC} = 4.75 V to 5.50 V, T_A = -40°C to +125°C, C_{BYP} = 1.0 µF. Typical values are V_{CC} = 5.00 V and T_A = +25°C.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B_{RNG}	Magnetic Field Range		-12		+12	mT
V_{OQ}	Voltage Output Quiescent	T_A = +25°C, B_{OP} = 0 mT	2.490	2.500	2.510	V
S	Sensitivity	$B_{RNG(MIN)} < B_{OP} < B_{RNG(MAX)}$		166.7		mV/mT
f_{BW}	Bandwidth (1)	Small Signal = -3 dB		1.0		MHz
e_N	Noise (1)	T_A = +25°C, f_{BW} = 100 kHz		0.75		mV_{RMS}
				4.50		μT_{RMS}
OUT Accuracy Performance						
E_{OUT}	Total Output Error	$B_{OP} = B_{RNG(MAX)}$		±0.5	±1.0	% FS
E_{LIN}	Non-Linearity Error (1)	$B_{OP} = B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.1		% FS
E_{SENS}	Sensitivity Error (1)	$B_{OP} = B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.3		% FS
V_{OFFSET}	Offset Voltage (1)	B_{OP} = 0 mT, T_A = -40°C to +125°C		±7.0		mV
				±0.2		% FS
V_{OUT} – V_{REF} Accuracy Performance (TSSOP-8 only)						
$E_{OUT\text{-}VREF}$	VOUT – VREF Error	$B_{OP} = B_{RNG(MAX)}$, V_{CC} = 5.0 V T_A = -40°C to +125°C			±1.0	% FS
V_{OUT} - V_{REF}	OUT – VREF Offset Voltage	V_{CC} = 5.0 V T_A = -40°C to +125°C		±5.0		mV
Lifetime Drift						
E_{TOT_DRIFT}	Total Output Error Lifetime Drift (1)	$B_{OP} = B_{OP(MAX)}$		±1.0		% FS

(1) Guaranteed by design and characterization; not tested in production.

Electrical Characteristics for CT450-x12MR

V_{CC} = 5.0 V and T_A = +25°C and C_{BYP} = 1.0 μF (unless otherwise specified)

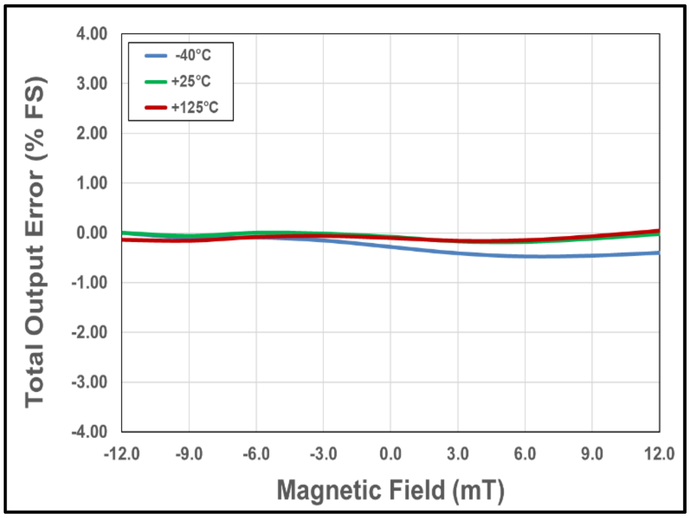


Figure 23. Total Output Error vs. Current vs. Temperature

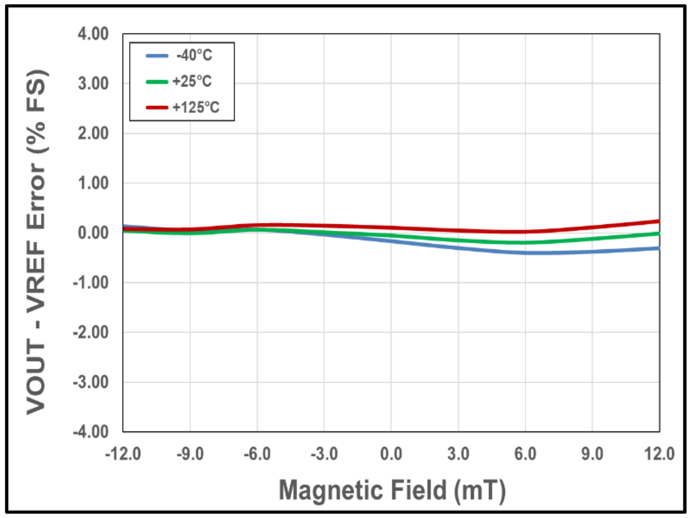


Figure 24. VOUT – VREF Error vs. Current vs. Temperature for TSSOP-8 Variant

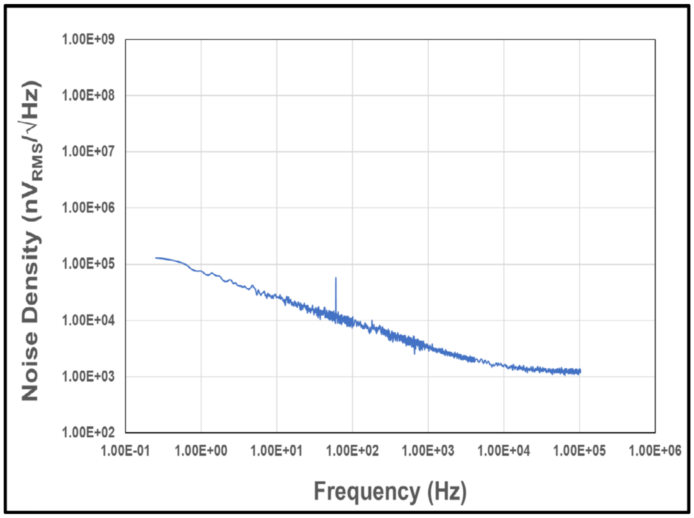


Figure 25. Noise Density vs. Frequency

CT450-x24DR: 0 mT to +24 mT

Unless otherwise specified: V_{CC} = 4.75 V to 5.50 V, T_A = -40°C to +125°C, C_{BYP} = 1.0 µF. Typical values are V_{CC} = 5.00 V and T_A = +25°C.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B_{RNG}	Magnetic Field Range		0		+24	mT
V_{OQ}	Voltage Output Quiescent	T_A = +25°C, B_{OP} = 0 mT	0.495	0.500	0.505	V
S	Sensitivity	$B_{RNG(MIN)} < B_{OP} < B_{RNG(MAX)}$		166.7		mV/mT
f_{BW}	Bandwidth (1)	Small Signal = -3 dB		1.0		MHz
e_N	Noise (1)	T_A = +25°C, f_{BW} = 100 kHz		0.46		mV_{RMS}
				2.77		μT_{RMS}
OUT Accuracy Performance						
E_{OUT}	Total Output Error	B_{OP} = $B_{RNG(MAX)}$		±0.7	±1.5	% FS
E_{LIN}	Non-Linearity Error (1)	B_{OP} = $B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.3		% FS
E_{SENS}	Sensitivity Error (1)	B_{OP} = $B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.3		% FS
V_{OFFSET}	Offset Voltage (1)	B_{OP} = 0 mT, T_A = -40°C to +125°C		±8.0		mV
				±0.2		% FS
V_{OUT} – V_{REF} Accuracy Performance (TSSOP-8 only)						
$E_{OUT-VREF}$	VOUT – VREF Error	B_{OP} = $B_{RNG(MAX)}$, V_{CC} = 5.0 V T_A = -40°C to +125°C			±1.0	% FS
V_{OUT} - V_{REF}	OUT – VREF Offset Voltage	V_{CC} = 5.0 V T_A = -40°C to +125°C		±5.0		mV
Lifetime Drift						
E_{TOT_DRIFT}	Total Output Error Lifetime Drift (1)	B_{OP} = $B_{OP(MAX)}$		±1.0		% FS

(1) Guaranteed by design and characterization; not tested in production.

Electrical Characteristics for CT450-x24DR

V_{CC} = 5.0 V and T_A = +25°C and C_{BYP} = 1.0 µF (unless otherwise specified)

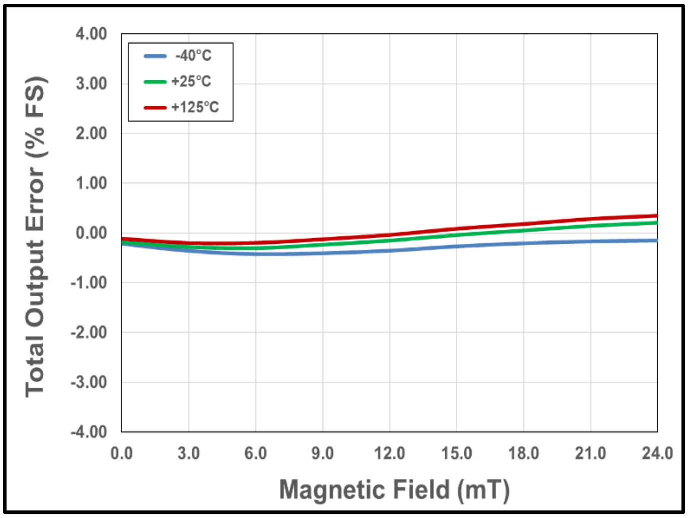


Figure 26. Total Output Error vs. Current vs. Temperature

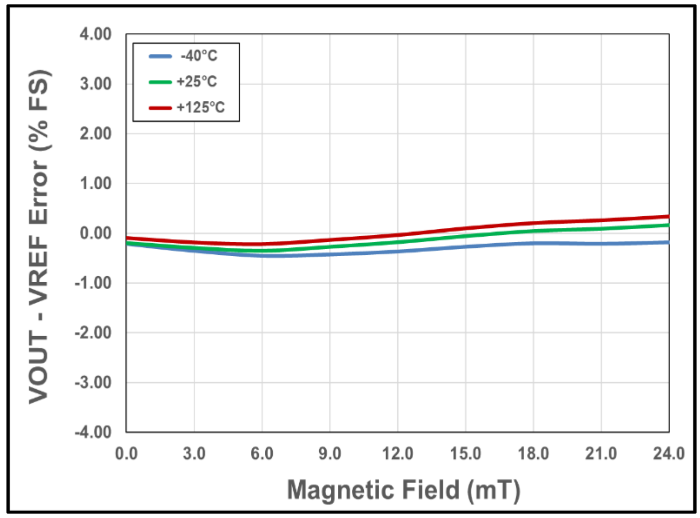


Figure 27. VOUT – VREF Error vs. Current vs. Temperature for TSSOP-8 Variant

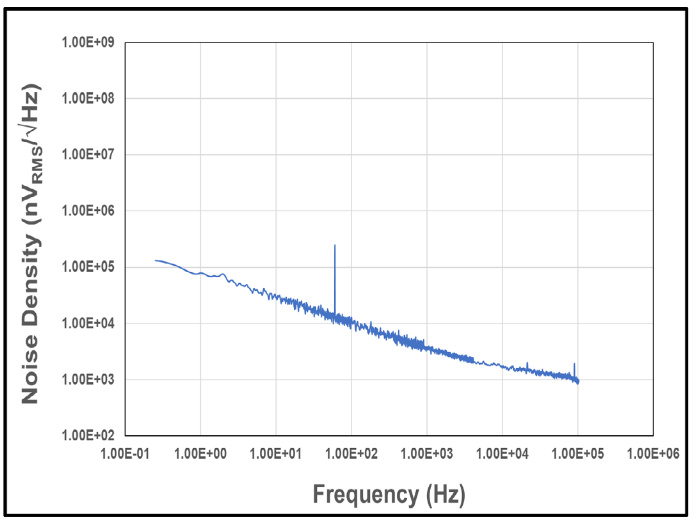


Figure 28. Noise Density vs. Frequency

CT450-x24MR: -24 mT to +24 mT

Unless otherwise specified: V_{CC} = 4.75 V to 5.50 V, T_A = -40°C to +125°C, C_{BYP} = 1.0 µF. Typical values are V_{CC} = 5.00 V and T_A = +25°C.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
B_{RNG}	Magnetic Field Range		-24		+24	mT
V_{OQ}	Voltage Output Quiescent	T_A = +25°C, B_{OP} = 0 mT	2.490	2.500	2.510	V
S	Sensitivity	$B_{RNG(MIN)} < B_{OP} < B_{RNG(MAX)}$		83.3		mV/mT
f_{BW}	Bandwidth (1)	Small Signal = -3 dB		1.0		MHz
e_N	Noise (1)	T_A = +25°C, f_{BW} = 100 kHz		0.38		mV_{RMS}
				4.56		μT_{RMS}
OUT Accuracy Performance						
E_{OUT}	Total Output Error	$B_{OP} = B_{RNG(MAX)}$		±0.5	±1.0	% FS
E_{LIN}	Non-Linearity Error (1)	$B_{OP} = B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.2		% FS
E_{SENS}	Sensitivity Error (1)	$B_{OP} = B_{RNG(MAX)}$, T_A = -40°C to +125°C		±0.3		% FS
V_{OFFSET}	Offset Voltage (1)	B_{OP} = 0 mT, T_A = -40°C to +125°C		±7.0		mV
				±0.2		% FS
V_{OUT} – V_{REF} Accuracy Performance (TSSOP-8 only)						
$E_{OUT-VREF}$	VOUT – VREF Error	$B_{OP} = B_{RNG(MAX)}$, V_{CC} = 5.0 V T_A = -40°C to +125°C			±1.0	% FS
V_{OUT} - V_{REF}	OUT – VREF Offset Voltage	V_{CC} = 5.0 V T_A = -40°C to +125°C		±5.0		mV
Lifetime Drift						
E_{TOT_DRIFT}	Total Output Error Lifetime Drift (1)	$B_{OP} = B_{OP(MAX)}$		±1.0		% FS

(1) Guaranteed by design and characterization; not tested in production.

Electrical Characteristics for CT450-x24MR

V_{CC} = 5.0 V and T_A = +25°C and C_{BYP} = 1.0 μF (unless otherwise specified)

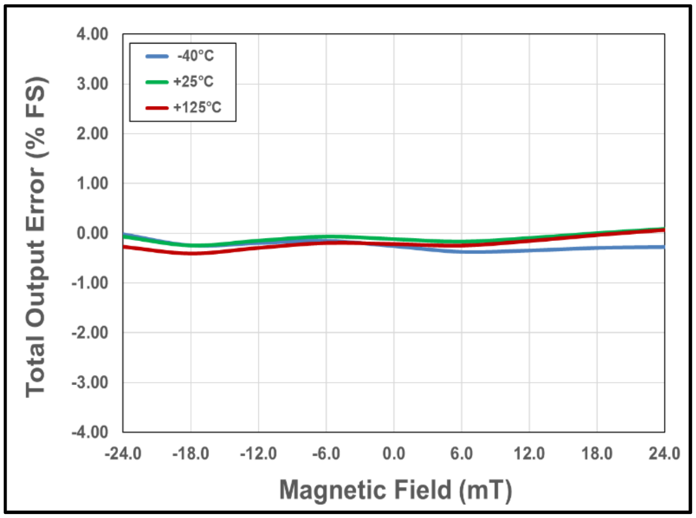


Figure 29. Total Output Error vs. Current vs. Temperature

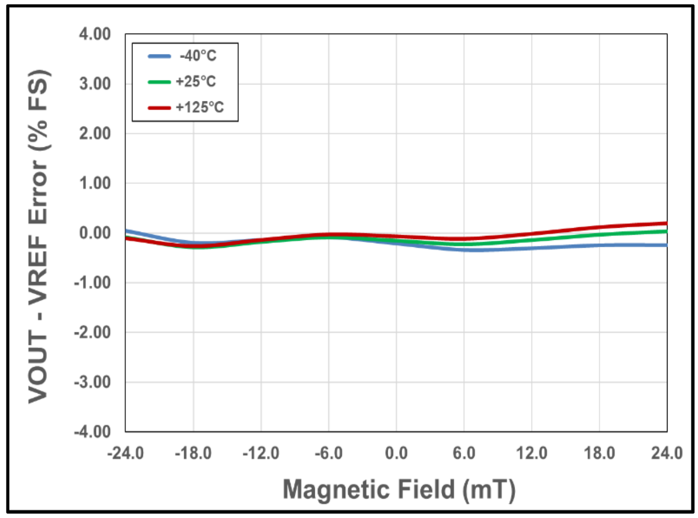


Figure 30. VOUT – VREF Error vs. Current vs. Temperature for TSSOP-8 Variant

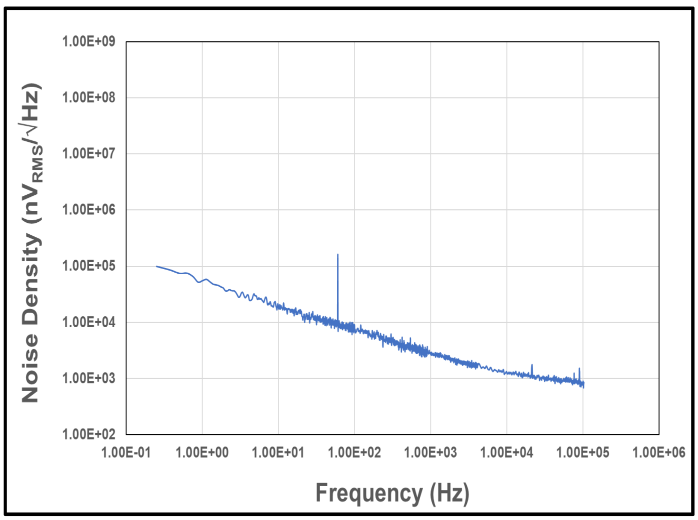


Figure 31. Noise Density vs. Frequency

Circuit Description

Overview

The CT450 is a very high accuracy contactless current sensor that can sense magnetic fields from 6 mT to 24 mT. It has very high sensitivity and a wide dynamic range with excellent accuracy (very low total output error) across temperature. This current sensor supports six (6) field ranges:

- 0 mT to +6 mT
- -6 mT to +6 mT
- 0 mT to +12 mT
- -12 mT to +12 mT
- 0 mT to 24 mT
- -24 mT to +24 mT

When current is flowing through a busbar above or below the CT450, the XtemeSense TMR sensor inside the chip senses the field which in turn generates a differential voltage signals that then goes through the Analog Front-End (AFE) to output a current measurement as low as ±1.0% full-scale (FS) total output error (E_{OUT}).

The chip is designed to enable a very fast response time of 300 ns for the current measurement from the OUT pin as the bandwidth for the CT450 is 1.0 MHz. Even with a high bandwidth, the chip consumes a minimal amount of power.

Linear Output Magnetic Field Measurement

The CT450 provides a continuous linear analog output voltage which represents the magnetic field generated by the current flowing through the busbar. The output voltage range of OUT is from 0.50 V to 4.50 V with a V_{OQ} of 0.50 V and 2.50 V for unidirectional and bidirectional fields, respectively. Figure 32 illustrates the output voltage range of the OUT pin as a function of the measured field.

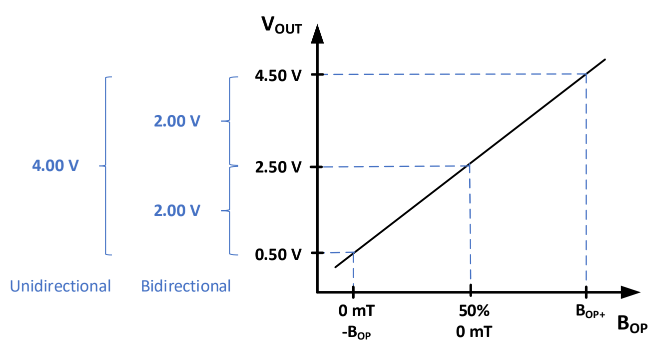


Figure 32. Linear Output Voltage Range (OUT) vs. Measured Magnetic Field (B_{OP})

Voltage Reference Function (VREF)

The CT450 in TSSOP-8 package has a reference voltage (VREF) pin that may be used as an output voltage reference for AC or DC field/current measurements. The VREF pin should be connected to a buffer circuit.

If the VREF is not used, then it should be left unconnected.

Filter Function (FILTER)

The CT450 in SOIC-8 package has a pin for the FILTER function which will enable it to improve the noise performance by changing the cut-off frequency. The bandwidth of the CT450 is 1.0 MHz, however by adding a capacitor to the FILTER pin which, will be in series with an internal resistance of approximately 15 kΩ, will set the cut-off frequency to reduce the noise. Table 3 shows the capacitor values required to achieve four (4) cut-off frequencies.

Table 3. R-C Filter Options for FILTER Pin

Cut-off Frequency	C_{FILTER} (pF)	Capacitor Part Number
100 kHz	91	GRM0225C1C910JA02
250 kHz	33	GRM0225C1C330JA02
500 kHz	16	GRM0225C1C160JA03
1.0 MHz	5	GRM0225C1C5R0CA03

If the FILTER pin is not used, then it should be left unconnected (No Connect).

Total Output Error

The Total Output Error is the difference between the magnetic field measured by CT450 and the actual field, relative to the actual field. It is equivalent to the ratio between the difference of the ideal and actual voltage to the ideal sensitivity multiplied by the magnetic field due to current flowing through the busbar. The equation below defines the Total Output Error (E_{OUT}) for the CT450:

$$E_{OUT} = \frac{V_{OUT\_IDEAL}(B_{OP}) - V_{OUT}(B_{OP})}{S_{IDEAL} \times B_{OP}(FS)}$$

The E_{OUT} incorporates all sources of error and is a function of the sensed magnetic field (B_{OP}) from CT450. At high field levels, the E_{OUT} will be dominated by the sensitivity error whereas at low fields, the dominant characteristic is the offset voltage. Figure 33 shows the behavior of E_{OUT} versus B_{OP}. When B_{OP} goes 0 from both directions, the curves exhibit asymptotic behavior i.e., E_{OUT} approaches infinity.

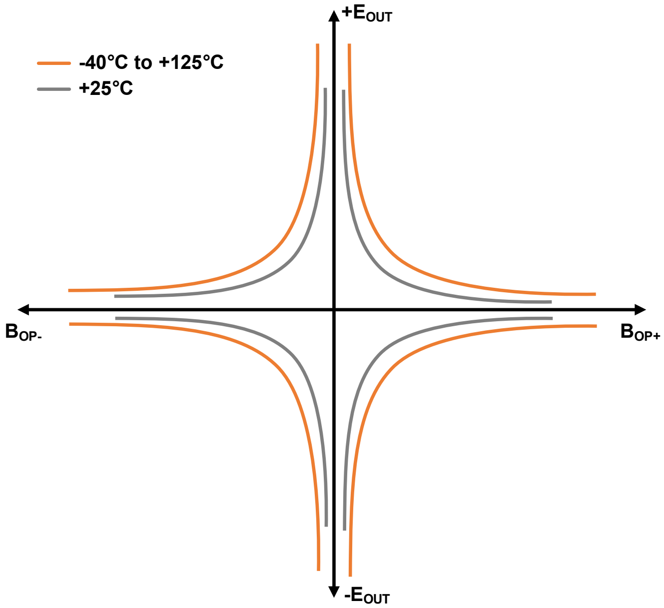


Figure 33. Total Output Error (E_{OUT}) vs. Field (B_{OP})

The CT450 achieves a total output error (E_{OUT}) that is less than ±1.0% of Full-Scale (FS) over supply voltage and temperature. It is designed with innovative and proprietary TMR sensors and circuit blocks to provide very accurate magnetic field measurements regardless of the operating conditions.

Power-On Time (t_{ON})

The Power-On Time (t_{ON}) of 100 µs is the amount of time required by CT450 to start up, fully power the chip and becoming fully operational from the moment the supply voltage is greater than the UVLO voltage. This time includes the ramp up time and the settling time (within 10% of steady-state voltage under an applied magnetic field) after the power supply have reach the minimum V_{CC}.

Response Time ($t_{RESPONSE}$)

The Response Time ($t_{RESPONSE}$) of 300 ns for the CT450 is the time interval between the following terms:

1. When the primary field/current signal reaches 90% of its final value,
2. When the chip reaches 90% of its output corresponding to the applied field/current.

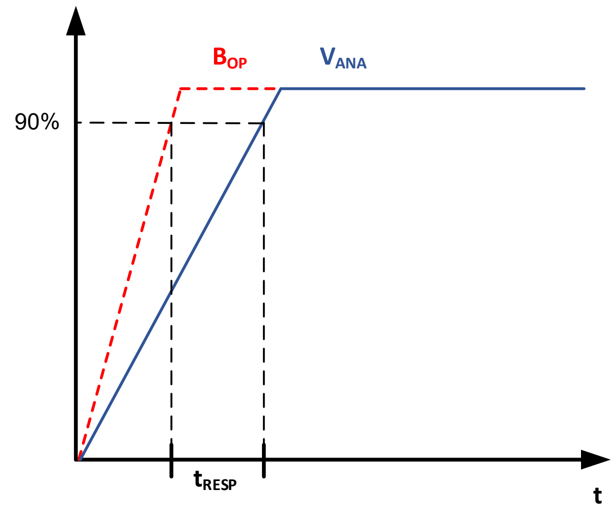


Figure 34. CT450 Response Time Curve

Rise Time (t_{RISE})

The CT450's rise time, t_{RISE}, is the time interval of when it reaches 10% and 90% of the full-scale output voltage. The t_{RISE} of the CT450 is 200 ns.

Propagation Delay (t_{DELAY})

The Propagation Delay (t_{DELAY}) is the time difference between these two events:

1. When the primary current reaches 20% of its final value
2. When the chip reaches 20% of its output corresponding to the applied field/current.

The CT450 has a propagation delay of 250 ns.

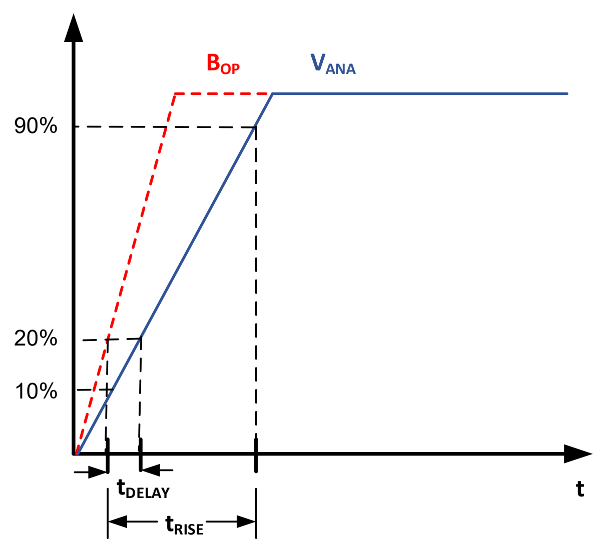


Figure 35. CT450 Propagation Delay and Rise Time Curve

Under-Voltage Lockout (UVLO)

The Under-Voltage Lock-out protection circuitry of the CT450 is activated when the supply voltage (V_{CC}) falls below 2.45 V. The CT450 remains in a low quiescent state until V_{CC} rises above the UVLO threshold (2.50 V). In this condition where the V_{CC} is less than 2.45 V and UVLO is triggered, the output from the CT450 is not valid and the FLT pin will go LOW. Once the V_{CC} rises above 4.0 V then the UVLO is cleared, and the $\overline{FLT}$ pin will be HIGH.

CT450 Calibration Guide

Introduction

All current sensors, no matter how expensive they are, or what materials they use, or even if they were factory calibrated, are susceptible to deviations from their Ideal Transfer Line.

To extract the absolute best performance from any current sensing system, calibration is required.

Ideal Transfer Line

Ideally, the sensor output follows a straight line, has a fixed slope, and crosses a fix offset point. This allows the user to apply a straightforward linear equation to extract the "physical" value being measured. In the case of a current sensor:

$$Current = \frac{Voltage - b}{a}$$

where a: slope and b: offset of the ideal curve. In a perfect sensor, both a and b coefficients can be simply looked up on the datasheet.

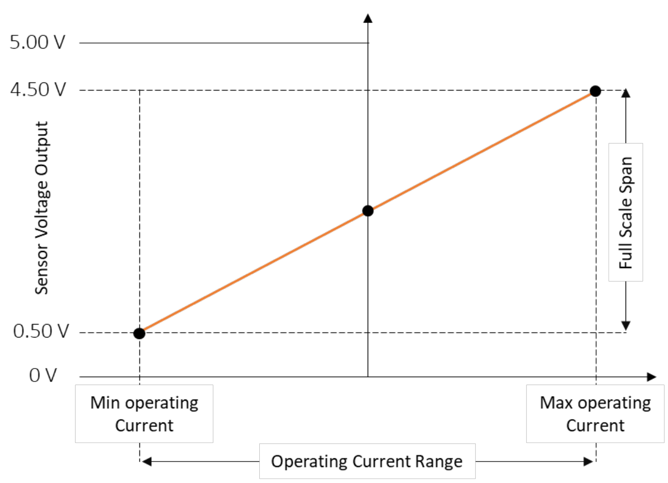


Figure 36. Ideal Transfer Line

Any deviation from this Ideal Line are considered sensor errors. More specifically Accuracy Errors as they related in the case of Crocus Technology's sensors to Gain and Offset errors.

Offset Error

Based on the Ideal Transfer Line, when no current is applied, the voltage output of the sensor should be equal to 2.50 V. On the datasheet, the user can find the spread (i.e. min-max) values of offsets of Crocus Technology's products.

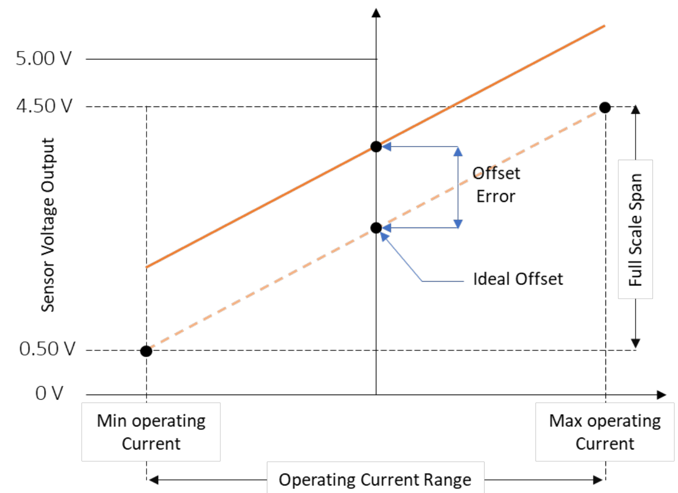


Figure 37. Exaggerated Offset Error

Gain Error

The Ideal Transfer Line shows a line that reaches 4.50 V at the maximum operating current and 0.50 V at the minimum. The datasheet also shows the spread of the gain found on the sensors.

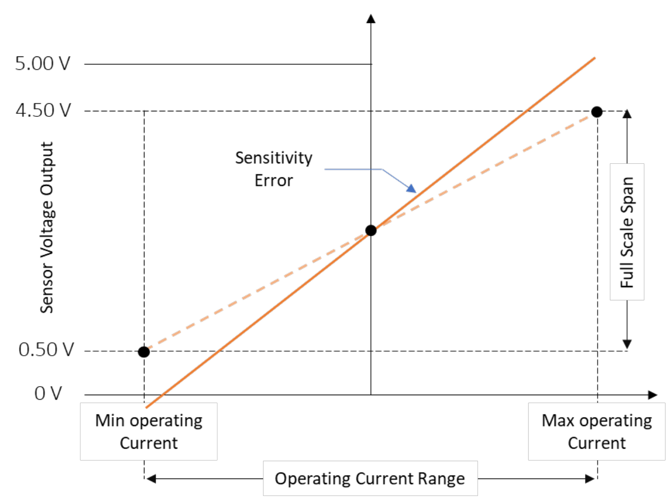


Figure 38. Exaggerated Gain Error

Calibration

Different methods can be applied for offset and/or gain correction. The complexity of these methods lead to different calibration results. The higher the complexity the better the error correction is.

Simple Offset Correction

Offset calibration is achieved simply by storing the voltage output of the sensor at zero flowing current.

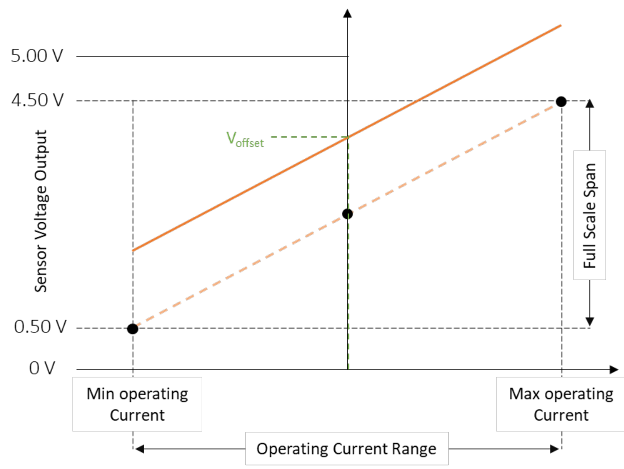


Figure 39. Simple Offset Calibration

This stored value V_{OFFSET} becomes the coefficient "b" in the linear transfer function:

$$Current = \frac{Voltage - b}{a}$$

Simple Gain Correction

Basic Gain calibration can be achieved by applying a known current value (A_1) and measure the sensor output voltage value (V_1)

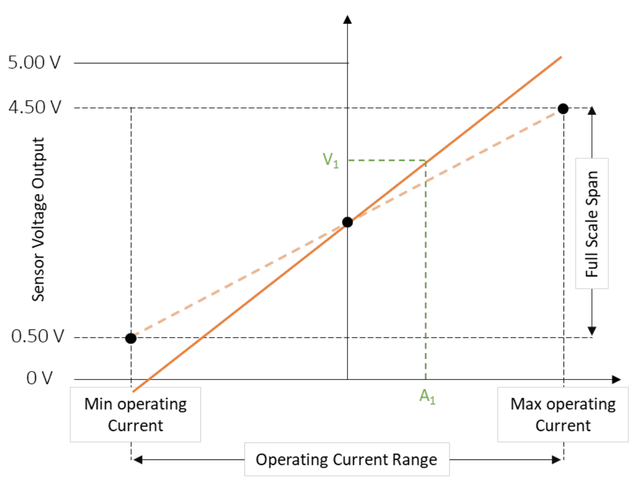


Figure 40. Simple Gain Calibration

The following equation is used to calculate the slope coefficient "a":

$$a = \frac{V_1 - V_{OFFSET}}{A_1}$$

Recommended Offset and Gain Correction

For bi-directional current applications, the steps below are recommended for users trying to perform the best error correction of gain and offset.

1. Apply a known current value (A_1) and measure voltage output (V_1)
2. Apply a "second current value" (A_2) and measure the voltage output (V_2)
3. Calculate the slope using the following equation.

It is recommended that the applied currents A_1 and A_2 are the absolute maximum and minimum operating current the sensor will see during its normal operations.

Also, $A_1 = -A_2$ for bi-directional current sensing.

$$a = \frac{V_1 - V_2}{A_1 - A_2} \qquad b = \frac{V_1 + V_2}{2}$$

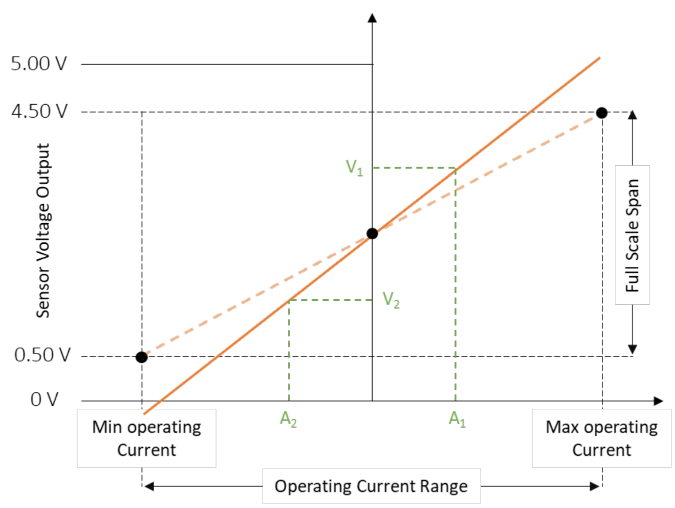


Figure 41. Gain Calibration

Both calculated coefficients "a" and "b" are then used to calculate the current:

$$Current = \frac{Voltage - b}{a}$$

Applications Information

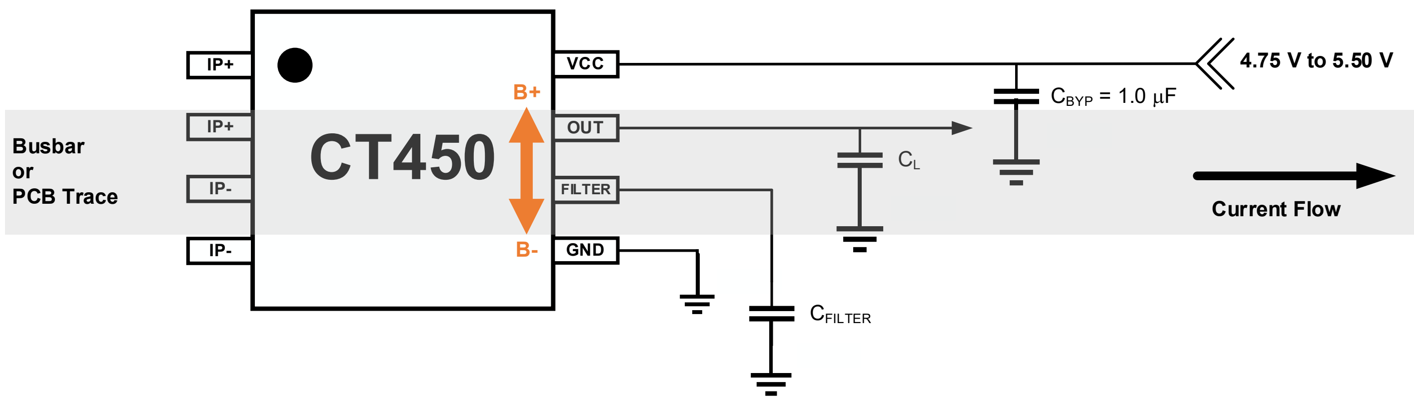


Figure 42. Application Diagram of CT450 Sensing Current of Busbar Above It or PCB Trace Underneath It

Application

The CT450 is an integrated contactless current sensor that can be used in many applications from measuring current in solar inverters and other high-current applications. The chip outputs to a microcontroller a simple linear analog output voltage which corresponds to a magnetic field (current) measurement value. A second output called FLT# (in the TSSOP-8 variant) alerts the host system to any fault event that may occur in the CT450. Figure 42 is an application diagram of how CT450 in SOIC-8 package would be implemented in a system.

It is designed to support an operating voltage range of 4.75 V to 5.50 V, but it is ideal to use a 5.00 V power supply where the output tolerance is less than ±5%.

Current Sensing

The CT450 can sense and therefore measure the current by either placing a current-carrying busbar above or under the device. The busbar should be placed crosswise or over the pins of the package for the CT450 (in SOIC-8) to measure the current. The chip is also sensitive enough to measure the current from a PCB trace that is routed underneath it.

Bypass Capacitor

A single 1.0 µF capacitor is needed for the VCC pin to reduce the noise from the power supply and other circuits. This capacitor should be placed as close as possible to the CT450 to minimize inductance and resistance between the two devices.

VREF Resistors (TSSOP-8)

In designs where the VREF pin of the CT450 in TSSOP-8 is used, a 10 kΩ resistor must be connected as close to the pin as possible in series with a load.

If the VREF pin is not needed in the application, then this pin should not be connected and be left floating.

Recommended PCB Layout

The CT450 is able to sense the field generated by the current flowing through a Printed Circuit Board (PCB) trace or busbar. Figure 43 shows the schematic for the CTD450 which is evaluation board for the CT450.

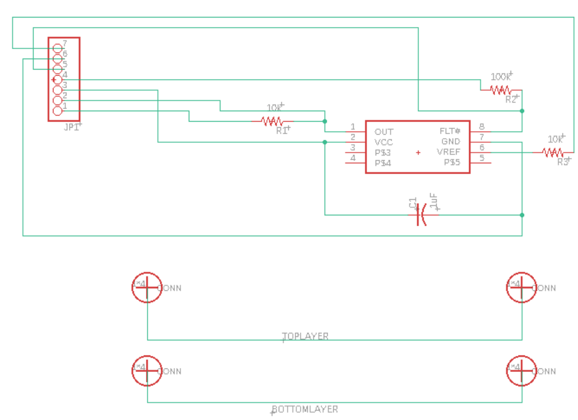


Figure 43. CTD450 Evaluation Board Schematic

The PCB layout of the CTD450 is shown in Figure 44 where the CT450 can sense the current through the PCB trace on the top layer (red trace) or bottom layer (blue trace) of a 4-layer PCB. The width of the PCB trace and the thickness of the copper used will dictate the amount of current that it can carry. Please use Crocus' Contactless Current Sensor Calculator: tools.crocus-technology.com to set the parameters to determine the amount of current the PCB trace (or busbar) can support.

In some applications the isolation voltage is an important requirement. If the PCB trace is implemented on the top layer and the clearance between the trace and the IC pads is 1.25 mm, then it will provide greater than 2.0 kV of isolation between the trace and the TSSOP pins.

For the implementation of the current trace on the bottom layer, a distance of 1.60 mm between the IC pads on the top side of the PCB to the bottom layer of the trace will yield an isolation voltage that is greater than 5.0 kV.

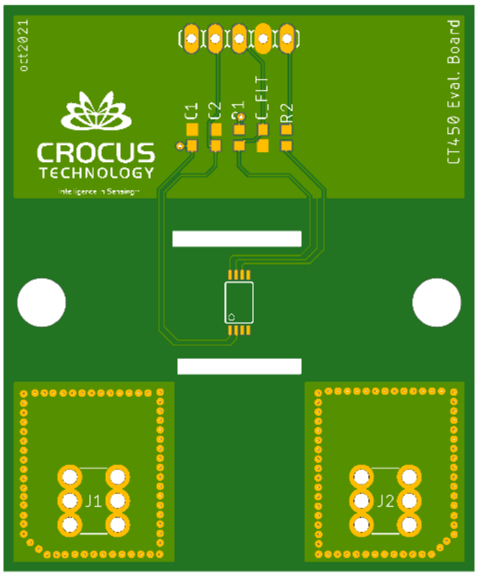


Figure 44. CTD450 Evaluation Board PCB Layout

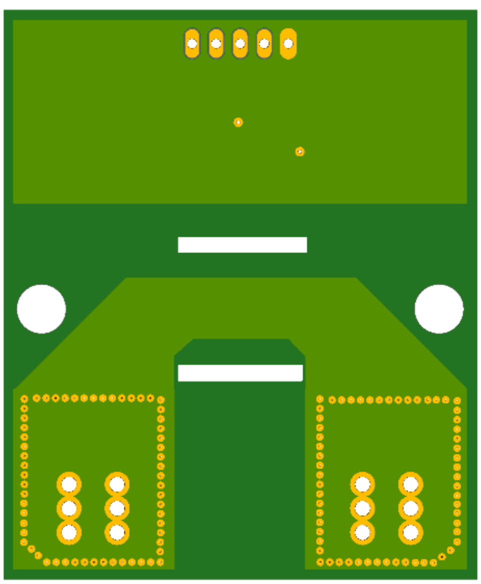


Figure 45. CTD450 Evaluation Board PCB Layout

XtremeSense TMR Current Sensor Location

The XtremeSense TMR current sensor location of the CT450 for the x and y dimensions are shown in Figure 46 and z dimension in Figure 47 for the SOIC-8 package. Similarly, Figure 48 and Figure 49 for the TSSOP-8 package. All dimensions in the figures below are nominal.

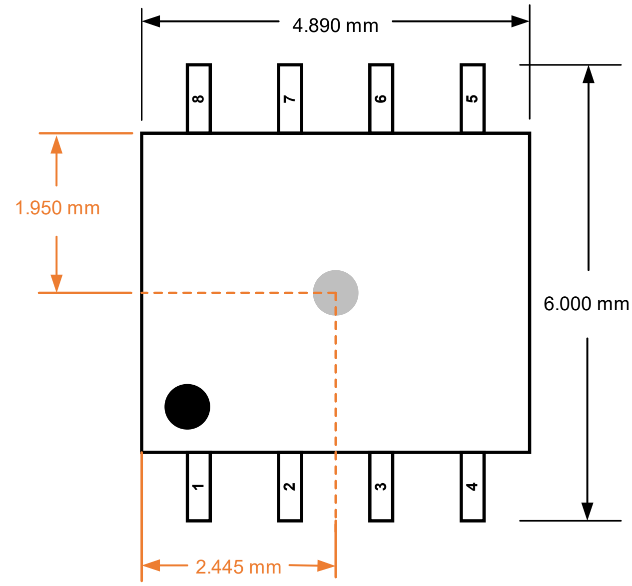


Figure 46. XtremeSense TMR Current Sensor Location in x-y Plane for CT450 in SOIC-8 Package

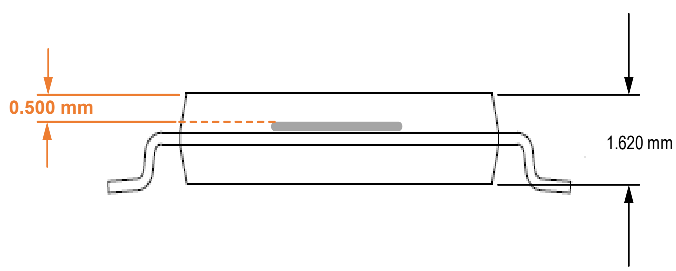


Figure 47. XtremeSense TMR Current Sensor Location in z Dimension for CT450 in SOIC-8 Package

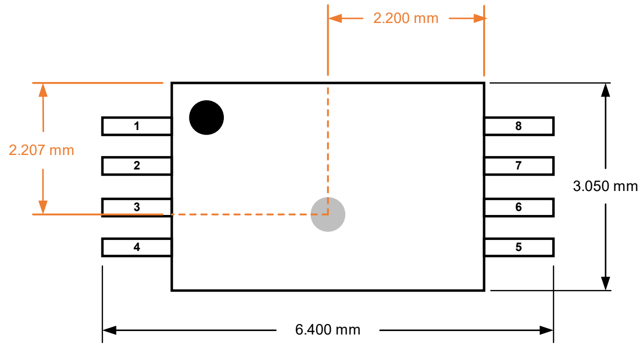


Figure 48. XtremeSense TMR Current Sensor Location in x-y Plane for CT450 in TSSOP-8 Package

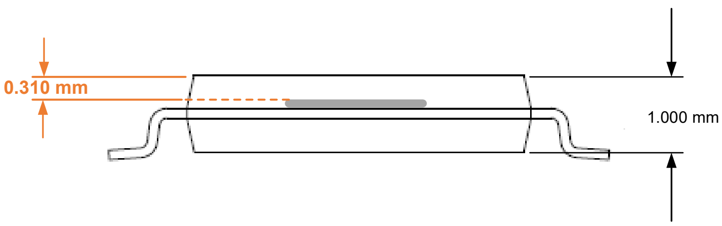


Figure 49. XtremeSense TMR Current Sensor Location in z Dimension for CT450 in TSSOP-8 Package

SOIC-8 Package Drawing and Dimensions

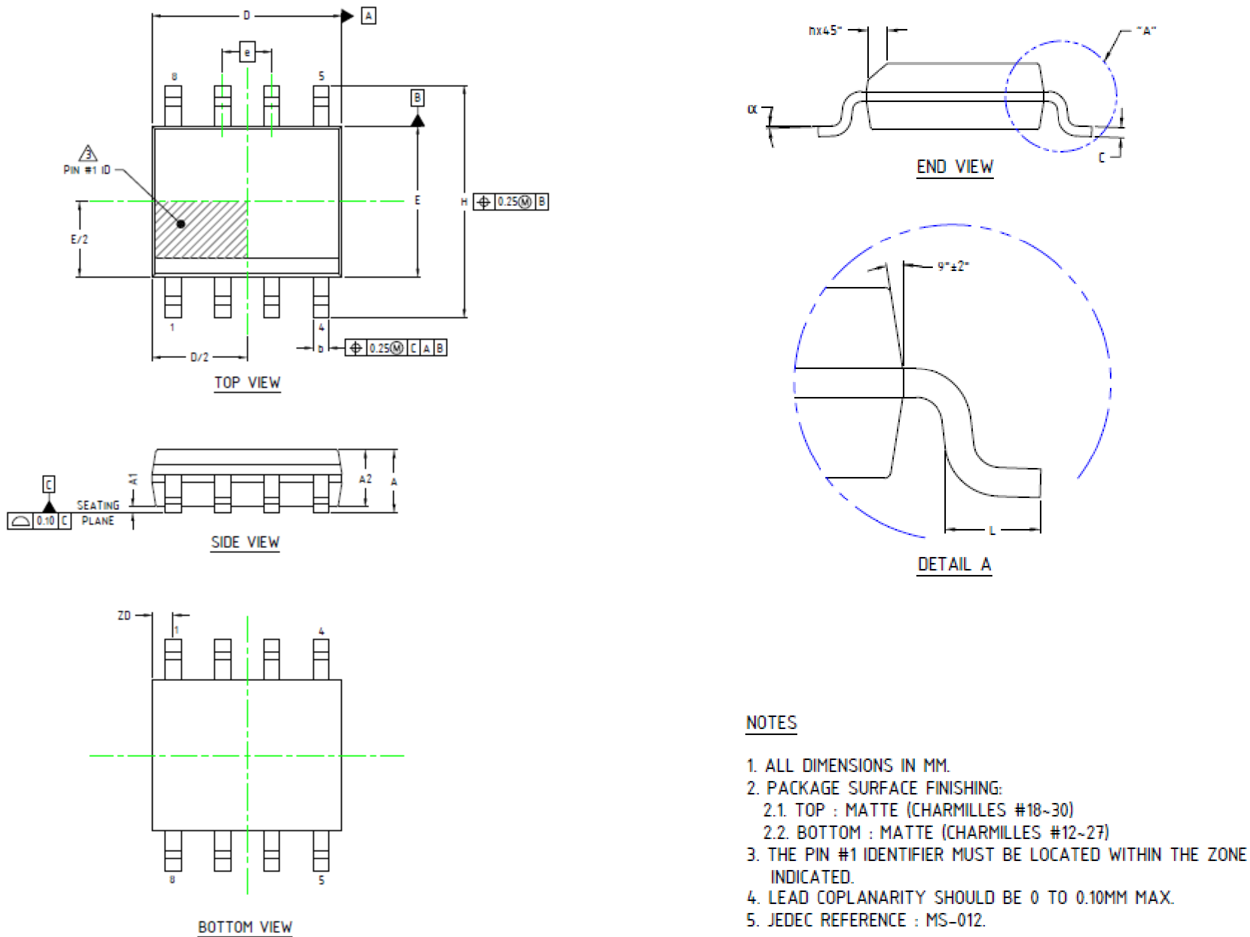


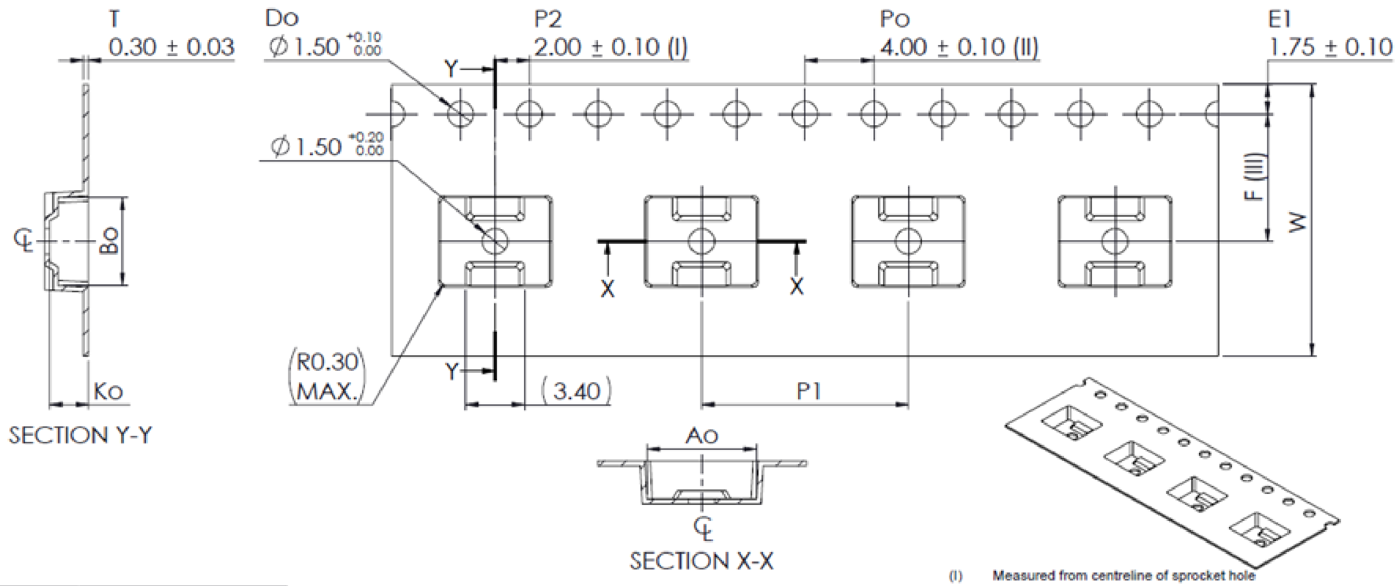
Figure 50. SOIC-8 Package Drawing

Table 4. CT450 SOIC-8 Package Dimensions

Symbol	Dimensions in Millimeters (mm)		
	Min.	Typ.	Max.
A1	0.10	0.18	0.25
b	0.36	0.41	0.46
C	0.19	0.22	0.25
D	4.80	4.89	4.98
E	3.81	3.90	3.99
e	1.27 BSC		
H	5.80	6.00	6.20
h	0.25	0.37	0.50
L	0.41	-	1.27
A	1.52	1.62	1.72
α	0°	-	8°
ZD	0.53 REF		
A2	1.37	1.47	1.57

Crocus Technology provides package drawings as a service to customers considering or planning to use Crocus products in their designs. Drawings may change without notice. Please note the revision and date of the data sheet and contact a Crocus Technology representative to verify or obtain the most recent version. The package specifications do not expand the terms of Crocus Technology's worldwide terms and conditions, specifically the warranty therein, which covers Crocus Technology's products.

SOIC-8 Tape & Pocket Drawing and Dimensions



Ao	6.35	+/- 0.10
Bo	5.13	+/- 0.10
Ko	2.25	+/- 0.15
K1	1.75	+/- 0.10
F	7.50	+/- 0.10
P1	12.00	+/- 0.10
W	16.00	+0.30 / -0.10

(I) Measured from centreline of sprocket hole to centreline of pocket.
(II) Cumulative tolerance of 10 sprocket holes is ± 0.20 .
(III) Measured from centreline of sprocket hole to centreline of pocket.
(IV) Other material available.
(V) Dimension with ( ) is used for design reference purposes, No measurement required.

ALL DIMENSIONS IN MILLIMETRES UNLESS OTHERWISE STATED.

Figure 51. SOIC-8 Package Drawing

TSSOP-8 Package Drawing and Dimensions

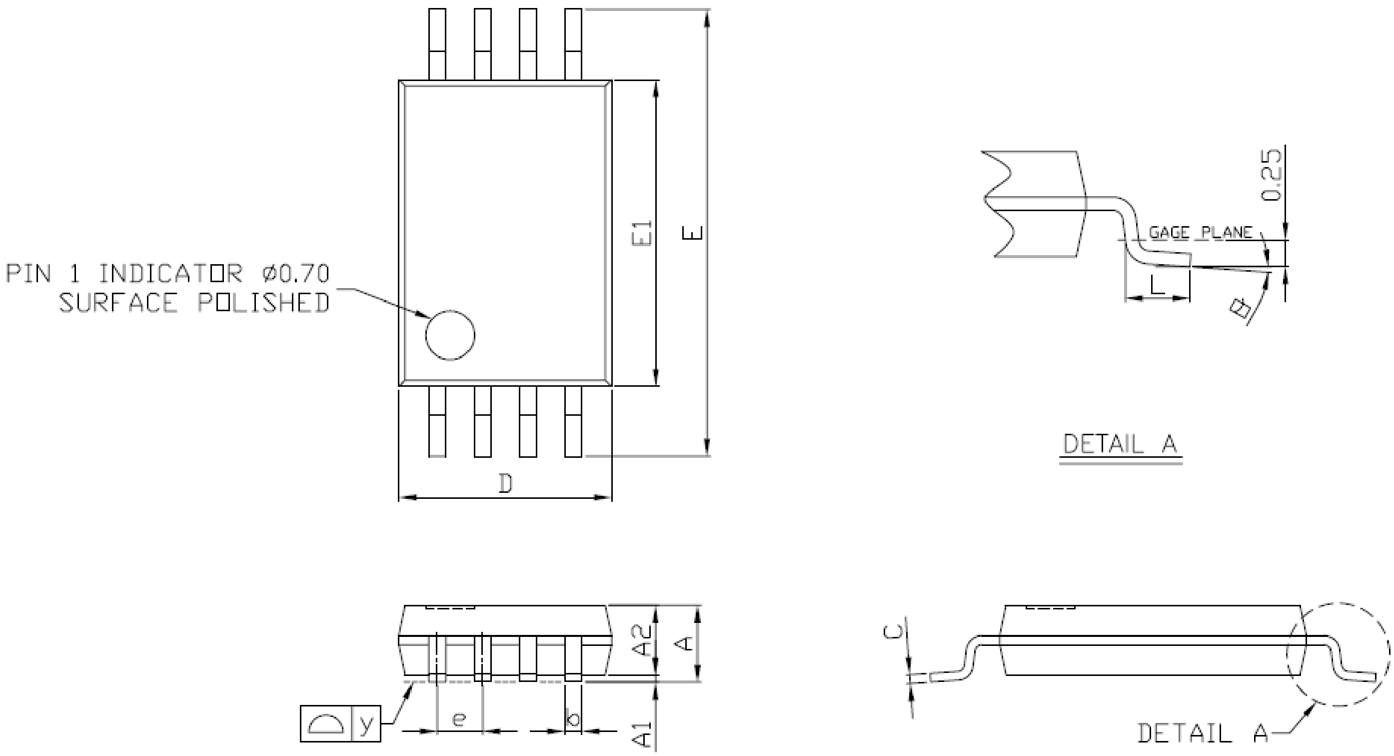


Figure 52. TSSOP-8 Package Drawing

Table 5. CT450 TSSOP-8 Package Dimensions

Symbol	Dimensions in Millimeters (mm)		
	Min.	Typ.	Max.
A	1.05	1.10	1.20
A1	0.05	0.10	0.15
A2	-	1.00	1.05
b	0.25	-	0.30
C	-	0.127	-
D	2.90	3.05	3.10
E	6.20	6.40	6.60
E1	4.30	4.40	4.50
e	-	0.65	-
L	0.50	0.60	0.70
y	-	-	0.076
θ	0°	4°	8°

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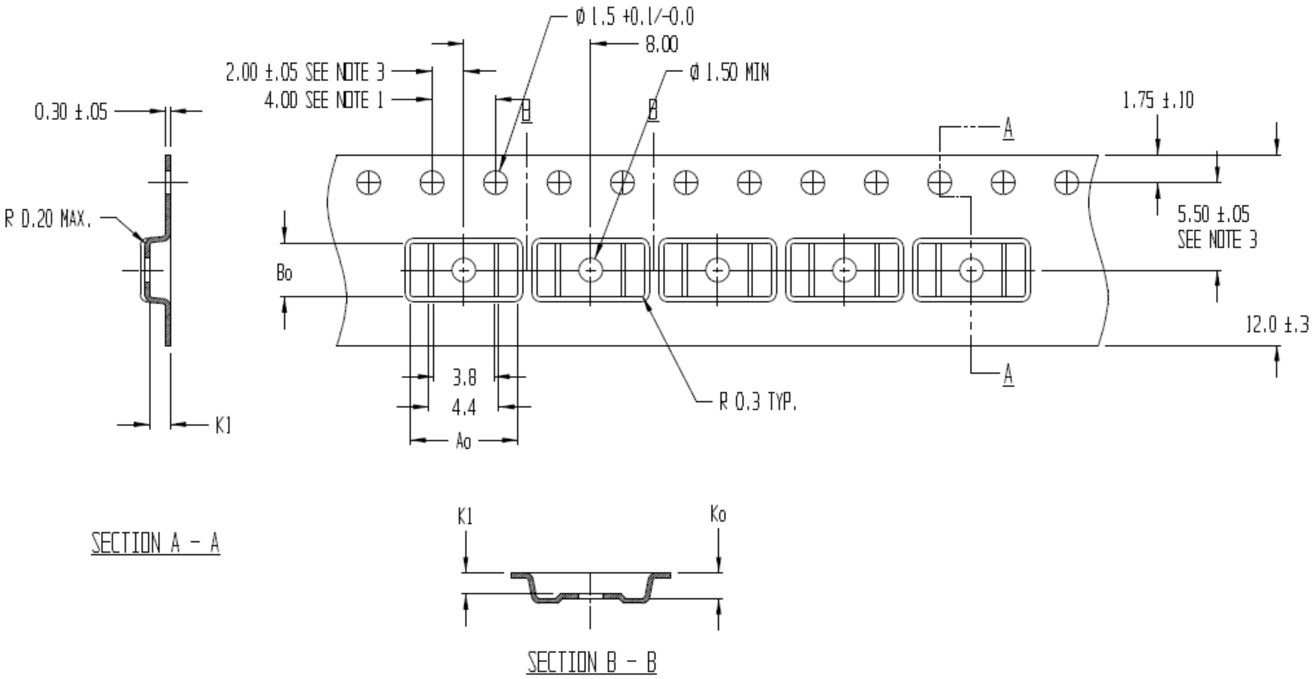
TSSOP-8 Tape & Pocket Drawing and Dimensions



NOTES:
1. 10 SPROCKET HOLE PITCH CUMULATIVE TOLERANCE ±0.2
2. CAMBER IN COMPLIANCE WITH EIA 481
3. POCKET POSITION RELATIVE TO SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE

Ao = 6.80
Bo = 3.40
Ko = 1.60
K1 = 1.30

Figure 53. TSSOP-8 Tape and Pocket Drawings

Package Information

Table 6. CT450 Package Information

Part Number	Package Type	# of Leads	Quantity per Reel	Lead Finish	MSL Rating (2)	Operating Temperature (3)	Device Marking (4)
CT450-H06DRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 06DR YYWWLL
CT450-A06DRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 A06DR YYWWLL
CT450-H06DRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-06DR YYWWLL
CT450-A06DRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-A06DR YYWWLL
CT450-H06MRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 06MR YYWWLL
CT450-A06MRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 A06MR YYWWLL
CT450-H06MRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-06MR YYWWLL
CT450-A06MRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-A06MR YYWWLL
CT450-H12DRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 12DR YYWWLL
CT450-A12DRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 A12DR YYWWLL
CT450-H12DRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-12DR YYWWLL
CT450-A12DRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-A12DR YYWWLL
CT450-H12MRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 12MR YYWWLL
CT450-A12MRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 A12MR YYWWLL
CT450-H12MRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-12MR YYWWLL
CT450-A12MRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-A12MR YYWWLL
CT450-H24DRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 24DR YYWWLL
CT450-A24DRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 A24DR YYWWLL

Part Number	Package Type	# of Leads	Quantity per Reel	Lead Finish	MSL Rating (2)	Operating Temperature (3)	Device Marking (4)
CT450-H24DRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-24DR YYWWLL
CT450-A24DRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-A24DR YYWWLL
CT450-H24MRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 24MR YYWWLL
CT450-A24MRSN08	SOIC	8	3,000	Sn	3	-40°C to +125°C	CT450 A24MR YYWWLL
CT450-H24MRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-24MR YYWWLL
CT450-A24MRTS08	TSSOP	8	3,000	Sn	1	-40°C to +125°C	CT450-A24MR YYWWLL

(1) RoHS is defined as semiconductor products that are compliant to the current EU RoHS requirements. It also will meet the requirement that RoHS substances do not exceed 0.1% by weight in homogeneous materials. Green is defined as the content of Chlorine (Cl), Bromine (Br) and Antimony Trioxide based flame retardants satisfy JS709B low halogen requirements of ≤ 1,000 ppm.

(2) MSL Rating = Moisture Sensitivity Level Rating as defined by JEDEC standard classifications.

(3) Package will withstand ambient temperature range of -40°C to +125°C and storage temperature range of -65°C to +150°C.

(4) Device Marking for CT450 in SOIC-8 is defined as CT450 xxZR YYWWLL where the first 2 lines = part number, and third line is YY = year, WW = work week and LL = lot code. In TSSOP-8 is defined as CT450 xxZR YYWWLL where the first line = part number, and second line is YY = year, WW = work week and LL = lot code.

Device Marking

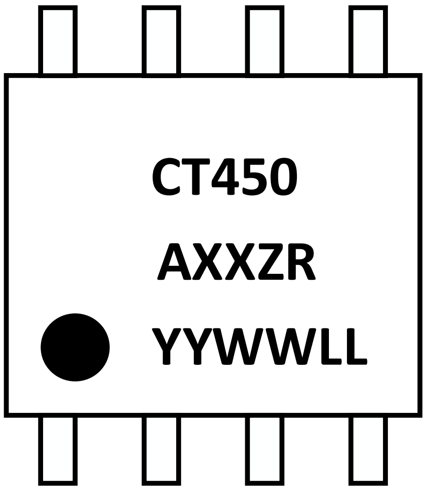


Figure 54. CT450 Device Marking for 8-lead TSSOP Package

Row No.	Code	Definition
3	•	Pin 1 Indicator
1	CT450	Crocus Part Number
2	A	Automotive Identifier
2	XX	Maximum Field Rating
2	ZR	Polarity
3	YY	Calendar Year
3	WW	Work Week
3	LL	Lot Code

Table 7. CT450 Device Marking Definition for 8-lead SOIC Package

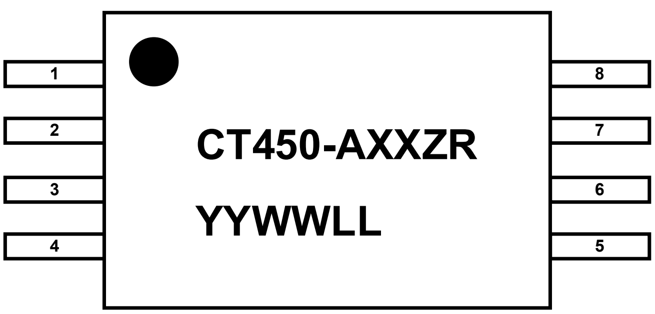


Figure 55. CT450 Device Marking for 8-lead TSSOP Package

Row No.	Code	Definition
1	•	Pin 1 Indicator
2	CT450	Crocus Part Number
2	A	Automotive Identifier
2	XX	Maximum Magnetic Field Rating
2	ZR	Magnetic Field Range
3	YY	Calendar Year
3	WW	Work Week
3	LL	Lot Code

Table 8. CT450 Device Marking Definition for 8-lead TSSOP Package

Part Ordering Number Legend

CT 4 5 0 - T FR XY PCKG

- **Package Type**
  - SN08 = 8-lead SOIC
  - TS08 = 8-lead TSSOP
- **Polarity**
  - DR = Unipolar Magnetic Field
  - MR = Bipolar Magnetic Field
- **Field Range**
  - 06 = 6 mT
  - 12 = 12 mT
  - 24 = 24 mT
- **Temperature Range**
  - A = AEC-Q100 Grade 1
  - H = -40°C to +125°C
- **Current Sensor Variant**
  - 0 = V_{CC} is 5.0 V with Filter
- **Current Sensor Type**
  - 5 = Contactless Current Sensor
- **Product Family**
  - 4 = Current Sensor
- **CT = Crocus Technology**