

date 12/21/2022

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SERIES: PQC10-0 | **DESCRIPTION:** DC-DC CONVERTER

FEATURES

- 10 W isolated output
- 2:1 input range (36~75 Vdc)
- single regulated outputs
- 1,500 Vdc isolation
- industry standard 1/16th brick
- over-current, input under-voltage, over-voltage and output short-circuit protection
- EN/BS EN 62368 certified



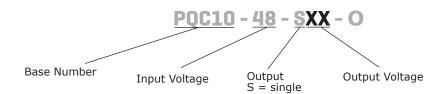


MODEL		nput oltage	output voltage		ıtput rrent	output power	ripple and noise ¹	efficiency ²
	typ (Vdc)	range (Vdc)	(Vdc)	min (mA)	max (mA)	max (W)	max (mVp-p)	typ (%)
PQC10-48-S5-O	48	36~75	5	0	2,000	10	120	83
PQC10-48-S12-O	48	36~75	12	0	833	10	120	87
PQC10-48-S15-O	48	36~75	15	0	667	10	120	88
PQC10-48-S24-O	48	36~75	24	0	417	10	120	88

Notes: 1. Ripple and noise are measured at 20 MHz BW, 5%~100% load by "parallel cable" method with 1 µF ceramic and 10 µF electrolytic capacitors on the output.

 ${\hbox{\bf 2. Efficiency is measured at nominal input voltage and rated output load.}}\\$

PART NUMBER KEY



INPUT

parameter	conditions/description	min	typ	max	units
operating input voltage		36	48	80	Vdc
current (full load/no load)	at nominal input voltage		252/4	258/8	mA
reflected ripple current	at nominal input voltage		50		mA
start-up voltage				36	Vdc
under-voltage protection		26	29		Vdc
start-up time	at nominal input voltage & constant resistance load			100	ms
surge voltage	for maximum of 1 second	-0.7		100	Vdc
	module on (CTRL pin open or pulled high (TTL 3.5~12	!Vdc)			
CTRL ³	module off (CTRL pin pulled low to GND (0~1.2Vdc)				
	input current when off		6	10	mA
filter	C filter				

Notes: 3. The CTRL pin voltage is referenced to input GND.

OUTPUT

parameter	conditions/description	min	typ	max	units
	5 Vdc output			2,200	μF
maximum capacitive load	12 Vdc output			470	μF
maximum capacitive load	15 Vdc output			330	μF
	24 Vdc output			100	μF
line regulation	full load, input voltage from low to high		±0.2	±0.5	%
load regulation	5% to 100% load		±0.5	±1	%
voltage accuracy	5% to 100% load		±1	±3	%
switching frequency ⁴	PWM mode		300		kHz
transient recovery time	25% load step change		300	500	μs
	25% load step change				
transient response deviation	5 Vdc output voltage		±5	±8	%
	all other output models		±3	±5	%
temperature coeffecient	full load			±0.03	%/°C

Notes: 4. Switching frequency is measured at full load. The module reduces the switching frequency for light load (below 50%) efficiency improvement.

PROTECTIONS

parameter	conditions/description	min	typ	max	units
over voltage protection		110		160	%
over current protection		110	140	190	%
short circuit protection	auto recovery, continuous				

SAFETY AND COMPLIANCE

parameter	conditions/description	min	typ	max	units
isolation voltage	input to output for 1 minute at 1 mA max.	1,500			Vdc
isolation resistance	input to output at 500 Vdc	1,000			МΩ
isolation capacitance	input to output at 100kHz/0.1V		1,000		pF
vibration	10-150Hz, 5G, 0.75mm. along X, Y and Z				
safety approvals	certified to 62368: EN/BS EN				
conducted emissions	CISPR32/EN55032 CLASS B (see Fig.2-2 for recommended circuit)				
radiated emissions	CISPR32/EN55032 CLASS B (see Fig.2-2 for recommended circuit)				
ESD	IEC/EN61000-4-2 Contact ±4kV, perf. Criteria B				
radiated immunity	IEC/EN61000-4-3 10V/m, perf. Criteria A				

SAFETY AND COMPLIANCE

parameter	conditions/description	min	typ	max	units
EFT/burst	IEC/EN61000-4-4 \pm 2kV (see Fig.2-1 for re	ecommended circuit), p	erf Criteria B		
surge	IEC/EN61000-4-5 line to line ±2kV (see F	IEC/EN61000-4-5 line to line ±2kV (see Fig.2-1 for recommended circuit), perf. Criteria B			
conducted immunity	IEC/EN61000-4-6 3Vrms, perf. Criteria A				
MTBF	as per MIL-HDBK-217F @ 25°C	1,000,000			hours
RoHS	yes				

ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curve	-40		85	°C
storage temperature		-55		125	°C
storage humidity	non-condensing	5		95	%

MECHANICAL

parameter	conditions/description	min	typ	max	units
dimensions	33.02 x 22.86 x 11.40 (1.300 x 0.900 x 0.449 inch)				mm
weight			5.8		g
cooling method	natural convection				

MECHANICAL DRAWING

units: mm[inch]

tolerance: $\pm 0.50[\pm 0.020]$

pin section tolerance: $\pm 0.10[\pm 0.004]$

pin 1,2,3: Ø1.5mm pin 4,8: Ø2mm

Note: The layout of the device is for reference only, please refer to the actual product.

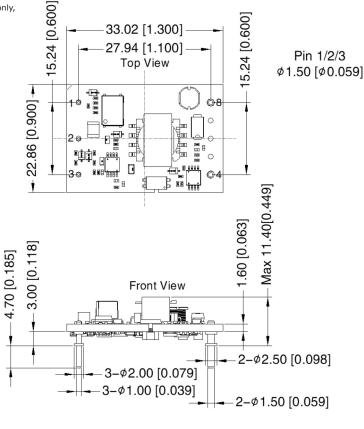
0V

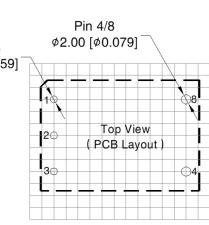
+Vo

PIN CONNECTIONS				
PIN	Function			
1	Vin			
2	CTRL			
3	GND			

4

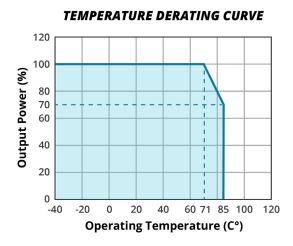
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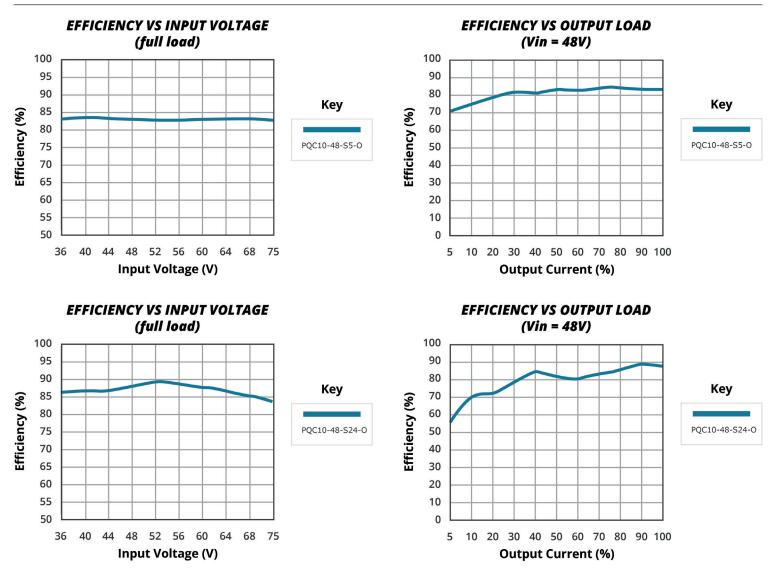


Note: Grid 2.54*2.54mm

DERATING CURVE



EFFICIENCY CURVES



APPLICATION NOTES

All DC-DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 1. Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cou t and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.

Figure 1

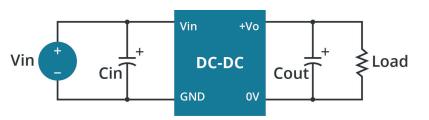
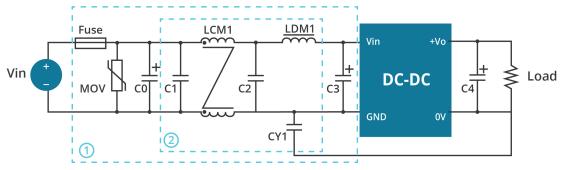


Table 1

Vin (Vdc)	Vout (Vdc)	Cin (µF/V)	Cout (µF)
	5		10μF/16V
48	12/15	100µF/100V	10μF/25V
	24		10μF/50V

EMC RECOMMENDED CIRCUIT

Figure 2



Notes: For EMC tests we use Part 1 in Fig. 3 for immunity and part 2 for emissions test. Selecting based on needs.

Table 2

	Recommended external circuit components			
FUSE	choose according to practical input current			
MOV	S14K60			
C0	680µF/100V			
C1/C2	4.7μF/100V			
C3	330μF/100V			
C4	refer to the Cout in Fig. 1			
LCM1	4.7mH			
LDM1	10μH			
CY1	1nF/2kV			

Note: 1. Maximum capacitive load is tested at input voltage range and full load.

^{2.} All specifications are measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.

Additional Resources: Product Page | PCB Footprint

CUI Inc | SERIES: PQC10-0 | DESCRIPTION: DC-DC CONVERTER date 12/21/2022 | page 6 of 6

REVISION HISTORY

rev.	description	date
1.0	initial release	12/21/2022

The revision history provided is for informational purposes only and is believed to be accurate.



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IAB0124D15 IEB0112S12 IEB0124S12