

02/15/2021 date

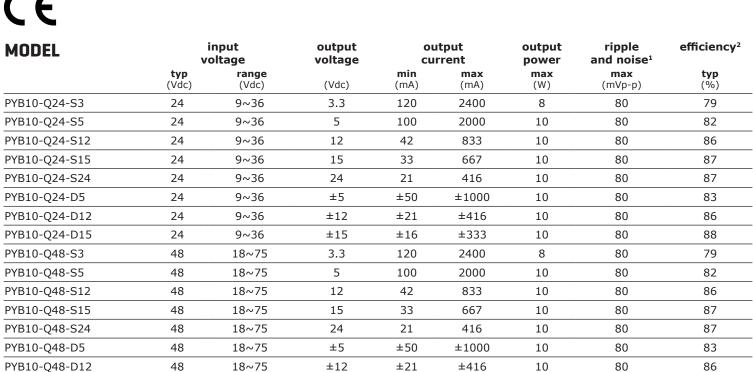
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#### **DESCRIPTION:** DC-DC CONVERTER **SERIES:** PYB10-T & PYB10-U

#### **FEATURES**

- up to 10 W isolated output
- industry standard pinout
- 4:1 input range (9~36 V, 18~75 V)
- smaller package
- single/dual regulated outputs
- 1,500 Vdc isolation
- continuous short circuit, over voltage protection
- reverse polarity protection on chassis mount (-T) option
- temperature range (-40~85°C)
- six-sided metal shielding
- efficiency up to 88%
- EN 62368-1





Notes:

PYB10-O48-D15

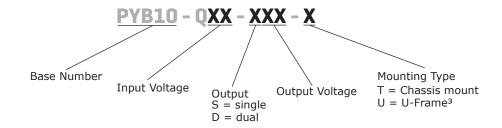
1. Ripple and noise are measured at 20 MHz BW by "parallel cable" method

18~75

- 2. Efficiency is approximately 2% lower for Chassis Mount (-T) models.
- 3. EN 62398-1 certification does not apply to U-frame models.

48

## **PART NUMBER KEY**



±16

 $\pm 15$ 

±333

10

80

88

## **INPUT**

parameter	conditions/description	min	typ	max	units
operating input voltage	24 V input models 48 V input models	9 18	24 48	36 75	Vdc Vdc
	<u> </u>	10	40		
start-up voltage	24 V input models 48 V input models			18	Vdc Vdc
	for maximum of 1 second				
surge voltage	24 V input models 48 V input models	-0.7 -0.7		50 100	Vdc Vdc
filter	pi filter				
	models ON (CTRL open or connect high I	evel, 3.5-12 Vdc)			
CTRL <sup>1</sup>	models OFF (CTRL connect GND or low le	evel, 0-1.2 Vdc)			
	input current (models OFF)		1	3	mA

1. CTRL pin voltage is referenced to GND.

## **OUTPUT**

parameter	conditions/description	min	typ	max	units
line regulation	full load, input voltage from low to high		±0.2	±0.5	%
load regulation	5% to 100% load		±0.5	±1	%
cross regulation	dual output models: main output 50% load, secondary output from 10% to 100% load			±5	%
voltage accuracy			±1	±2	%
voltage balance <sup>2</sup>	dual output, balanced loads		±0.5	±1.5	%
switching frequency	PWM mode		350		KHz
transient recovery time	25% load step change		300	500	μs
transient response deviation	25% load step change		±3	±5	%
temperature coefficient	100% load			±0.03	%/°C

Note: 2. For dual output models, unbalanced load can not exceed  $\pm 5\%$ . If  $\pm 5\%$  is exceeded, it may not meet all specifications.

## **PROTECTIONS**

parameter	conditions/description	min	typ	max	units
short circuit protection	continuous, automatic recovery				
over voltage protection		110	120	140	%Vo

## **SAFETY AND COMPLIANCE**

parameter	conditions/description	min	typ	max	units
isolation voltage	for 1 minute at 1 mA max.	1,500			Vdc
isolation resistance	at 500 Vdc	1,000			МΩ
safety approvals	certified to 62368-1: EN	certified to 62368-1: EN			
conducted emissions	CISPR22/EN55022, class A, class B (extern	al circuit required, see	Figure 1-b)		
radiated emissions	CISPR22/EN55022, class A, class B (extern	al circuit required, see	Figure 1-b)		
ESD	IEC/EN61000-4-2, class B, contact ± 4kV				
radiated immunity	IEC/EN61000-4-3, class A, 10V/m				
EFT/burst	IEC/EN61000-4-4, class B, $\pm$ 2kV (external circuit required, see Figure 1-a)				
surge	IEC/EN61000-4-5, class B, $\pm$ 2kV (external circuit required, see Figure 1-a)				
conducted immunity	IEC/EN61000-4-6, class A, 3 Vr.m.s				
voltage dips & interruptions	IEC/EN61000-4-29, class B, 0%-70%				
MTBF	as per MIL-HDBK-217F @ 25°C	1,000,000			hours
RoHS compliant	yes				

### **ENVIRONMENTAL**

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curve	-40		85	°C
storage temperature		-55		125	°C
storage humidity	non-condensing	5		95	%
case temperature	at full load, Ta=71°C			105	°C
vibration	10~55 Hz for 30 min. along X, Y and Z axis		10		G

## **MECHANICAL**

parameter	conditions/description	min	typ	max	units
dimensions	chassis mount: 76.0 x 31.5 x 21.2 U-Frame: 52.32 x 54.99 x 19.05				mm mm
case material aluminum alloy					
weight	chassis mount U-Frame		44 58		g g

## **MECHANICAL DRAWING**

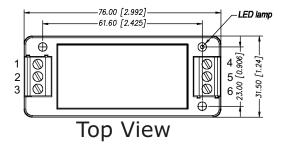
#### **CHASSIS MOUNT**

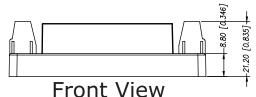
units: mm[inch] tolerance: ±0.5[±0.020]

tolerance:  $\pm 0.5[\pm 0.020]$ 

Wire range: 24~12 AWG

PIN CONNECTIONS				
PIN	Single Output	Dual Output		
1	CTRL	CTRL		
2	GND	GND		
3	Vin	Vin		
4	0V	-Vo		
5	NC	0V		
6	+Vo	+Vo		





### **U-FRAME**

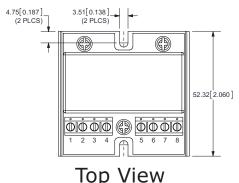
units: mm[inch]

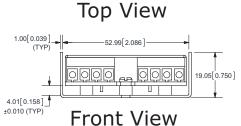
tolerance:  $\pm 0.5[\pm 0.020]$ 

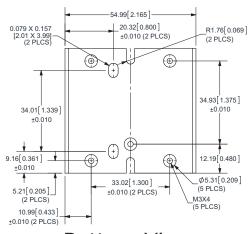
Wire range: 22~14 AWG DIN rail mounting kit available

(part# STK-DIN)

PIN CONNECTIONS					
PIN	Single Output	Dual Output			
1	GND	GND			
2	Vin	Vin			
3	CTRL	CTRL			
4	Case	Case			
5	NC	NC			
6	+Vo	+Vo			
7	NC	0V			
8	0V	-Vo			

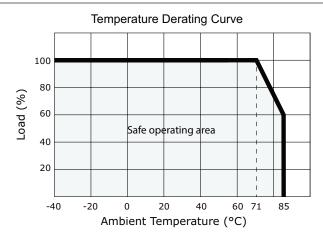






**Bottom View** 

## **DERATING CURVES**



## **EMC RECOMMENDED CIRCUIT**

CY2 **FUSE** Vin ⊙ Vin +Vo LDM1 LDM2 Figure 1 EUT LOAD CO MOV-Vo GND  $\bigcirc$ GND (0V) (b) CY1= (a)

Table 1

Recommended external circuit components						
Vin (Vdc)	24 48					
FUSE	Choose according to input currer					
MOV	S14K35	S14K60				
LDM1	56µH	56μH				
TVS	SMCJ48A	SMCJ90A				
C0	330µF/50V	330µF/100V				
C1	1μF/50V	1μF/100V				
LDM2	4.7µH	4.7µH				
CY1	1 nF/2 KV	1 nF/2 KV				
CY2	1 nF/2 KV	1 nF/2 KV				

## **TEST CONFIGURATION**

Oscilloscope

Lin

Current
Probe

DC

Load

Table 2

External components				
Lin	4.7µH			
Cin	$220\mu\text{F, ESR} < 1.0\Omega$ at 100 KHz			

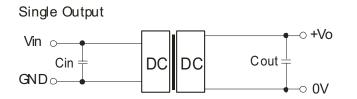
Note: Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.

### **APPLICATION NOTES**

#### 1. Recommended circuit

This series has been tested according to the following recommended testing circuit before leaving the factory. This series should be tested under load (see Figure 3). If you want to further decrease the input/output ripple, you can increase the capacitance accordingly or choose capacitors with low ESR (see table 3). However, the capacitance of the output filter capacitor must be appropriate. If the capacitance is too high, a startup problem might arise. For every channel of the output, to ensure safe and reliable operation, the maximum capacitance must be less than the maximum capacitive load (see Table 4).

Figure 3



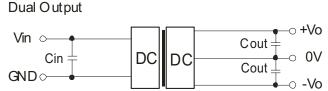


Table 3

Vin (Vdc)	Cin (µF)	Cout (µF)
24	10~47	10
48	10~47	10

Table 4

Single Vout (Vdc)	Max. Capacitive Load (µF)	Dual Vout (Vdc)	Max. Capacitive Load <sup>4</sup> (μF)
3.3	2200		
5	2200	5	680
12	470¹	12	220³
15	330²	15	100
24	100		

Notes:

- 1. 330  $\mu F$  for 48Vin.
- 2. 220 μF for 48Vin.
   150 μF for 48Vin.
- 4. For each output.

2. Maximum capacitive load is tested at input voltage range and full load.

3. All specifications are measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.

Note: 1. Minimum load shouldn't be less than 5%, otherwise ripple may increase dramatically. Operation under minimum load will not damage the converter, however, they may not meet all specifications listed.

## **REVISION HISTORY**

rev.	description	date
1.0	initial release	06/26/2013
1.01	updated spec	08/16/2013
1.02	safeties updated in features and safety approvals line	02/15/2021

The revision history provided is for informational purposes only and is believed to be accurate.



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