

date 01/03/2014

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SERIES: VFK400W | **DESCRIPTION:** DC-DC CONVERTER

FEATURES

- up to 400 W isolated output
- rugged metal enclosure with integrated heat sink
- 4:1 input range (10~36 Vdc, 18~75 Vdc)
- single output from 12~48 Vdc
- 1,500 Vdc isolation
- over current, over temperature, over voltage, and short circuit protection
- remote on/off
- efficiency up to 87%

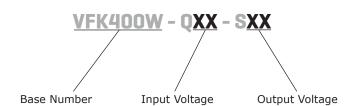




MODEL	input voltage	output voltage	output current	output power	ripple and noise¹	efficiency
	range (Vdc)	(Vdc)	max (A)	max (W)	max (mVp-p)	max (%)
VFK400W-Q24-S12	10~36	12	33.3	400	200	87
VFK400W-Q24-S24	10~36	24	16.7	400	240	86
VFK400W-Q24-S48	10~36	48	8.3	398	480	86
VFK400W-Q48-S12 ²	20~75	12	33.3	400	200	87
VFK400W-Q48-S24	18~75	24	16.7	400	240	86
VFK400W-Q48-S48	18~75	48	8.3	398	480	86.5

Note:

PART NUMBER KEY



^{1.} Ripple and noise are measured at full load, 20 MHz BW with $10\mu F$ tantalum capacitor and $1\mu F$ ceramic capacitor across the output. The 48 Vdc output models require a 22 μF aluminum capacitor and a $1\mu F$ ceramic capacitor across the output.

^{2.} An external input capacitor of 470uF is recommended to reduce input ripple voltage.

INPUT

conditions/d	escription	min	typ	max	units
24 Vdc input		10	24	36	Vdc
48 Vdc input	12 Vdc output model 24/48 Vdc output models	20 18	48 48	75 75	Vdc Vdc
24 Vdc input	power up power down		9.5 8.5		Vdc Vdc
48 Vdc input	power up power down		17.8 15.5		Vdc Vdc
nacitiva lacia	models ON (>3.5 Vdc or open circuit)				
positive logic	models OFF (0~1.2 Vdc)				
pi filter					
	24 Vdc input 48 Vdc input 24 Vdc input 48 Vdc input 48 Vdc input positive logic	48 Vdc input 12 Vdc output model 24/48 Vdc output models 24 Vdc input power up power down 48 Vdc input power down positive logic models ON (>3.5 Vdc or open circuit) models OFF (0~1.2 Vdc)	24 Vdc input 12 Vdc output model 24/48 Vdc output models 24 Vdc input 24 Vdc input 24 Vdc input 25 power up power down 26 Vdc input 27 power up power down 28 Vdc input 29 power up power down 29 power up power down 20 power up power up power down 20 power up power up power down 20 power up power up power down	24 Vdc input 10 24 48 Vdc input 12 Vdc output model 24/48 Vdc output models 20 48 48 24 Vdc input 24/48 Vdc output models 18 48 24 Vdc input 29.5 8.5 48 Vdc input 48.5 48.5 48 Vdc input 50 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	24 Vdc input 10 24 36 48 Vdc input 12 Vdc output model 24/48 Vdc output models 18 20 48 75 24 Vdc input power up power down 8.5 9.5 48 Vdc input power up power down 15.5 positive logic models ON (>3.5 Vdc or open circuit) models OFF (0~1.2 Vdc)

Note: 1. Open collector refer to -Vin

OUTPUT

parameter	conditions/description	min	typ	max	units
maximum output capacitance	for all models			2,200	μF
line regulation	measured from high line to low line			±1	%
load regulation	measured from full load to zero load			±1	%
voltage accuracy				±1.5	%
adjustability		90		105	%
switching frequency			250		kHz
transient response	25% step load change			500	μs
temperature coefficient			±0.03		%/°C

PROTECTIONS

parameter	conditions/description	min	typ	max	units
short circuit protection	continuous				
over current protection	% nominal output current	110		150	%
over voltage protection		115		140	%
over temperature protection	shutdown		110		°C

SAFETY AND COMPLIANCE

parameter	conditions/description	min	typ	max	units
isolation voltage	for 1 minute: input to output; input to case; output to case	1,500			Vdc
isolation resistance		10			ΜΩ
RoHS	2011/65/EU (CE)				

ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curves	-40		85	°C
storage temperature		-55		105	°C

MECHANICAL

parameter	conditions/description	min	typ	max	units
dimensions	198.90 x 127.00 x 38.93 (7.831 x 5.000 x 1.533 inch)				mm
case material	steel and aluminum extrusion				
weight			1.18		kg

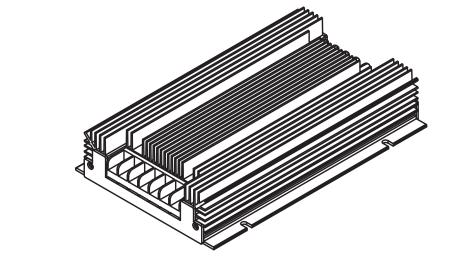
MECHANICAL DRAWING

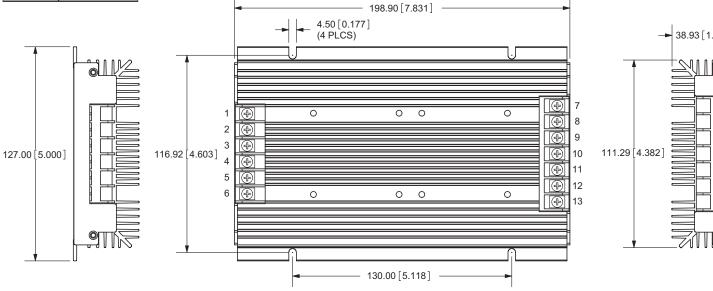
units: mm[inch]

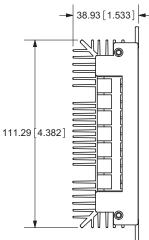
tolerance: $X.X = \pm 0.5[\pm 0.02]$ $X.XX = \pm 0.25[\pm 0.010]$

wire range: 22~12 AWG screw size: #6-32

PIN CO	PIN CONNECTIONS				
PIN	FUNCTION				
1, 2	+Vin				
3, 4	-Vin				
5	on/off				
6	case				
7, 8	+Vout				
9	+S				
10	trim				
11	-S				
12, 13	-Vout				

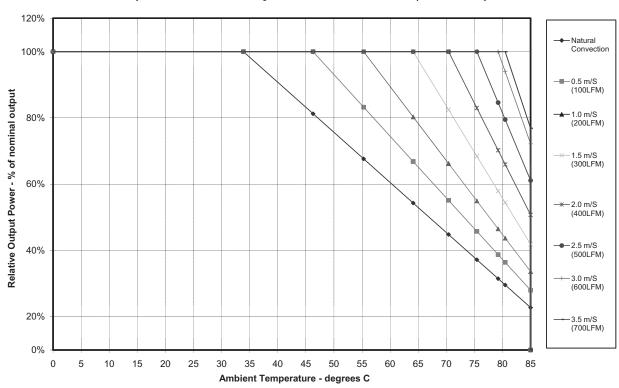




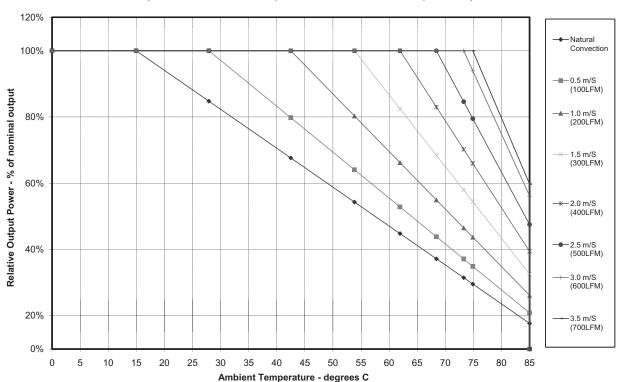


DERATING CURVES

VFK400W Power Derating Curves At Nominal Input (Includes VFK400W-Q24-S12 and all 48 Vdc input models)



VFK400W Power Derating Curves At Nominal Input (Includes VFK400W-Q24-S24 and VFK400W-Q24-S48)



APPLICATION NOTES

Output Voltage Trimming

Leave open if not used.

Figure 1 Application Circuit for Trim pin

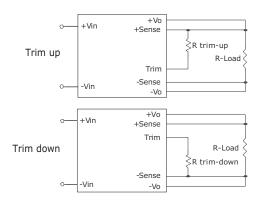


Table 1 Trim Up Resistor Values $(M\Omega)^1$

Desired Vout (%Vout) 101% 102% 103% 104% 105% Nom. Vout (Vdc) 12 2.2 0.82 0.68 1.6 1.2 24 4.3 3.3 2.2 1.6 1.5 48 10 6.8 4.8 3.9 3.5

Table 2 Trim Down Resistor Values ($K\Omega$)

Desired Vout (%Vout) Nom. Vout (Vdc)		92%	94%	96%	98%
12	9	12	22	51	100
24	12	22	51	100	300
48	22	32	49	100	300

Note:

1. VFK400W-Q48-S12 model requires minimum input voltage of 21.6 Vdc in order to trim between $100 \sim 105\%$.

REVISION HISTORY

rev.	description	date
1.0	initial release	03/13/2012
1.01	updated adjustability range	09/20/2012
1.02	corrected weight	12/18/2012
1.03	updated spec	04/01/2013
1.04	added trimming information	01/03/2014

The revision history provided is for informational purposes only and is believed to be accurate.



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