

Sealed Choke Coil PST031B type

■ Features

Low profile : 3.0mm x 3.0mm x 1.2mm

Low coil resistance with large currents.

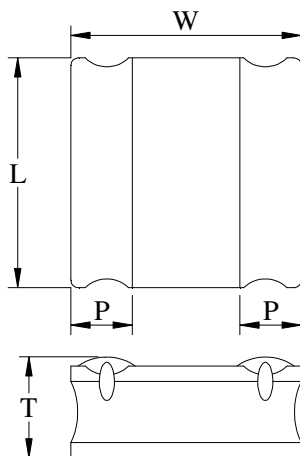
High magnetic shield construction should actualize high resolution for EMC protection.

100% lead (Pb) free meet RoHS standard

■ Application

Cellular phones, LCD displays, HDDs, DVCs, DSCs, PDAs etc..

■ Outline Dimensions

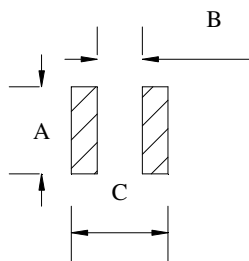


Code	Dimensions (mm)
L	3.0 ± 0.1
W	3.0 ± 0.1
T	1.2 Max
P	0.75 ± 0.2

Note : This graph is in regard to outline dimensions spec. For outer appearance, please refer to actual product.

■ Recommend Land Pattern Dimensions

The customer shall determine the land dimensions shown above after confirming and safety.



A	2.7 ~ 2.9
B	1.2 ~ 1.4
C	3.0

Unit : mm

■ Specifications

Part Number	L0 Inductance (μ H) @ (0A)	R _{dc} (m Ω)		Heat Rating Current DC Amps. Idc (A)		Saturation Current DC Amps. Isat (A)	
		Typical	Maximum	Typical	Maximum	Typical	Maximum
PST031B-R47MS	0.47	36	44	3.40	3.06	3.80	3.42
PST031B-1R0MS	1.0	52	63	2.80	2.52	3.40	3.06
PST031B-1R5MS	1.5	60	72	2.50	2.25	3.10	2.60
PST031B-2R2MS	2.2	85	102	2.20	1.98	2.90	2.40
PST031B-3R3MS	3.3	134	161	1.71	1.53	1.92	1.72
PST031B-4R7MS	4.7	184	221	1.43	1.28	1.71	1.53
PST031B-5R6MS	5.6	231	277	1.26	1.14	1.55	1.39
PST031B-6R8MS	6.8	256	307	1.25	1.13	1.49	1.24
PST031B-8R2MS	8.2	337	405	1.05	0.95	1.32	1.20
PST031B-100MS	10.0	397	476	1.00	0.90	1.26	1.05
PST031B-150MS	15.0	572	686	0.80	0.72	1.10	0.83
PST031B-220MS	22.0	854	1,025	0.60	0.54	0.86	0.72
PST031B-330MS	33.0	1,587	1,904	0.40	0.36	0.48	0.43
PST031B-470MS	47.0	2,246	2,695	0.36	0.32	0.36	0.32

* : If you require another part number please contact with us.

** : Inductance Tolerance \pm 20%

Note 1. : All test data is referenced to 25 $^{\circ}$ C ambient.

Note 2. : Test Condition: 1MHz, 1.0Vrms

Note 3. : Idc : DC current (A) that will cause an approximate Δ T of 40 $^{\circ}$ C

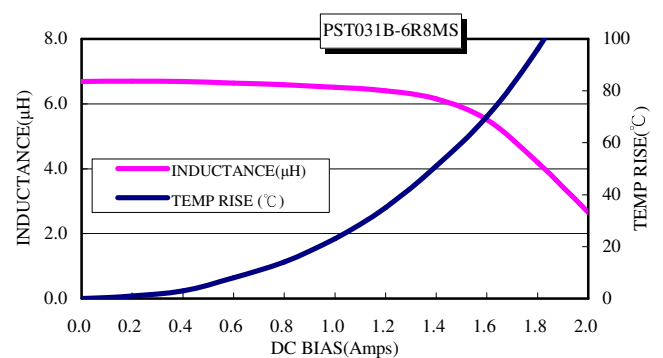
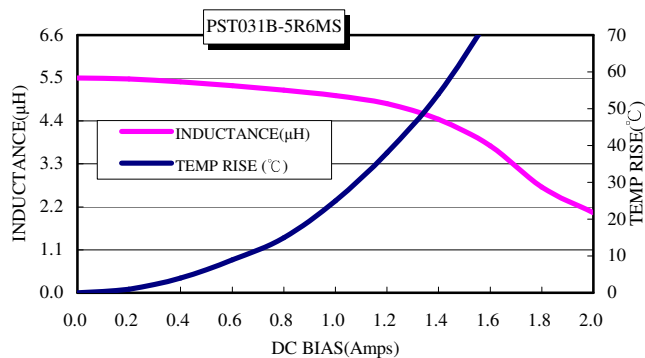
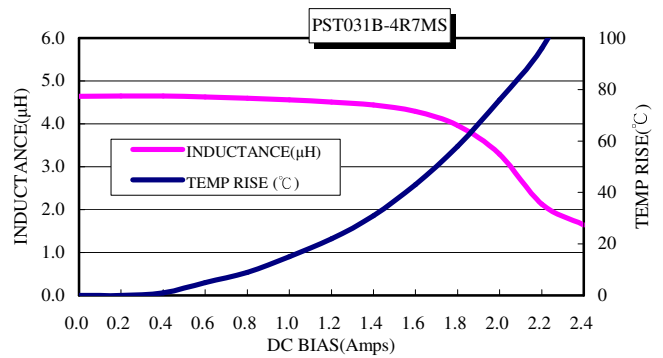
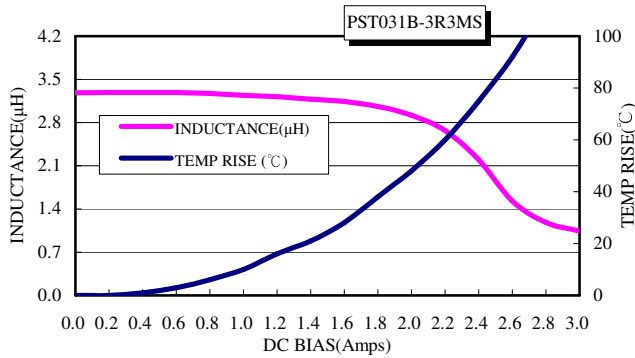
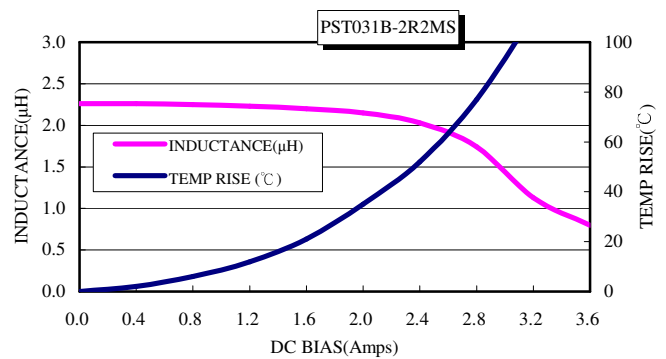
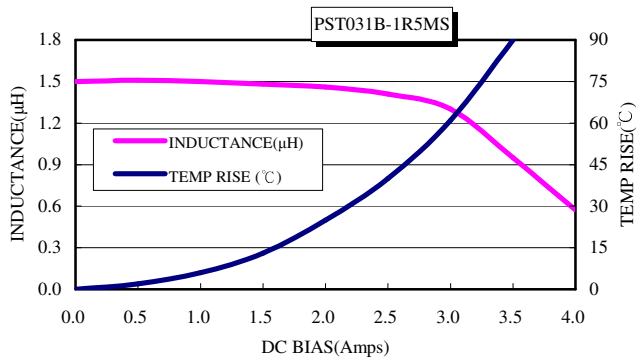
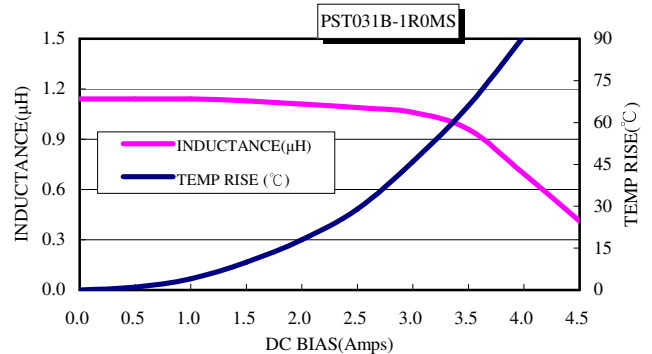
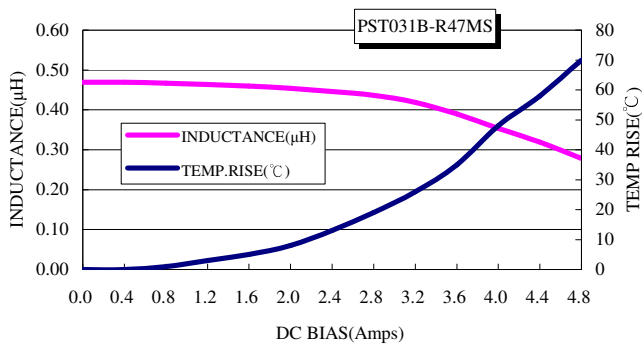
Note 4. : Isat : DC current (A) that will cause Lo to drop approximately 30%

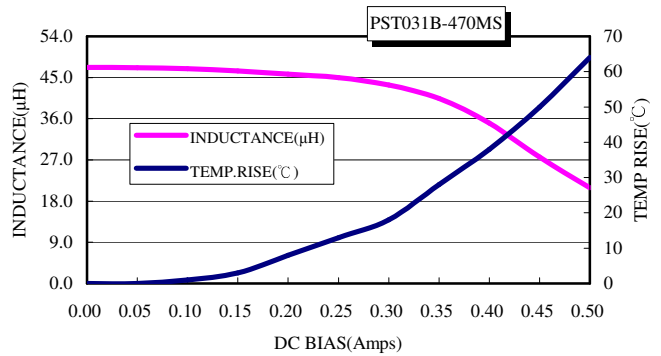
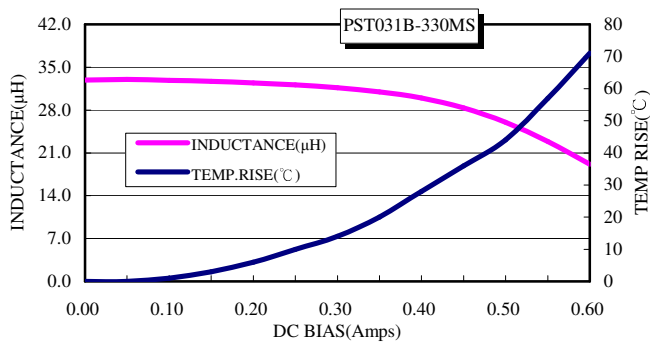
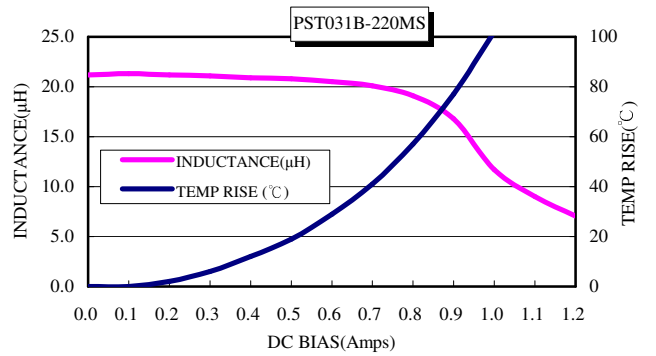
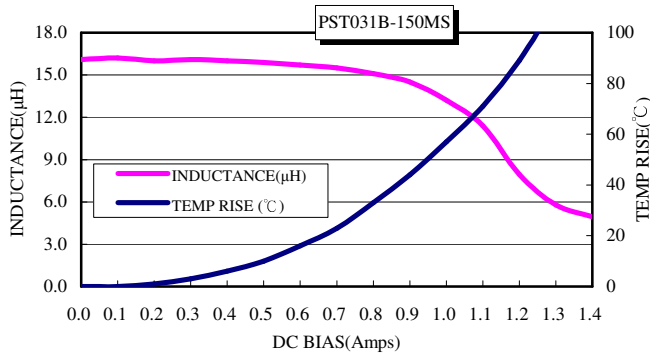
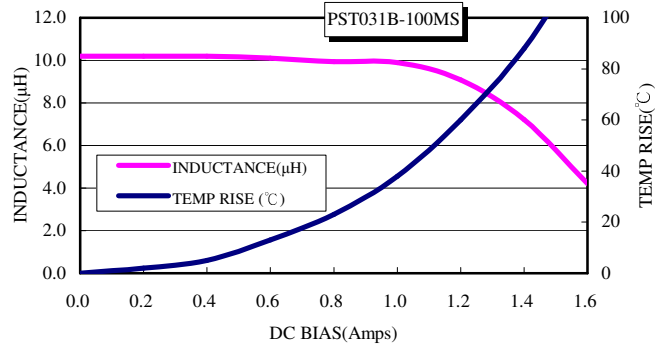
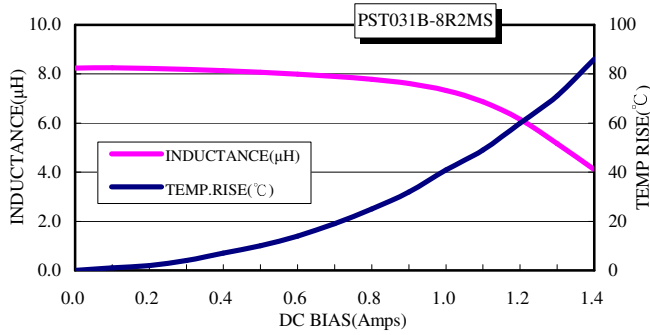
Note 5. : Operating Temperature Range -55 $^{\circ}$ C to + 125 $^{\circ}$ C

Note 6. : The part temperature (ambient + temp rise) should not exceed 125 $^{\circ}$ C under worse case operating conditions. Circuit design , component placement, PWB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.

Note 7. : The rated current as listed is either the saturation current or the heating current depending on which value is lower.

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