

CY8C4127AZS-M485

Status: **In Production**

Product Specs	Pricing & Inventory	Packaging/Ordering	Quality and RoHS	Technical Documents
CY8C4127AZS-M485				DESIGN SUPPORT
Automotive Qualified	Y			Development Kits Forums Knowledge Base Packaging Product Brochures Quality & Reliability Samples Technical Support Technical Training Third Party Network (Cypress)
CPU Core	ARM Cortex-M0			
CapSense	Y			
Dedicated ADC (Max Resolution @ Sample Rate)	SAR(1, 12-bit @ 806 ksp/s)			
EEPROM (KB)	0			
Flash (KB)	128			
JTAG and SI ID	0x11C4			
LCD Direct Drive	Y			
Max. Operating Frequency (MHz)	24			
Max. Operating Temp. (°C)	105			
Max. Operating Voltage (V)	5.50			
Min. Operating Temp. (°C)	-40			
Min. Operating Voltage (V)	1.71			
No. of CAN Controllers	0			
No. of Continuous Time Blocks	2			
No. of DMA Channels	8			
No. of Dedicated Comparators	2			
No. of Dedicated Timer/Counter/PWM Blocks	8			
No. of GPIOs	51			
No. of Op Amps	4			
No. of SIO	0			
No. of Serial Communication Blocks	4			
No. of USB IO	0			
No. of Universal Analog Blocks	0			
No. of Universal Digital Blocks	0			
PLL	N			
SRAM (KB)	16			
Series	PSOC 4100M			
Tape & Reel	N			
USB (Type)	None			

PRICING & INVENTORY AVAILABILITY					
1-9 unit Price*	10-24 unit Price*	25-99 unit Price*	100-249 unit Price*	250-999 unit Price*	1000+ unit Price*
Contact Sales					

Availability	Quantity	Ships In	Buy from Cypress	Buy from Distributors
Out of Stock	0	Please click here to check lead times	Contact Sales	Contact Sales

* Prices are shown in United States dollars and are for budgetary use only for volume of 1ku

PACKAGING/ORDERING	
Package	TQFP
No. of Pins	44
Package Dimensions	393 L x 1.4 H x 393 W (Mils)
Package Weight	338.95 (mgs)
Package Cross Section Drawing	Download
Package Carrier	TRAY
Standard Pack Quantity	160
Minimum Order Quantity (MOQ)	160
Order Increment	160
Estimated Lead Time (days)	91
HTS Code	8542.31.0001
ECCN	(A3)
ECCN Suball	3A991.A3

QUALITY AND ROHS	
Moisture Sensitivity Level (MSL)	3
Peak Reflow Temp. (°C)	260 (Cypress Reflow Profile)
RoHS Compliant	Y Print RoHS Certificate of Compliance
PB Free	Y
Lead/Ball Finish	Ni/Pd/Au
Marking	Cypress Marking Format

IPC 1752 Material Declaration
 IPC 1752 MATERIAL DECLARATION TQFP64 (10X10 mm) (A264)_ASEK_Au_WIRE_PURE_Sn_HENKEL 8340_HITACHI CEL9200THF *Last Update: Oct 30, 2019*

Device Qualification Reports
 FIT/MTBF, ESD (HBM/CDM) and Latch-up data available in the Device Qualification Report.
 QTP# 622707: AUTOMOTIVE PSOC 4100M and PSOC 4200M DEVICE FAMILY SRPHD-10P, FAB25 (AL BEOL INTERCONNECT).pdf *Last Update: Mar 06, 2019*

TECHNICAL DOCUMENTS
Application Notes (17)
AN8485B - PSOC4 Programming Using an External Microcontroller (HSEF) Jan 19, 2020
AN93637 - PSOC4 Sensorless Field-Oriented Control (FOC) Dec 30, 2019
AN82156 - PSOC4, PSOC 4, and PSOC SLP - Designing PSOC Creator™ Components With UDB Datapaths Oct 02, 2019
AN82250 - PSOC Creator - Implementing Programmable Logic Designs with Verilog Sep 22, 2019
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