

SOLUTIONS PRODUCTS DESIGN SUPPORT BUY & SAMPLE ABOUT CYPRESS







Product Specs	Pricing & Inventor	ry Packaging/Ordering	Quality and RoHS	Technical
Status: In Production				
C10C4247EWA W1404	Rolls			

Product Specs Prici	ng & inventory	Packaging/Ordering	Quality and RoHS	Technical Documen
CY8C4247LWA-M484				DESIGN SUPPORT
Automotive Qualified	Y			
CPU Core	ARM Cortex-M	0		Development Kits
CapSense	Y			Forums
Dedicated ADC (#Max Resolution @ Sample Rate)	SAR (1, 12-bit @	1 msps)		Knowledge Base
EEPROM (KB)	0			Packaging
Flash (KB)	128			Product Brochures
JTAG and Si ID	0x11A8			Quality & Reliability
LCD Direct Drive	Y			Samples
Max. Operating Frequency (MHz)	48			Technical Support
Max. Operating Temp. (°C)	85			Technical Training
Max. Operating Voltage (V)	5.50			-
Min. Operating Temp. (°C)	-40			Third Party Network (CYF
Min. Operating Voltage (V)	1.71			
No. of CAN Controllers	2			
No. of Continuous Time Blocks	2			TRAINING
No. of DMA Channels	8			
No. of Dedicated Comparators	2			Workshops
No. of Dedicated Timer/Counter/PWM Block	s 8			Training On-Demand
No. of GPIOs	48			Webinars
No. of Op Amps	4			
No. of SIO	0			
No. of Serial Communication Blocks	4			
No. of USB IO	0			
No. of Universal Analog Blocks	0			
No. of Universal Digital Blocks	4			
PLL	N			
SRAM (KB)	16			
Series	4200M			
Tape & Reel	N			
USB (Type)	None			

RICING & INV	ENTORY AVAILABI	LITY			
1-9 unit Price*	10-24 unit Price*	25-99 unit Price*	100-249 unit Price*	250-999 unit Price*	1000+ unit Price*
		Co	ntact Sales		

PACKAGING/ORDERING	
Package	QFN
No. of Pins	56
Package Dimensions	315 L x 39.4 H x 314 W (Mils)
Package Cross Section Drawing	Download
Package Carrier	TRAY
Standard Pack Quantity	260
Minimum Order Quantity (MOQ)	260
Order Increment	260
Estimated Lead Time (days)	91
HTS Code	8542.31.0001
ECCN	(A.3)
ECCN Suball	3A991.A.3

QUALITY AND ROHS		
Moisture Sensitivity Level (MSL)	3	
Peak Reflow Temp. (°C)	260 (Cypress Reflow Profile)	
RoHS Compliant	Y Print RoHS Certificate of Compliance	
PB Free	Υ	
Lead/Ball Finish	Ni/Pd/Au	
Marking	Cypress Marking Format	

TITATTBF, ESD (HBM/CDM) and Latch-up data available in the Device Qualification Report.

OTP9162707-AUTOMOTIVE PSC 4100M and PSoC 4200M DEVICE FAMILY SBPFHD-10P, FAB25 (AL BEOL INTERCONNECT), pdf Last Update: Mar 06, 2019

TECHNICAL DOCUMENTS

Application Notes (17)
ANABASS - PSoC(84 Programming Using an External Microcontroller (HSSP) Jan 19, 2020
ANABASS - PSoC(84 Sensorless Reid-Oriented Control (FOC) Det 30, 2019
ANABASS - PSoC 84 Sensorless Reid-Oriented Control (FOC) Det 30, 2019
ANABASS - PSoC 95, PSoC 4, and PSoC St P- Designing PSoC Creation** Components With UDB Datapaths Oct 02, 2019
ANABASS - PSoC Creator - Implementing Programmable Logic Designs with Verilog Sep 22, 2019
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RB00AM - 500PM (local time)

Create a MyCase 430AM - 130PM (pacific time)

Cypress Developer Community

Time)

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Report a Security Vulnerability

Time)









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MB96F001YBPMC1-GSE1 MB9BF121KPMC-G-JNE2 VA10800-D000003PCA CP8547AT CY9AF156NPMC-G-JNE2
MB9BF104NAPMC-G-JNE1 NHS3152UK/A1Z MK26FN2M0CAC18R EFM32TG230F32-D-QFN64 EFM32TG232F32-D-QFP64
EFM32TG825F32-D-BGA48 MB9AFB44NBBGL-GE1 MB9BF304RBPMC-G-JNE2 MB9BF416RPMC-G-JNE2 MB9AF155MABGL-GE1
MB9BF306RBPMC-G-JNE2 MB9BF618TBGL-GE1 ATSAMS70N21A-CN LPC1754FBD80 STM32F030K6T6TR AT91M42800A-33AU
ATSAM3N0AA-MU ATSAM3N0CA-CU ATSAM3SD8BA-MU ATSAM4LC2BA-UUR ATSAM4LS2AA-MU ADuC7023BCPZ62I-R7
ATSAM4LS4CA-CFU ADUCM3027BCPZ-R7 ADUCM3027BCPZ-RL ADUCM3029BCPZ-R7 LPC2141FBD64.151 CEC1702Q-B1-SX
ADUC7020BCPZ62IRL7 TMS5701114CZWTQQ1 TM4C123AE6PMIR S6E2H14G0AGV20000 ATSAMD20J15B-AU ATSAMD20J16B-MN ATSAMV71N20B-CB