MB3793-34D/37D/40D/42D/45D

Power-Voltage Monitoring IC with Watchdog Timer

Description

The MB3793 is an integrated circuit to monitor power voltage; it incorporates a watchdog timer.
A reset signal is output when the power supply voltage is cut or falls instantaneously. When the power supply recovers normally after resetting, a power-on reset signal is output to monitor the power supply voltage. A built-in watchdog timer with two inputs for system operation diagnosis can provide a fail-safe function for various application systems.

Model No.	Marking Code	Detection Voltage
MB3793-34D	3793DJ	3.4 V
MB3793-37D	3793DF	3.7 V
MB3793-40D	3793DC	4.0 V
MB3793-42D	3793DA	4.2 V
MB3793-45D	3793D7	4.5 V

Features

- Precise detection of power voltage fall: ±2.5%
- Detection voltage with hysteresis
- Built-in dual-input watchdog timer
- Watchdog timer halt function
- Independently-set watchdog and reset times
- Open drain output
- Package : SOP8

Applications

The MB3793 has various uses such as the amusement devices.

Online Design Simulation

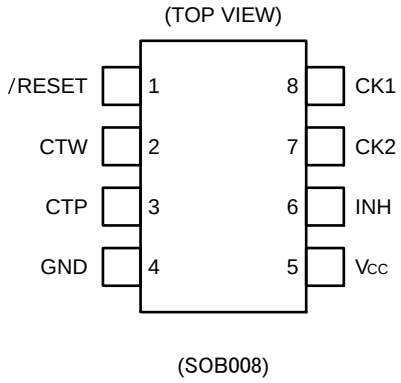
Easy DesignSim

This product supports the web-based design simulation tool. It can easily select external components and can display useful information. Please access from the following URL.

cypress.transim.com/login.aspx

Contents

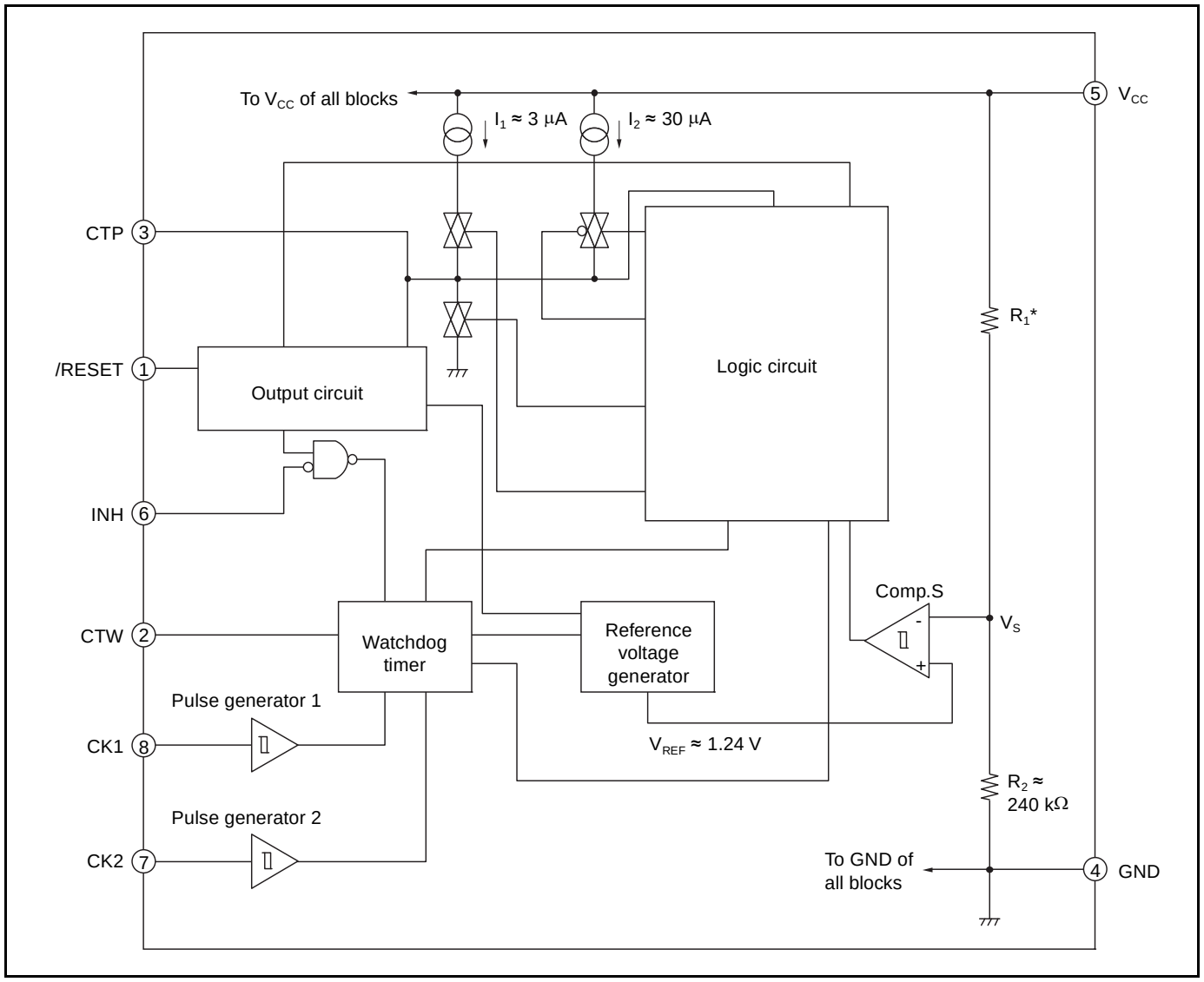
1. Pin Assignment



2. Pin Description

Pin No.	Symbol	Description
1	/RESET	Reset output pin (Open drain)
2	CTW	Watchdog timer monitoring time setting pin
3	CTP	Power-on reset hold time setting pin
4	GND	Ground pin
5	V_{CC}	Power supply pin
6	INH	This pin forces the watchdog timer on/off. When setting this pin to the High level, the watchdog timer is stopped.
7	CK2	Clock 2 input pin
8	CK1	Clock 1 input pin

3. Block Diagram



*: See the following table.

Model No.	Resistance Value (R_1)
MB3793-34D	435 kΩ
MB3793-37D	494 kΩ
MB3793-40D	553 kΩ
MB3793-42D	590 kΩ
MB3793-45D	650 kΩ

4. Block Functions

Comp.S

Comp.S is a comparator with hysteresis to compare the reference voltage with a voltage (V_S) that is the result of dividing the power supply voltage (V_{CC}) by resistors R_1 and R_2. When V_S falls below 1.24 V, a reset signal is output. This function enables the MB3793 to detect an abnormality within 1 μs when the power supply is cut or falls instantaneously.

Output Circuit

The output circuit has a comparator to control the reset signal (/RESET) output. When the voltage at the CTP pin for setting the power-on reset hold time exceeds the threshold voltage, resetting is canceled.

Pulse Generator

The pulse generator generates pulses when the voltage at the CK1 and CK2 input clock pins changes from Low level to High level (positive-edge trigger) and exceeds the threshold voltage; it sends the clock signal to the watchdog timer.

Watchdog Timer

The watchdog timer can monitor two clock pulses. Short-circuit the CK1 and CK2 clock pins to monitor a single clock pulse.

Logic Circuit

The logic circuit controls charging and discharging of the power-on reset hold time setting capacity (C_{TP}) on a signal of Comp.S and Watchdog timer.

5. Absolute Maximum Ratings

Parameter		Symbol	Conditions	Rating		Unit
				Min	Max	
Power supply voltage (*1)		V_{CC}	—	−0.3	+7.0	V
/RESET pin voltage		$V_{/RESET}$	—	−0.3	+7.0	V
Input voltage(*1)	CK1	V_{CK1}	—	−0.3	V_{CC} + 0.3 (≤ +7)	V
	CK2	V_{CK2}	—			
	INH	V_{INH}	—			
Reset output voltage(*1)	/RESET	V_{OL}, V_{OH} (*2)	—	−0.3	+7.0	V
Reset output current		I_{OL}	—	0	+10	mA
Power dissipation		P_D	Ta ≤ +85 °C	—	200	mW
Storage temperature		Tstg	—	−55	+125	°C

*1: The voltage is based on the ground voltage (0 V).

*2: The reset output voltage V_{OH} is the applied voltage to the pull-up resistor.

WARNING:

1. Semiconductor devices may be permanently damaged by application of stress (including, without limitation, voltage, current or temperature) in excess of absolute maximum ratings. Do not exceed any of these ratings.

6. Recommended Operating Conditions

Parameter	Symbol	Conditions	Value			Unit
			Min	Typ	Max	
Power supply voltage	V_{CC}	—	1.2	5.0	6.0	V
/RESET pin voltage	$V_{/RESET}$	—	0	—	6.0	V
Reset (/RESET) output current	I_{OL}	—	0	—	+ 5	mA
Power-on reset hold time setting capacity	C_{TP}	—	0.001	0.1	10	µF
Watchdog timer monitoring time setting capacity (*1)	C_{TW}	—	0.001	0.01	1	µF
Operating ambient temperature	Ta	—	−40	+25	+85	°C

*1: The watchdog timer monitor time range depends on the rating of the setting capacitor.

WARNING:

1. The recommended operating conditions are required in order to ensure the normal operation of the semiconductor device. All of the device's electrical characteristics are warranted when the device is operated under these conditions.
2. Any use of semiconductor devices will be under their recommended operating condition.
3. Operation under any conditions other than these conditions may adversely affect reliability of device and could result in device failure.
4. No warranty is made with respect to any use, operating conditions or combinations not represented on this data sheet. If you are considering application under any conditions other than listed herein, please contact sales representatives beforehand.

7. Electrical Characteristics

7.1 DC Characteristics

(V_{CC} = +5 V, Ta = +25°C)

Parameter		Symbol	Conditions		Value			Unit
					Min	Typ	Max	
Power supply current	MB3793-34D	I_{CC1}	After exit from reset		—	38	50	µA
	MB3793-37D				—	30	50	µA
	MB3793-40D				—	33	48	µA
	MB3793-42D				—	27	50	µA
	MB3793-45D				—	31	45	µA
Detection voltage	MB3793-34D	V_{SL}	V_{CC} falling	Ta = + 25 °C	3.32	3.40	3.48	V
				Ta = – 40 °C to + 85 °C	3.27 (*1)	3.40	3.53 (*1)	V
		V_{SH}	V_{CC} rising	Ta = + 25 °C	3.40	3.48	3.56	V
				Ta = – 40 °C to + 85 °C	3.35 (*1)	3.48	3.61 (*1)	V
	MB3793-37D	V_{SL}	V_{CC} falling	Ta = + 25 °C	3.60	3.70	3.80	V
				Ta = – 40 °C to + 85 °C	3.55 (*1)	3.70	3.85 (*1)	V
		V_{SH}	V_{CC} rising	Ta = + 25 °C	3.69	3.79	3.89	V
				Ta = – 40 °C to + 85 °C	3.64 (*1)	3.79	3.94 (*1)	V
	MB3793-40D	V_{SL}	V_{CC} falling	Ta = + 25 °C	3.90	4.00	4.10	V
				Ta = – 40 °C to + 85 °C	3.85 (*1)	4.00	4.15 (*1)	V
		V_{SH}	V_{CC} rising	Ta = + 25 °C	3.99	4.09	4.19	V
				Ta = – 40 °C to + 85 °C	3.94 (*1)	4.09	4.24 (*1)	V
	MB3793-42D	V_{SL}	V_{CC} falling	Ta = + 25 °C	4.10	4.20	4.30	V
				Ta = – 40 °C to + 85 °C	4.05 (*1)	4.20	4.35 (*1)	V
		V_{SH}	V_{CC} rising	Ta = + 25 °C	4.20	4.30	4.40	V
				Ta = – 40 °C to + 85 °C	4.15 (*1)	4.30	4.45 (*1)	V
	MB3793-45D	V_{SL}	V_{CC} falling	Ta = + 25 °C	4.40	4.50	4.60	V
				Ta = – 40 °C to + 85 °C	4.35 (*1)	4.50	4.65 (*1)	V
		V_{SH}	V_{CC} rising	Ta = + 25 °C	4.50	4.60	4.70	V
				Ta = – 40 °C to + 85 °C	4.45 (*1)	4.60	4.75 (*1)	V
Detection voltage hysteresis width	MB3793-34D	V_{SHYS}	$V_{SH} – V_{SL}$		35	80	120	mV
	MB3793-37D				40	85	130	mV
	MB3793-40D				50	95	140	mV
	MB3793-42D				50	100	150	mV
	MB3793-45D				50	100	150	mV
CK input threshold voltage		V_{CIH}	—		1.4(*1)	1.9	2.5	V
		V_{CIL}	—		0.8	1.3	1.8(*1)	V
CK input hysteresis width		V_{CHYS}	—		0.4(*1)	0.6	0.8(*1)	V
INH input voltage		V_{IIH}	—		3.5	—	V_{CC}	V
		V_{IIL}	—		0	—	0.8	V

(V_{CC} = +5 V, Ta = +25°C)

Parameter	Symbol	Conditions	Value			Unit
			Min	Typ	Max	
Logic input current (CK1, CK2, INH)	I_{IH}	$V_{IH} = V_{CC}$	—	0	1.0	µA
	I_{IL}	VI_L = 0 V	-1.0	0	—	µA
Reset output voltage	V_{OL}	$I_{/RESET}$ = +5 mA	—	0.12	0.40	V
Reset output minimum	V_{CCL}	$I_{/RESET}$ = +50 µA	—	0.8	1.2	V
Cut off current	Ioff	$V_{/RESET}$ = 6.0 V	—	—	1	µA

*1: This parameter is guaranteed by design, which is not supported by a final test.

7.2 AC Characteristics

(V_{CC} = +5 V, Ta = +25°C)

Parameter	Symbol	Conditions	Value			Unit
			Min	Typ	Max	
Power-on reset hold time	t_{PR}	C_{TP} = 0.1 µF	80	130	180	ms
Watchdog timer monitoring time	t_{WD}	C_{TW} = 0.01 µF,	7.5	15.0	22.5	ms
Watchdog timer reset time	t_{WR}	C_{TP} = 0.1 µF	5	10	15	ms
CK input pulse width	t_{CKW}	—	500	—	—	ns
CK input pulse cycle	t_{CKT}	—	20	—	—	µs
Reset falling time	Tf (*1)	C_L = 50 pF,	—	—	500	ns

*1: The voltage range is 10% to 90% at testing the reset output transition time.

8. Timing Diagram

Figure 8-1 Basic Operation (Positive Clock Pulse)

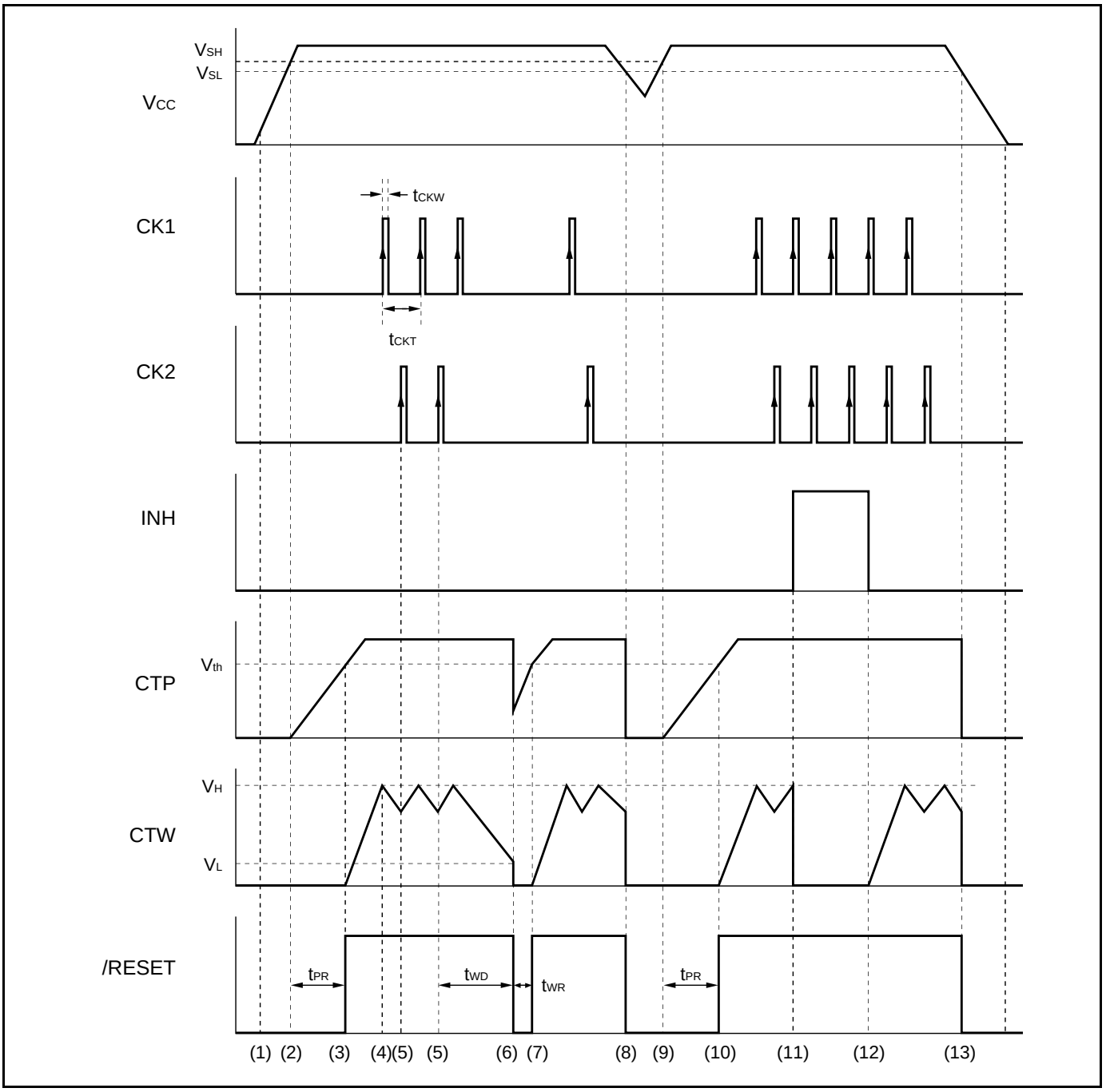


Figure 8-2 Basic Operation (Negative Clock Pulse)

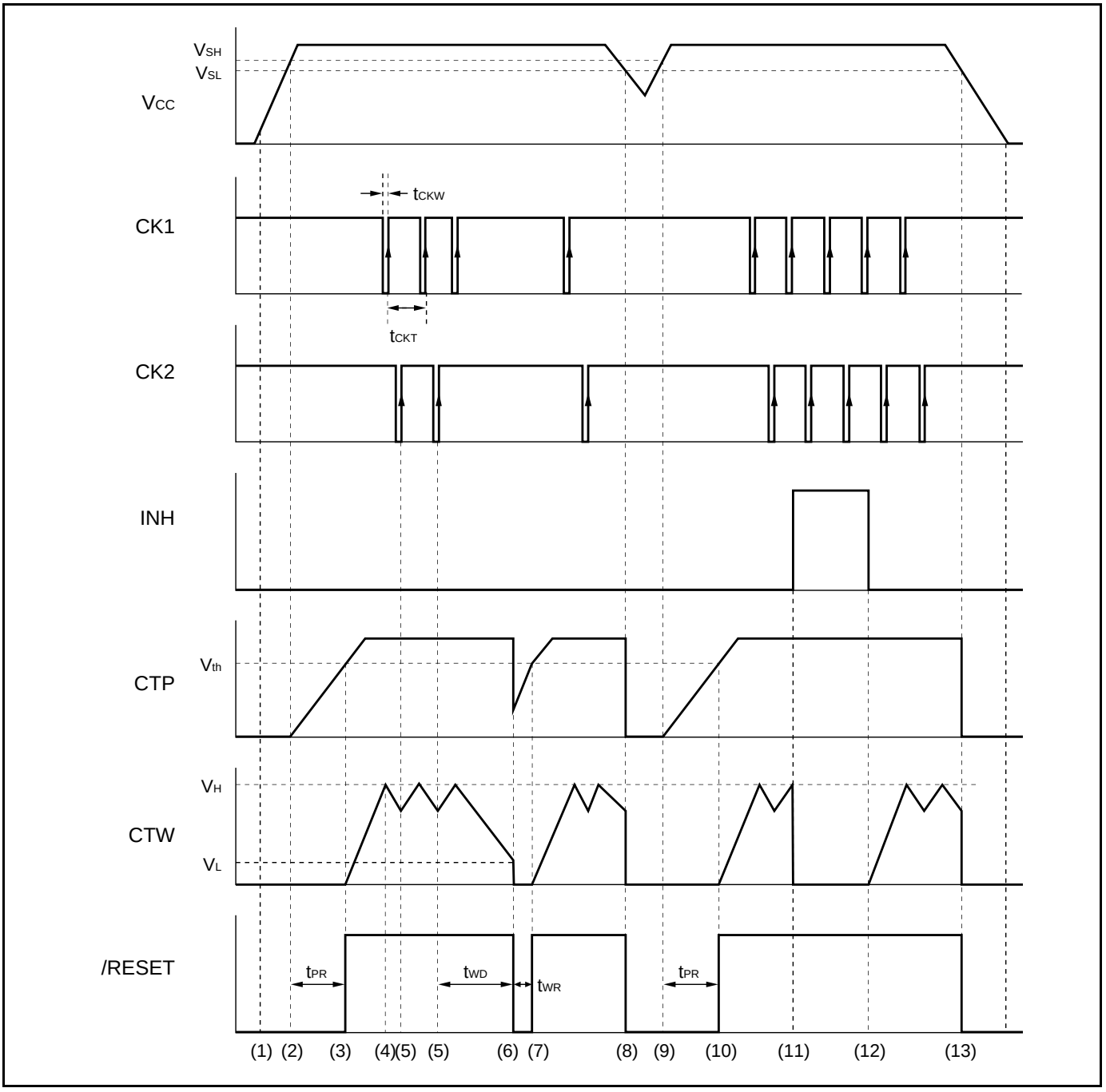
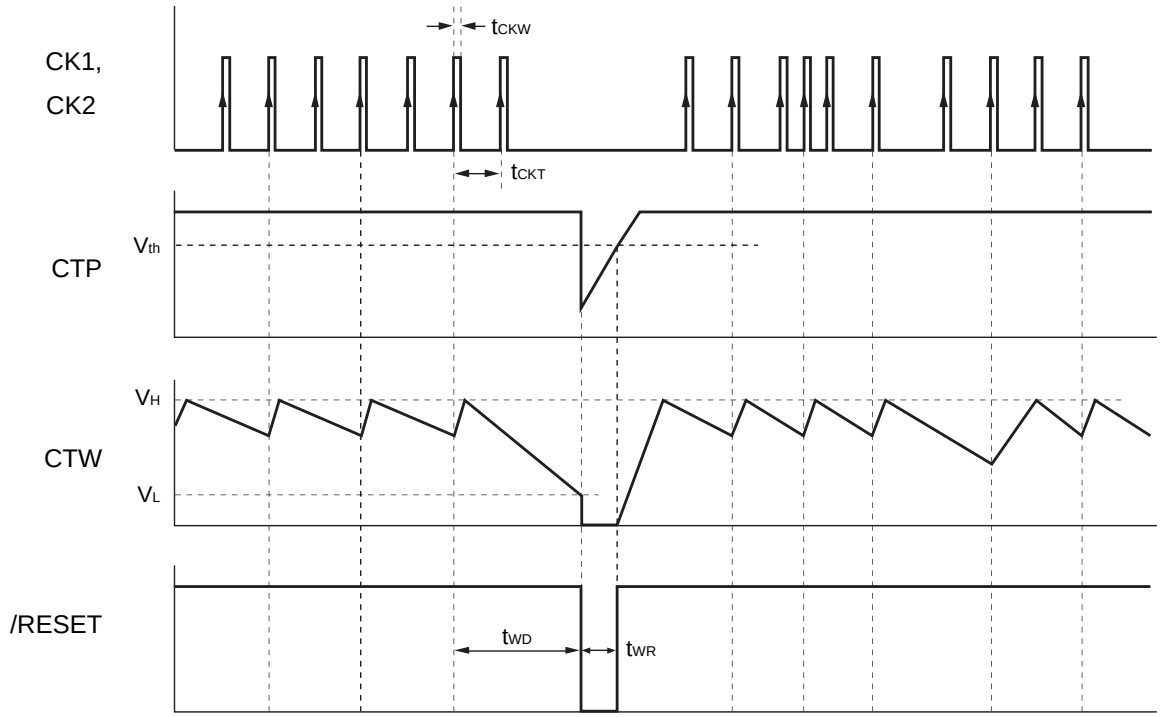


Figure 8-3 Single-clock Input Monitoring (Positive Clock Pulse)



Note : The MB3793 can monitor only one clock.
The MB3793 checks the clock signal at every other input pulse.
Therefore, set watchdog timer monitor time t_{WD} to the time that allows the MB3793 to monitor the period twice as long as the input clock pulse.

Figure 8-4 Inhibit Function Operation (Positive Clock Pulse)

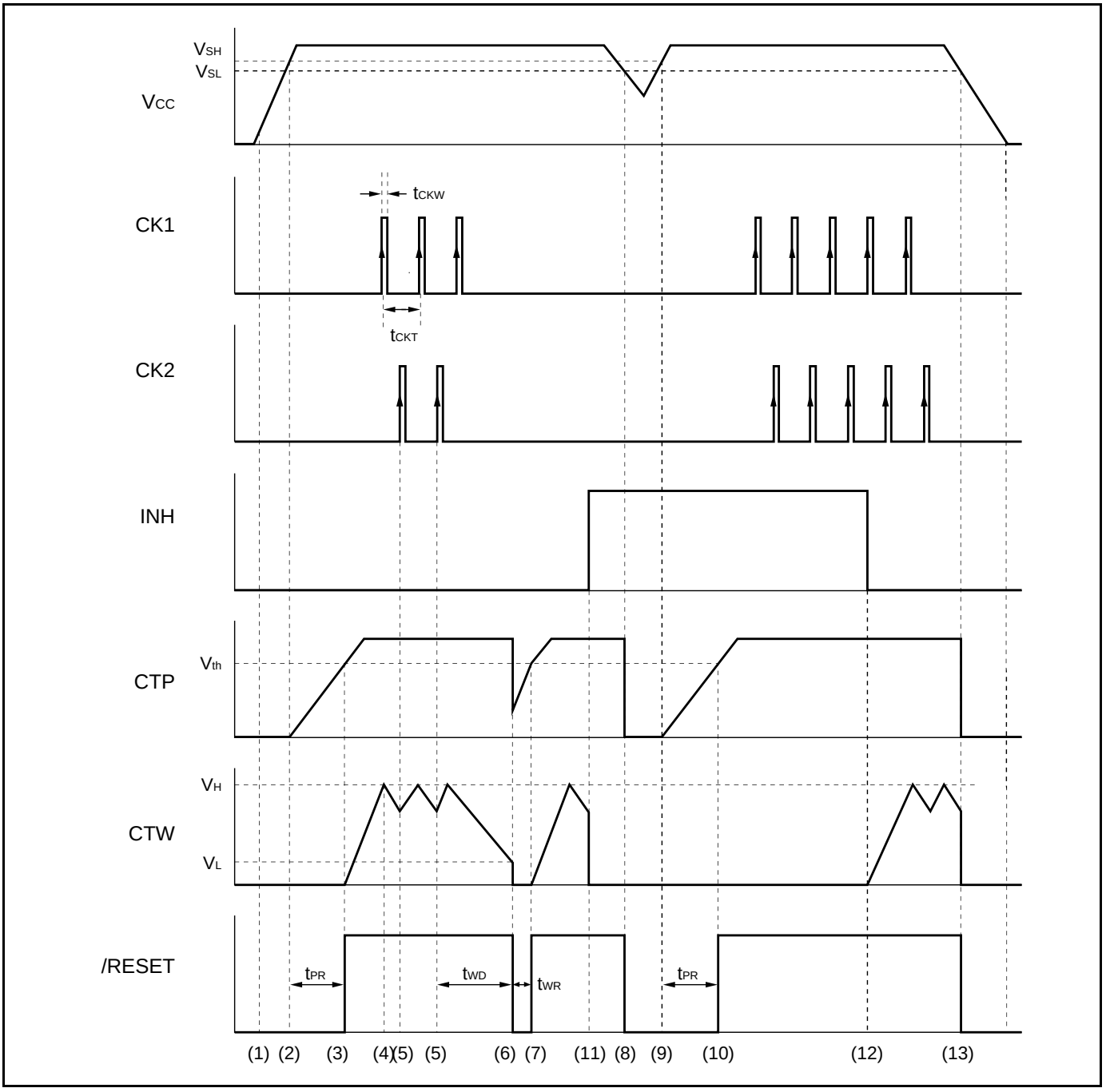
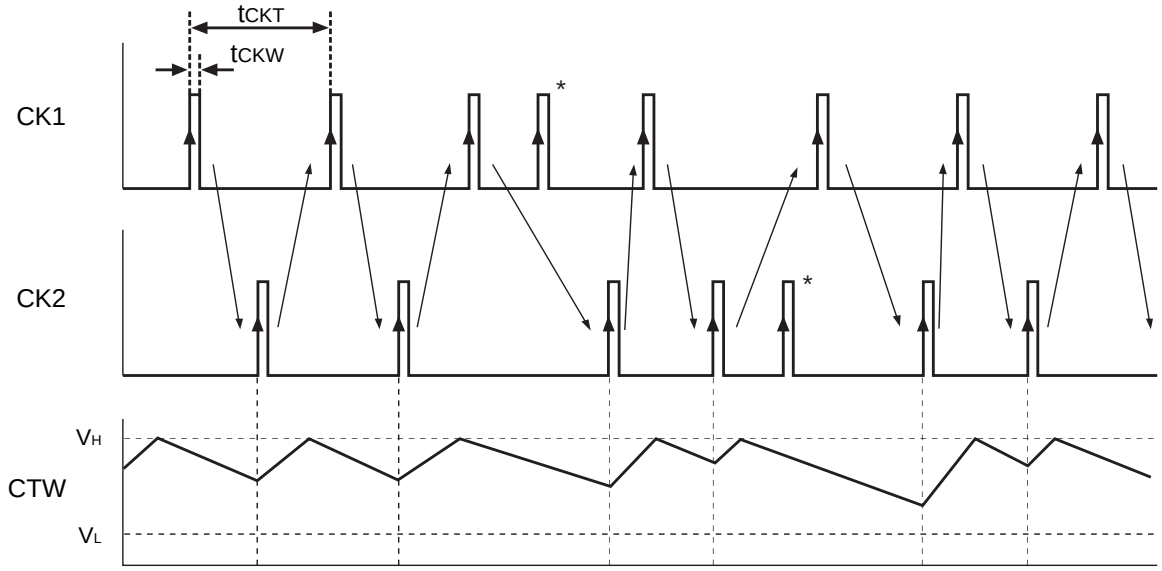


Figure 8-5 Clock Pulse Input Supplementation (Positive Clock Pulse)



Note : The MB3793 watchdog timer monitors Clock1 (CK1) and Clock2 (CK2) pulses alternately. When a CK2 pulse is detected after detecting a CK1 pulse, the monitoring time setting capacity (C_{TW}) switches to charging from discharging.
Therefore, the second and later pulses will be ignored even if only CK1 or CK2 pulses are input continuously like * (the * pulse is ignored in the example above).

9. Operation Sequence

Positive Clock Pulse Input

See “Figure 8-1 Basic Operation (Positive Clock Pulse)” under “8. Timing Diagram”.

Negative Clock Pulse Input

See “Figure 8-2 Basic Operation (Negative Clock Pulse)” under “8. Timing Diagram”.

The MB3793 operates in the same way whether it inputs positive or negative pulses.

Single-clock Input monitoring

To use the MB3793 while monitoring only one clock, connect clock pins CK1 and CK2.

Although the MB3793 operates basically in the same way as when monitoring two clocks, it monitors the clock signal at every other input pulse.

See “Figure 8-3 Single-clock Input Monitoring (Positive Clock Pulse)” under “8. Timing Diagram”.

Description of Operations

The numbers given to the following items correspond to numbers (1) to (13) used in “8. Timing Diagram”.

(1) The MB3793 outputs a reset signal when the power supply voltage (V_{CC}) reaches about 0.8 V (V_{CCL}).

(2) If V_{CC} reaches or exceeds the rise-time detected voltage V_{SH}, the MB3793 starts charging the power-on reset hold time setting capacitor C_{TP}. At this time, the output remains in a reset state.

(3) When C_{TP} has been charged for a certain period of time T_{PR} (until the CTP pin voltage exceeds the threshold voltage (V_{th}) after the start of charging), the MB3793 cancels the reset (setting the /RESET pin to “H” level from “L” level).
The V_{th} value is about 3.6 V with V_{CC} = 5.0 V

The power-on reset hold time t_{PR} is set with the following equation:

t_{PR} (ms) ≈ A × C_{TP} (μF)

The value of A is about 1300 with V_{CC} = 5.0 V. The MB3793 also starts charging the watchdog timer monitor time setting capacitor (C_{TW}).

(4) When the voltage at the watchdog timer monitor time setting pin CTW reaches the “H” level threshold voltage V_H, the C_{TW} switches from the charge state to the discharge state.
The value of V_H is always about 1.24 V regardless of the detected voltage.

(5) If the CK2 pin inputs a clock pulse (positive edge trigger) when the C_{TW} is being discharged in the CK1-CK2 order or simultaneously, the C_{TW} switches from the discharge state to the charge state.
The MB3793 repeats operations (4) and (5) as long as the CK1/CK2 pin inputs clock pulses with the system logic circuit operating normally.

(6) If no clock pulse is fed to the CK1 or CK2 pin within the watchdog timer monitor time t_{WD} due to some problem with the system logic circuit, the CTW pin is set to the “L” level threshold voltage V_L or less and the MB3793 outputs a reset signal (setting the /RESET pin to “L” level from “H” level).
The value of V_L is always about 0.24 V regardless of the detected voltage.
The watchdog timer monitor time t_{WD} is set with the following equation:

t_{WD} (ms) ≈ B × C_{TW} (μF) + C × C_{TP} (μF)

The value of B is hardly affected by the power supply voltage; it is about 1500 with V_{CC} = 5.0 V.

The value of C is 0.

For this reason:

t_{WD} (ms) ≈ B × C_{TW} (μF)

(7) When a certain period of time t_{WR} has passed (until the CTP pin voltage reaches or exceeds V_{th} again after recharging the C_{TP}), the MB3793 cancels the reset signal and starts operating the watchdog timer.
The watchdog timer monitor reset time t_{WR} is set with the following equation:

t_{WR} (ms) ≈ D × C_{TP} (μF)

The value of D is 100 with V_{CC} = 5.0 V.

The MB3793 repeats operations (4) and (5) as long as the CK1/CK2 pin inputs clock pulses. If no clock pulse is input, the MB3793 repeats operations (6) and (7).

(8) If V_{CC} is lowered to the fall-time detected voltage (V_{SL}) or less, the CTP pin voltage decreases and the MB3793 outputs a reset signal (setting the /RESET pin to "L" level from "H" level).

(9) When V_{CC} reaches or exceeds V_{SH} again, the MB3793 starts charging the C_{TP}.

(10) When the CTP pin voltage reaches or exceeds V_{th}, the MB3793 cancels the reset and restarts operating the watchdog timer. It repeats operations (4) and (5) as long as the CK1/CK2 pin inputs clock pulses.

(11) Making the Inhibit function active (setting the INH pin to "H" from "L") forces the watchdog timer to stop operation.
This stops only the watchdog timer, leaving the MB3793 monitoring V_{CC} (operations (8) to (10)).
The watchdog timer remains inactive unless the Inhibit function pin input is canceled.
The inhibit function (INH) pin must be connecting a voltage of lower as possible impedance, to evade noise.
Set the input pulse time width for Inhibit function (time of "L" level or "H" level) longer than the watchdog timer monitoring time (t_{WD}).

(12) Canceling the inhibit input (setting the INH pin to "L" from "H") restarts the watchdog timer.

(13) The reset signal is output when the power supply is turned off to set V_{CC} to V_{SL} or less.

1. Equation of time-setting capacitances (C_{TP} and C_{TW}) and set time

t_{PR} [ms] ≈ A × C_{TP} [μF]

t_{WD} [ms] ≈ B × C_{TW} [μF]

t_{WR} [ms] ≈ D × C_{TP} [μF]

Values of A, B and D

A	B	D	Remark
1300	1500	100	V_{CC} = 5.0 V

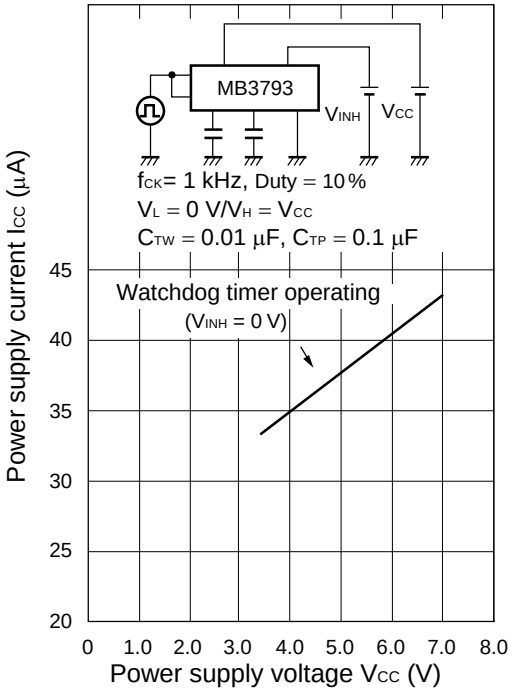
Note: The width of value of t_{PR}, t_{WD} and t_{WR} becomes the same ratio as width (Min, Max) of each specification value.

2. Example (when C_{TP} = 0.1 μF and C_{TW} = 0.01 μF)

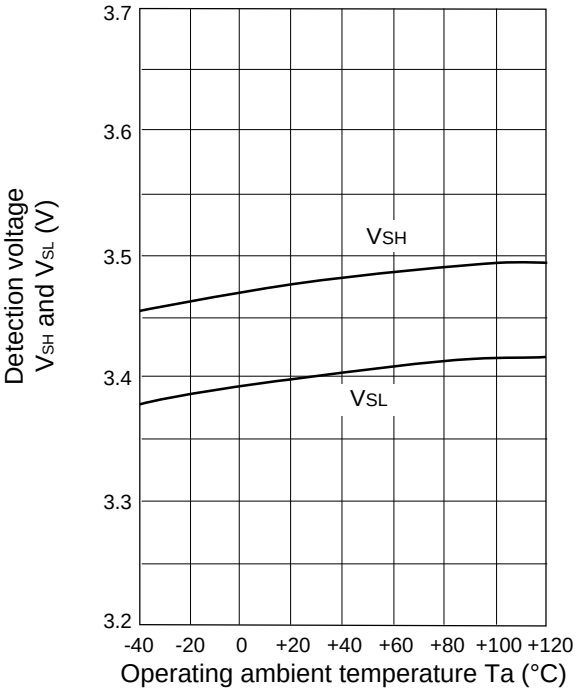
	Symbol	V_{CC} = 5.0 V
time (ms)	t_{PR}	130
	t_{WD}	15
	t_{WR}	10

10. Typical Characteristics

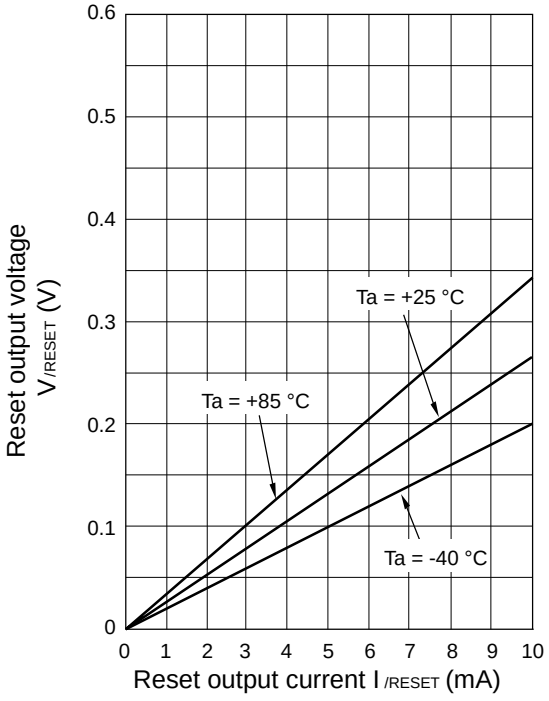
I_{CC} - V_{CC} characteristics (MB3793-34D)



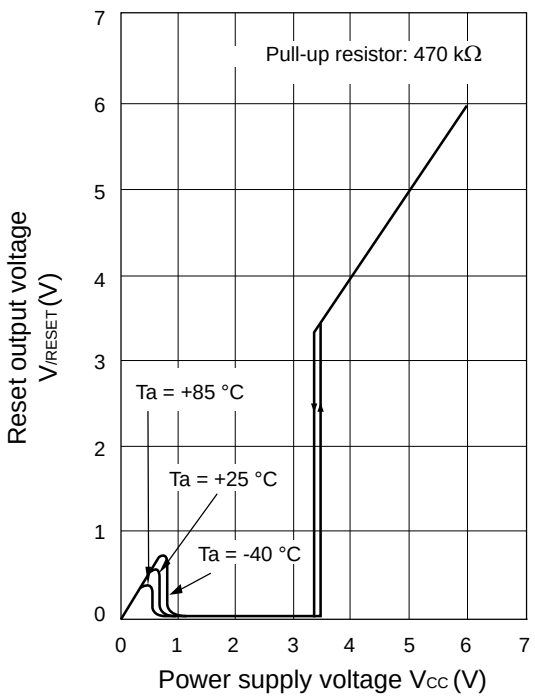
V_{SH}, V_{SL} - Ta characteristics (MB3793-34D)



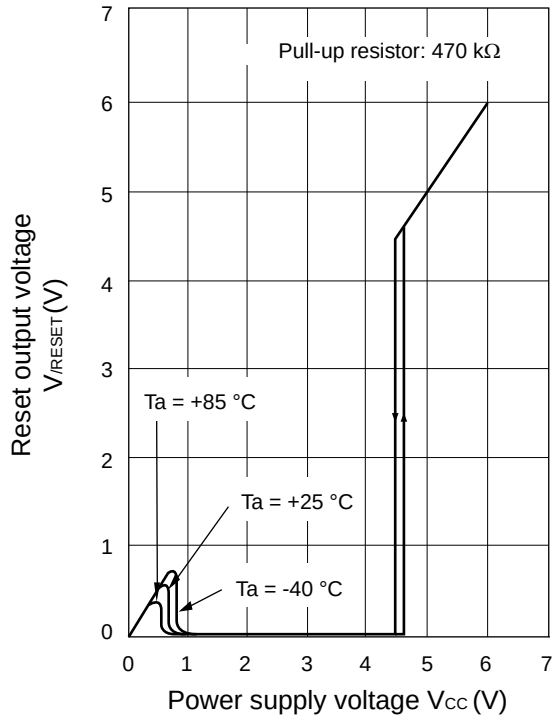
$V_{/RESET}$ - $I_{/RESET}$ characteristics (N-MOS side)



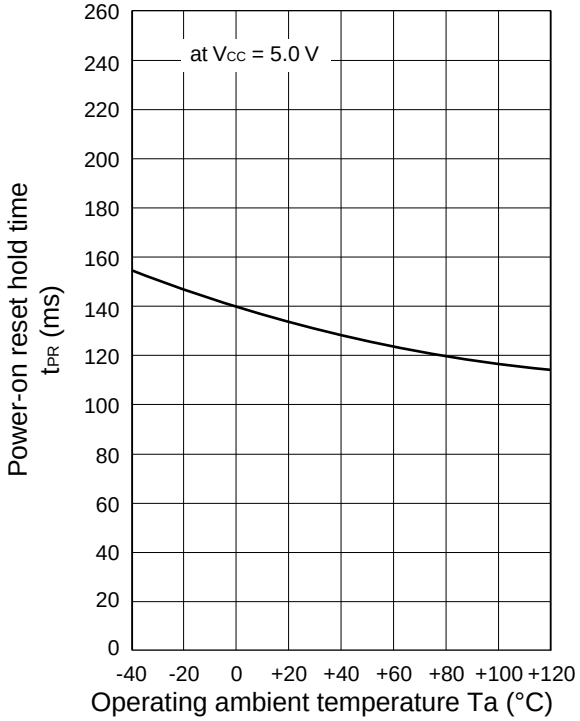
$V_{/RESET}$ - V_{CC} characteristics (MB3793-34D)



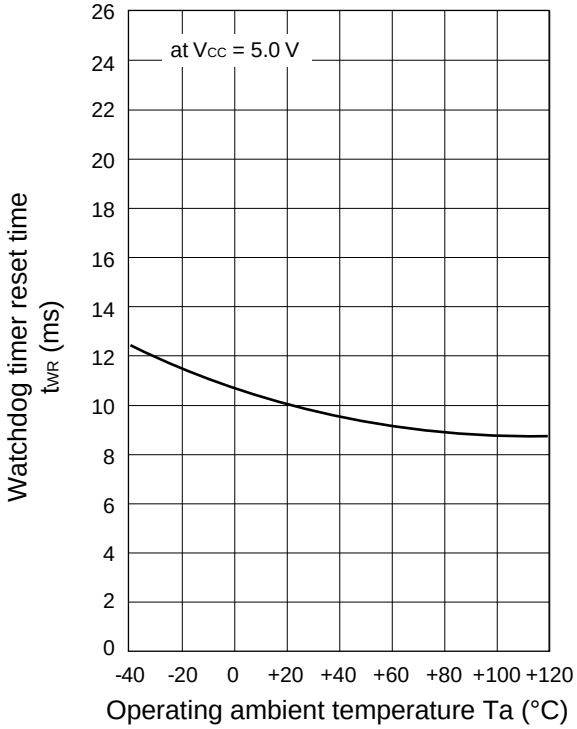
$V_{/RESET}$ - V_{CC} characteristics (MB3793-45D)



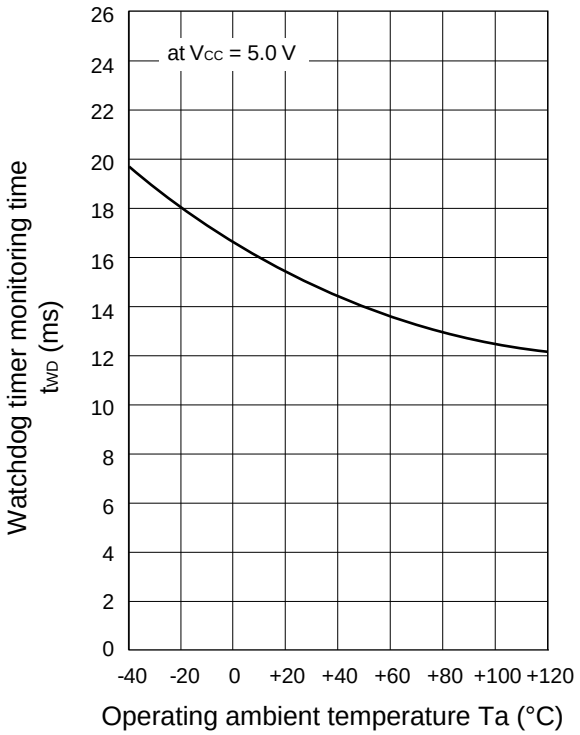
t_{PR} - Ta characteristics



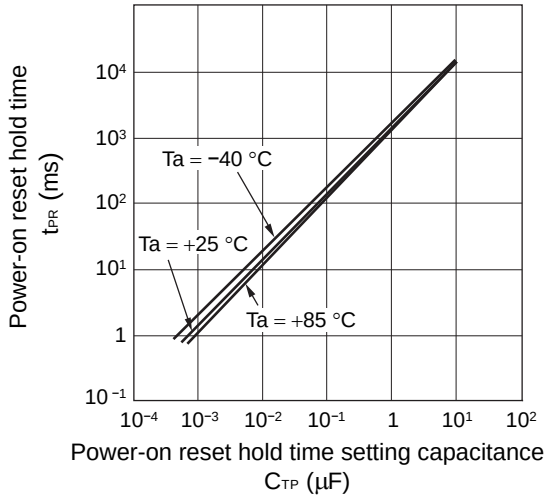
t_{WR} - Ta characteristics



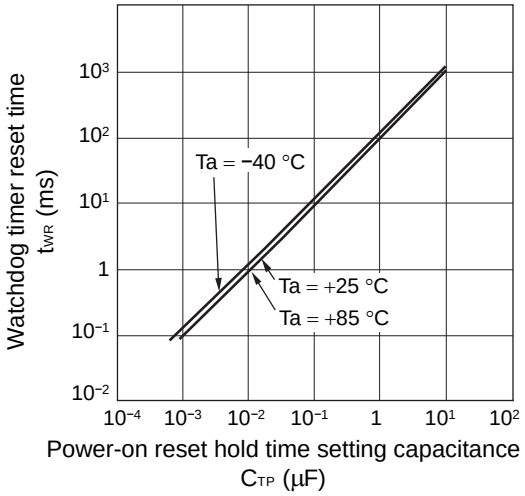
t_{WD} - Ta characteristics



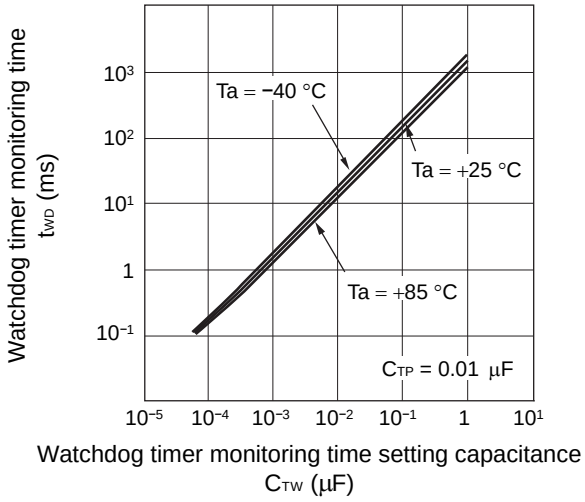
t_{PR} - C_{TP} characteristics



t_{WR} - C_{TP} characteristics



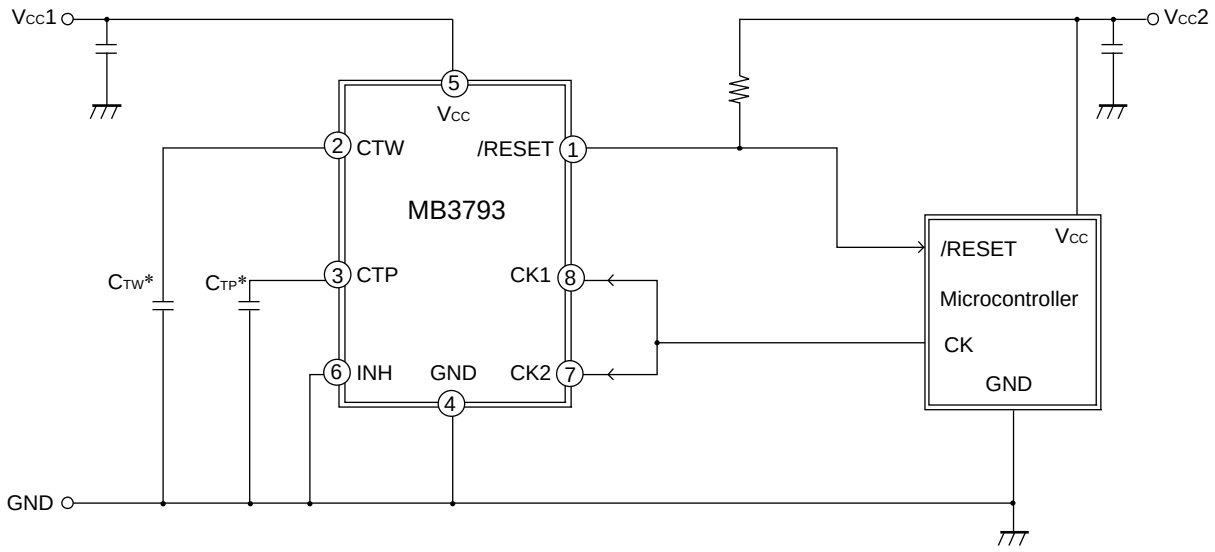
t_{WD} - C_{TW} characteristics



11. Application Example

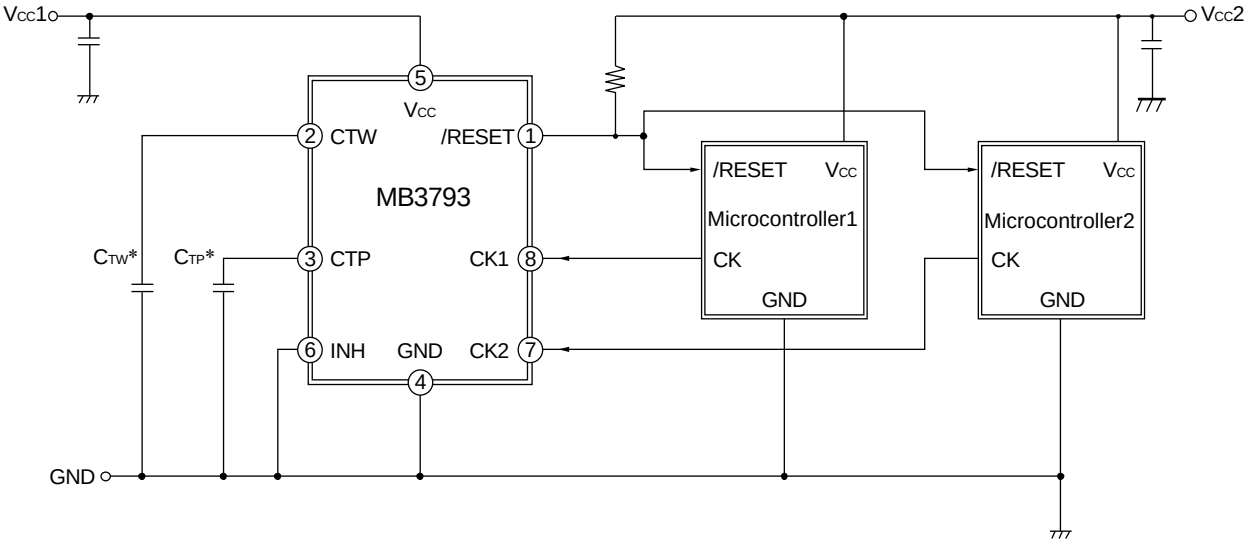
Power supply Voltage Monitor and Watchdog Timer

1. 1-clock monitor



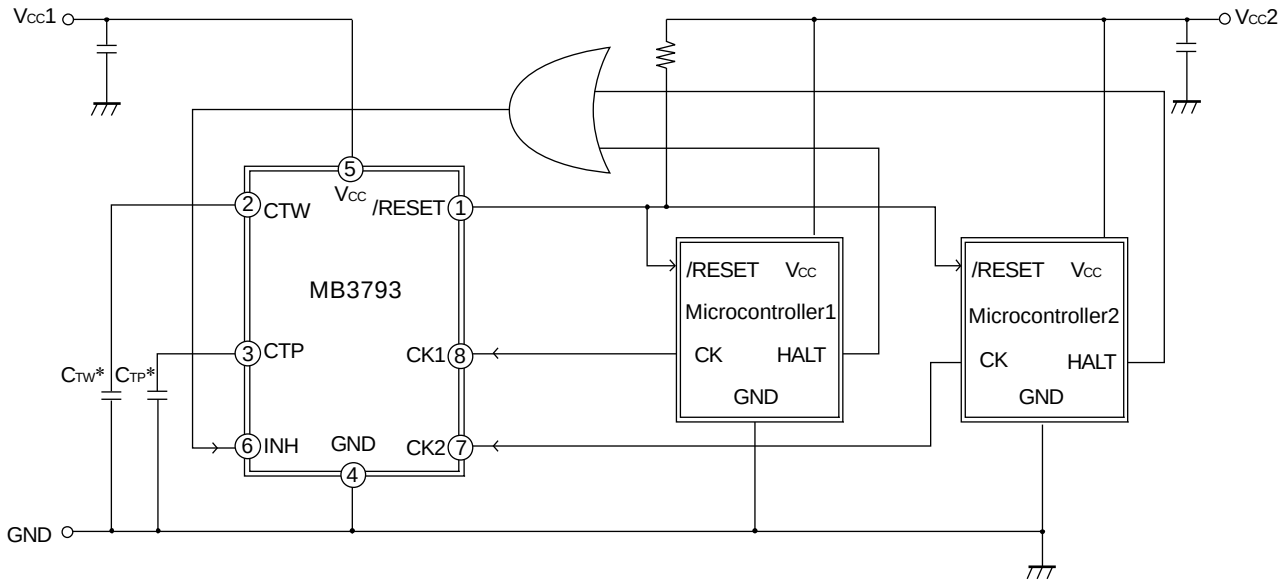
* : Use a capacitor with less leakage current.

2. 2-clock monitor



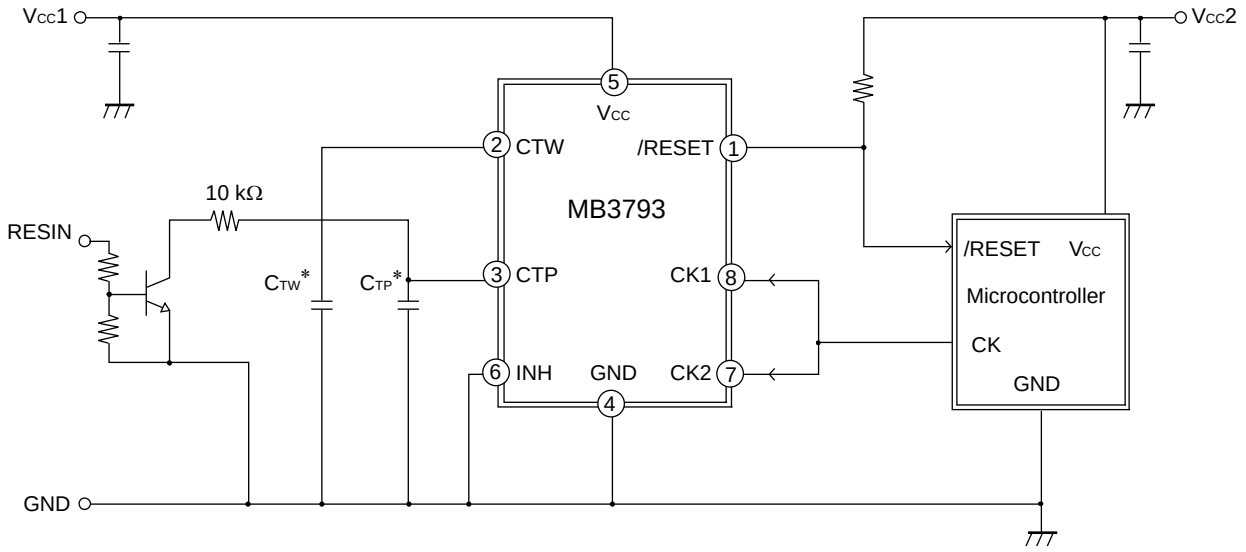
* : Use a capacitor with less leakage current.

Power Supply Voltage Monitor and Watchdog Timer Stop



* : Use a capacitor with less leakage current.

Setting of Compulsory Reset



* : Use a capacitor with less leakage current.

It is possible for the /RESET pin to fix to “L” if the CTP pin is short-circuited to GND. Take care not to change the value of the C_{TP} capacity because of the influence of Tr that is used at the time.

12. Usage Precaution

Do not Configure the IC Over the Maximum Ratings

If the IC is used over the maximum ratings, the LSI may be permanently damaged.
It is preferable for the device to normally operate within the recommended usage conditions. Usage outside of these conditions can have a bad effect on the reliability of the LSI.

Use the Devices within Recommended Operating Conditions

The recommended operating conditions are the recommended values that guarantee the normal operations of LSI.
The electrical ratings are guaranteed when the device is used within the recommended operating conditions and under the conditions stated for each item.

Printed Circuit Board Ground Lines Should be Set up with Consideration for Common Impedance

Take Appropriate Measures Against Static Electricity

- Containers for semiconductor materials should have anti-static protection or be made of conductive material.
- After mounting, printed circuit boards should be stored and shipped in conductive bags or containers.
- Work platforms, tools, and instruments should be properly grounded.
- Working personnel should be grounded with resistance of 250 kΩ to 1 MΩ in series between body and ground.

Do not Apply Negative Voltages

The use of negative voltages below –0.3 V may create parasitic transistors on LSI lines, which can cause malfunctions.

13. Ordering Information

Part Number	Package
MB3793-34DPNF	8-pin plastic SOP (SOB008)
MB3793-37DPNF	8-pin plastic SOP (SOB008)
MB3793-40DPNF	8-pin plastic SOP (SOB008)
MB3793-42DPNF	8-pin plastic SOP (SOB008)
MB3793-45DPNF	8-pin plastic SOP (SOB008)

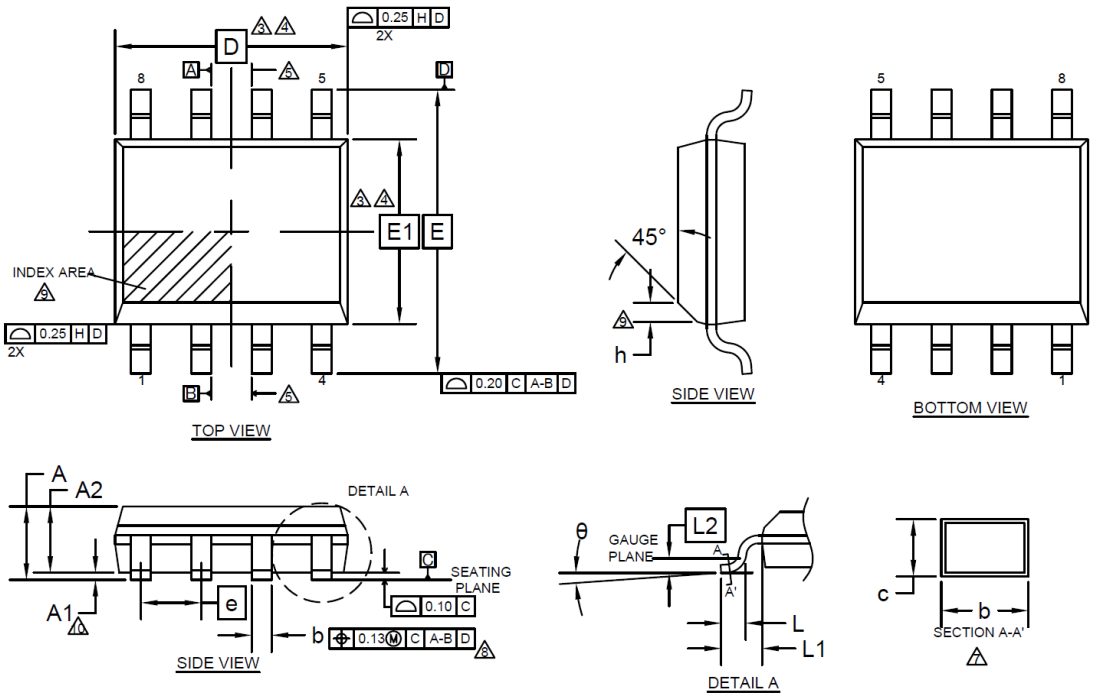
14. RoHS Compliance Information

The LSI products of Cypress with “E1” are compliant with RoHS Directive, and have observed the standard of lead, cadmium, mercury, Hexavalent chromium, polybrominated biphenyls (PBB), and polybrominated diphenyl ethers (PBDE).

The product that conforms to this standard is added “E1” at the end of the part number.

15. Package Dimensions

Package Code: SOB008



SYMBOL	DIMENSIONS		
	MIN.	NOM.	MAX.
A	——	——	1.75
A1	0.05	——	0.25
A2	1.30	1.40	1.50
D	5.05 BSC.		
E	6.00 BSC.		
E1	3.90 BSC		
θ	0°	——	8°
c	0.15	——	0.25
b	0.36	0.44	0.52
L	0.45	0.60	0.75
L 1	1.05 REF		
L 2	0.25 BSC		
e	1.27 BSC.		
h	0.40 BSC.		

NOTES

1. ALL DIMENSIONS ARE IN MILLIMETER.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. DIMENSIONING D INCLUDE MOLD FLASH, DIMENSIONING E1 DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.025 mm PER SIDE. D and E1 DIMENSION ARE DETERMINED AT DATUM H.
4. THE PACKAGE TOP MAY BE SMALLER THAN THE PACKAGE BOTTOM. DIMENSIONING D and E1 ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY EXCLUSIVE OF MOLD FLASH, THE BAR BURRS, GATE BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY MISMATCH BETWEEN THE TOP AND BOTTOM OF THE PLASTIC BODY.
5. DATUMS A & B TO BE DETERMINED AT DATUM H.
6. "N" IS THE MAXIMUM NUMBER OF TERMINAL POSITIONS FOR THE SPECIFIED PACKAGE LENGTH.
7. THE DIMENSION APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.10 mm TO 0.25mm FROM THE LEAD TIP.
8. DIMENSION "b" DOES NOT INCLUDE THE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.10mm TOTAL IN EXCESS OF THE "b" DIMENSION AT MAXIMUM MATERIAL CONDITION. THE DAMBAR MAY NOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.
9. THIS CHAMFER FEATURE IS OPTIONAL. LF IT IS NOT PRESENT, THEN A PIN 1 IDENTIFIER MUST BE LOCATED WITHIN THE INDEX AREA INDICATED
10. "A1" IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY EXCLUDING THE LID AND OR THERMAL ENHANCEMENT ON CAVITY DOWN PACKAGE CONFIGURATIONS.
11. JEDEC SPECIFICATION NO. REF : N/A

002-15856 Rev. **

16. Major Changes

Spansion Publication Number: DS405-00011

Page	Section	Change Results
Revision 1.0		
-	-	Initial release
Revision 2.0 (12/08/2014)		
-	-	1pin name is changed to /RESET
24	16. ORDERING INFORMATION	MB3793-34DPFV/ MB3793-37DPFV/ MB3793-40DPFV/ MB3793-42DPFV/ MB3793-45DPFV ordering part number is deleted
25 to 27	18. LABELING SAMPLE	Labeling sample is changed
32	21. PACKAGE DIMENSIONS	FPT-8P-M03 package information is deleted

NOTE: Please see "Document History" about later revised information.

Document History

Document Title: MB3793-34D/37D/40D/42D/45D Power-Voltage Monitoring IC with Watchdog Timer
Document Number: 002-08399

Revision	ECN	Orig. of Change	Submission Date	Description of Change
**	-	TAOA	12/08/2014	Migrated to Cypress and assigned document number 002-08399. No change to document contents or format.
*A	5131436	TAOA	02/18/2016	Updated to Cypress template
*B	5623700	HIXT	02/09/2017	Updated Pin Description: Change the package name from FPT-8P-M02 to SOB008 Updated description in the 4. Block Functions Output circuit Updated Ordering Information: Change the package name from FPT-8P-M02 to SOB008 Updated Package Dimensions: Updated to Cypress format Deleted "Marking Format" Deleted "Labeling Sample" Deleted "MB3793-34D/37D/40D/42D/45D Recommended Conditions of Moisture Sensitivity"
*C	5787039	MASG	06/27/2017	Adapted Cypress new logo.

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