USB Power Delivery 3.0 Controller
Supports Qualcomm Quick Charge 4+

1 Description

The iW656P is a USB Power Delivery (USB PD) interface controller which handles the communication between a Power Adapter and Mobile Device (MD). The controller enables the mobile device to alter the V_{BUS} voltage from the default 5V and/or maximum current limitation (e.g. 3A) for higher power output or better efficiency. The iW656P supports USB PD 3.0, including Programmable Power Supply (PPS) support, while being backwards compatible with USB PD 2.0. The device also supports Qualcomm® Quick Charge™ 4+ technology. The iW656P enables the use of advanced USB Type-C™ connector technology with CC1/CC2 pins for MD attach/detach detection and V_{CONN} support for Electronically Marked Cable (EMC) rated current reading.

The iW656P resides on the secondary side of an AC/DC power supply and negotiates voltage and current settings with the primary-side controller, depending on the requests from the MD. The iW656P uses Dialog's proprietary secondary-to-primary digital communication technology and when paired with one of Dialog's primary-side *RapidCharge™* controllers, such as the iW1791 or iW1799, the iW656P eliminates the need for a discrete decoder on the primary side by using one optocoupler to transmit all necessary information for rapid charging to the primary side. This includes output voltage requests, output current limits, output voltage undershoot, output over-voltage, and fault and reset signals. The iW656P incorporates Dialog's proven and reliable DLNK technology to communicate from the secondary to the primary and also has a built-in optocoupler LED driver to minimize the bill of material cost.

The power supply designed with the iW656P is fully protected. Using over-voltage protection on the D+/D-/CC1/CC2 pins, the iW656P helps to address soft short issues in cables and connectors caused by poor or loose connections between the cable connector and the socket, contamination in the USB connector, or a worn out cable. Additionally, proprietary short circuit protection on the V_{BUS} NFET ensures safe operation in the event of a short on the output, while the SD pin can be used with an external NTC resistor for protection from over-temeperature faults.

2 Features

- Supports USB PD 3.0 and PPS
  - » Maximum power limit in PPS to minimize power adapter size
  - » Integrated CC transmitter/receiver supports BMC communication
  - » Backwards-compatible with USB PD 2.0
- Supports Qualcomm Quick Charge 4+ Technology
- Supports up to 7 Power Data Objects (PDOs)
- Supports wide output voltage range from 3.3 to 21V
- Compatible with USB Type-C specification Rev 1.2 for MD attach/detach detection and V_{CONN} support for smart cables
- Integrated charge pump supports single NFET V_{BUS} switch
- 9-bit ADC provides accurate voltage sensing

- Proprietary V_{BUS} NFET protection to protect V_{BUS} switch from damage due to an output short circuit
- Proprietary secondary-to-primary digital communication eliminates discrete decoders on the primary side and simplifies system designs
- D+/D-/CC1/CC2 over-voltage protection (OVP) address soft short issues in the output cables and connectors
- Uses external NTC for power adapter temperature sensing
- Programmable active fast discharge from a high voltage V_{BUS} level to 5V at MD unplug or upon request with built-in switch or external switch
- Intelligent circuits helps achieve < 20mW system no-load power at 5V steady-state operation.
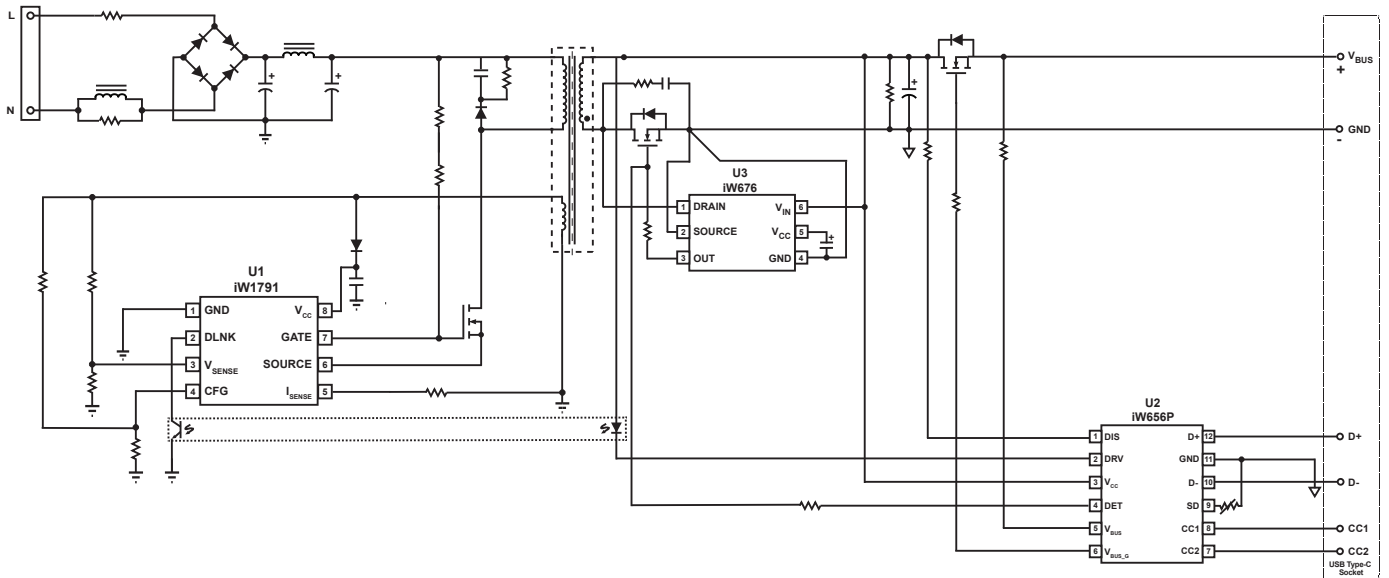- 12-pin 3x3mm TDFN package

3 Applications

- Rapid-charging AC/DC adapters for smart phones, tablets and other portable devices

Qualcomm® Quick Charge™ is a product of Qualcomm Technologies, Inc.

Product Summary   **Rev. 1.0**   **21-Feb-2019**

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Figure 3.1 : iW656P Typical Application Circuit
(with iW1791 as the Primary-Side Controller and the iW676 as the Synchronous Rectifier Controller)
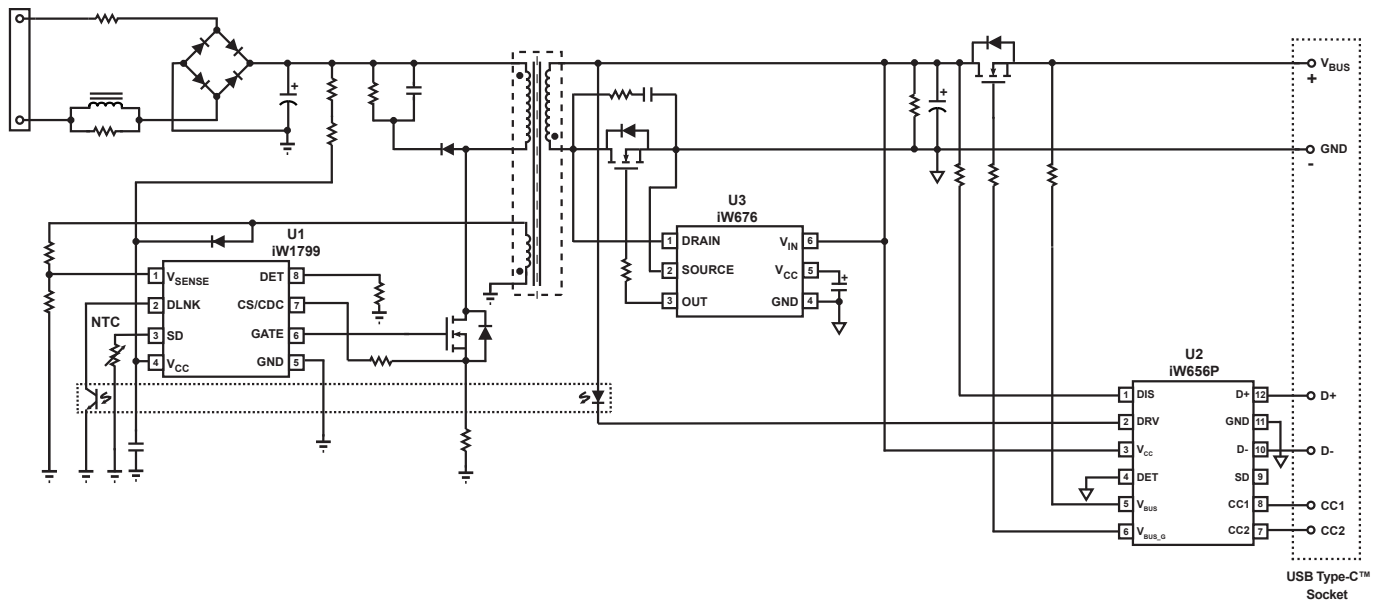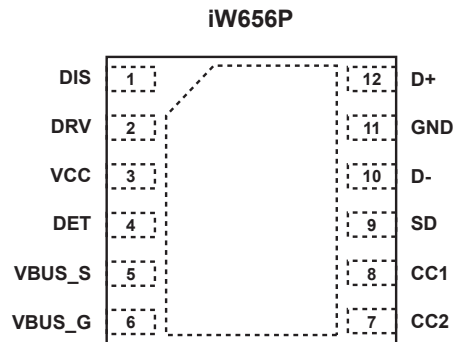

Figure 3.2 : iW656P Typical Application Circuit
(with iW1799 as the Primary-Side Controller and the iW676 as the Synchronous Rectifier Controller)

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4 Pinout Description

iW656P

DIS	1		12	D+
DRV	2		11	GND
VCC	3		10	D-
DET	4		9	SD
VBUS_S	5		8	CC1
VBUS_G	6		7	CC2

**Figure 4.1 : 12-Lead TDFN 3x3mm Package
(Top View, Transparent)**

Pin Number DFN-12	Pin Name	Type	Pin Description
1	DIS	Analog Output	Discharging circuit. Used for fast discharging of output capacitor.
2	DRV	Analog Output	External circuit drive. Can be used to drive optocoupler LED with automatic current limiting for transmitting signals to primary side.
3	V_{CC}	Power Supply	IC power supply.
4	DET	Analog Input	AC unplug detect.
5	V_{BUS}	Analog Input/ Output	Monitor V_{BUS} voltage after N-FET switch.
6	V_{BUS_G}	Analog Input/ Output	Connect to external N-FET gate pin for gate-source voltage control.
7	CC2	Analog Input/ Output	Configuration Channel 2.
8	CC1	Analog Input/ Output	Configuration Channel 1.
9	SD	Analog Input/ Output	External shutdown control. Can be configured for external over-temperature protection (OTP) by connecting an NTC resistor from this pin to Ground.
10	D-	Analog Input/ Output	USB D- signal.
11	GND	Ground	Ground.
12	D+	Analog Input/ Output	USB D+ signal.

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5 Absolute Maximum Ratings

Absolute maximum ratings are the parameter values or ranges which can cause permanent damage if exceeded.

Parameter	Symbol	Value	Units
V_{CC} voltage	V_{CC}	-0.3 to 30	V
DIS voltage	V_{DIS}	-0.3 to 30	V
DRV voltage	V_{DRV}	-0.3 to 30	V
V_{SD} voltage	V_{SD}	-0.3 to 7	V
D- voltage	V_{D-}	-0.3 to 7	V
D+ voltage	V_{D+}	-0.3 to 7	V
CC1 voltage	V_{CC1}	-0.3 to 30	V
CC2 voltage	V_{CC2}	-0.3 to 30	V
V_{BUS} voltage (I_{VBUS} < 10mA)	V_{BUS}	-0.7 to 30	V
V_{BUS_G} voltage	V_{BUS_G}	-0.7 to 35	V
DET voltage	V_{DET}	-0.7 to 30	V
Peak current at DIS pin (V_{DIS}= 12V)	I_{DIS}	600	mA
Maximum junction temperature	T_{JMAX}	150	°C
Operating junction temperature	T_{JOPT}	-40 to 150	°C
Storage temperature	T_{STG}	-65 to 150	°C
ESD rating per JS-001-2017 (D+, D-, CC1, CC2)		±8,000	V
ESD rating per JS-001-2017 (all other pins)		±2,000	V

Notes:

Note 1.   Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, so functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification are not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

USB Power Delivery 3.0 Controller
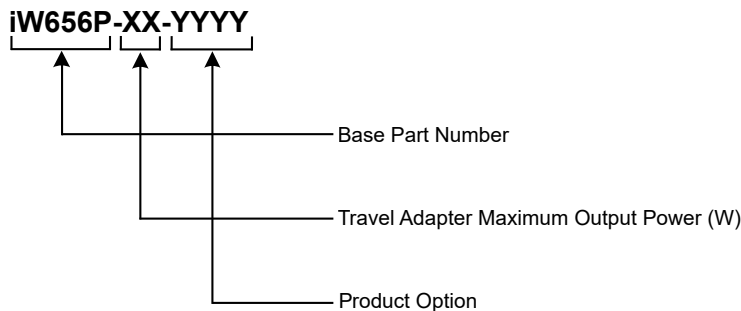**Supports Qualcomm Quick Charge 4+**

6 Physical Dimensions
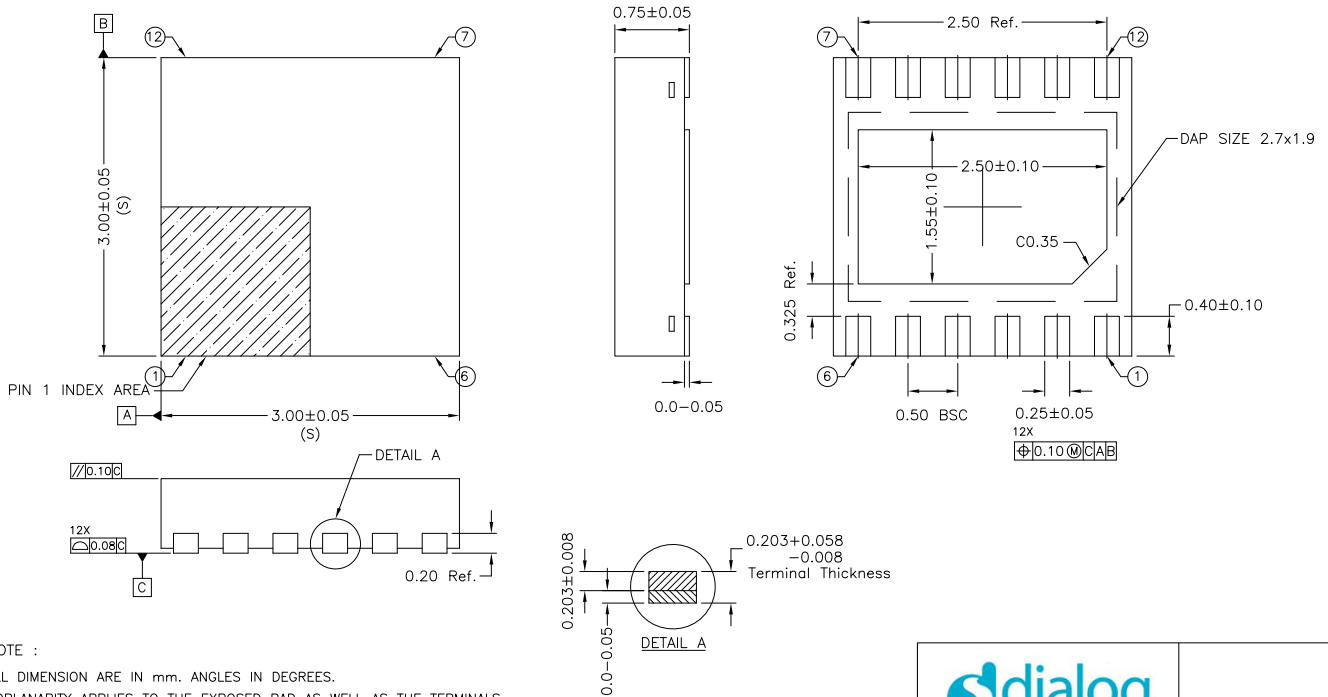
NOTE :

1. ALL DIMENSION ARE IN mm. ANGLES IN DEGREES.
2. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
   COPLANARITY SHALL NOT EXCEED 0.05 mm.
3. WARPAGE SHALL NOT EXCEED 0.05 mm.
4. PACKAGE LENGTH / PACKAGE WIDTH ARE CONSIDERED AS SPECIAL CHARACTERISTIC.(S)
5. REFER JEDEC MO−229.
6. L/F STOCK#FR0910 (Ag on lead only), UTL PKG CODE TD−300E300B012A
   OR TD−300S300B012A OR TD−300M300B012A OR TD−300D300B012A

STATUS: RELEASED	SCALE: DO NOT SCALE
TERMINAL FINISH: 100% Sn	
TITLE: 12 TDFN 3x3mm PACKAGE OUTLINE	
REV: B	REVISION NOTE: TITLE UPDATED	DATE: 19−DEC−2016

Figure 6.1 : 12-Lead TDFN 3x3mm Package

7 Part Number Code Description

iW656P-XX-YYYY

Base Part Number

Travel Adapter Maximum Output Power (W)

Product Option

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