

# PCI-DIO48H

## Specifications



**MEASUREMENT  
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# Specifications

All specifications are subject to change without notice.

Typical for 25 °C unless otherwise specified.

Specifications in *italic text* are guaranteed by design.

## Digital input/output

Table 1. Digital I/O specifications

Digital type	8255 emulation, Mode 0
Output	74ABT244
Input	74LS373
Configuration	4 banks of 8, 4 banks of 4, programmable by bank as input or output
Number of I/O	48
Output high	2.0 volts min @ -15 mA
Output low	0.55 volts max @ 64 mA
Input high	2.0 volts min, 5.5 volts absolute max
Input low	0.8 volts max, -0.5 volts absolute min
Power-up / reset state	Input mode (high impedance)
Pull-up/pull-down resistors	SIP resistor locations provided for pull-up or pull-down configuration.

## Power consumption

Table 2. Power consumption specifications

+5V Operating	405 mA typical, 1.6 A max
+5 V User Output	1 A max. per +5 V User Output (pin 49, protected with 1 A slow blow fuse)
+5 V User Output Fuse	<a href="#">0452001</a> - Littelfuse 1A NANO <sup>2</sup> ® Slo-Blo <sup>®</sup> Subminiature Surface Mount Fuse or equivalent

## Environmental

Table 3. Environmental specifications

<i>Operating temperature range</i>	0 to 50 °C
<i>Storage temperature range</i>	-20 to 70 °C
<i>Humidity</i>	0 to 90% non-condensing

## Mechanical

Table 4. Mechanical specifications

Card dimensions	174.7 mm (L) x 106.6 mm (H) x 18.72 mm (W max) 6.88" (L) x 4.2" (H) x 0.74" (W max)
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## Main connector and pin out

Table 5. Main connector specifications

Connector type	50-pin, male header connector	
Compatible cable	C50FF-x	
Compatible accessory products with the C50FF-x cable	CIO-MINI50 CIO-SPADE50 SCB-50 CIO-ERB24 CIO-SERB24/FD	CIO-ERB48 CIO-SERB48 SSR-RACK24 SSR-RACK48

Table 6. Main connector pin out

Pin	Signal name	Pin	Signal name
50	GND	49	+5V User Output*
48	FIRSTPORTC Bit 0	47	FIRSTPORTC Bit 1
46	FIRSTPORTC Bit 2	45	FIRSTPORTC Bit 3
44	FIRSTPORTC Bit 4	43	FIRSTPORTC Bit 5
42	FIRSTPORTC Bit 6	41	FIRSTPORTC Bit 7
40	FIRSTPORTB Bit 0	39	FIRSTPORTB Bit 1
38	FIRSTPORTB Bit 2	37	FIRSTPORTB Bit 3
36	FIRSTPORTB Bit 4	35	FIRSTPORTB Bit 5
34	FIRSTPORTB Bit 6	33	FIRSTPORTB Bit 7
32	FIRSTPORTA Bit 0	31	FIRSTPORTA Bit 1
30	FIRSTPORTA Bit 2	29	FIRSTPORTA Bit 3
28	FIRSTPORTA Bit 4	27	FIRSTPORTA Bit 5
26	FIRSTPORTA Bit 6	25	FIRSTPORTA Bit 7
24	SECONDPORTC Bit 0	23	SECONDPORTC Bit 1
22	SECONDPORTC Bit 2	21	SECONDPORTC Bit 3
20	SECONDPORTC Bit 4	19	SECONDPORTC Bit 5
18	SECONDPORTC Bit 6	17	SECONDPORTC Bit 7
16	SECONDPORTB Bit 0	15	SECONDPORTB Bit 1
14	SECONDPORTB Bit 2	13	SECONDPORTB Bit 3
12	SECONDPORTB Bit 4	11	SECONDPORTB Bit 5
10	SECONDPORTB Bit 6	9	SECONDPORTB Bit 7
8	SECONDPORTA Bit 0	7	SECONDPORTA Bit 1
6	SECONDPORTA Bit 2	5	SECONDPORTA Bit 3
4	SECONDPORTA Bit 4	3	SECONDPORTA Bit 5
2	SECONDPORTA Bit 6	1	SECONDPORTA Bit 7

\* Protected by a slow blow fuse rated at 1 A.

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