

HEX INVERTERS WITH SCHMITT TRIGGER INPUTS

Description

The 74AHC14 provides provides six independent Schmitt trigger input inverters with standard push-pull outputs. The device is designed for operation with a power supply range of 2.0V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment.

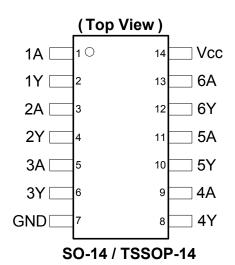
The gates perform the Boolean function:



Features

- Wide Supply Voltage Range from 2.0V to 5.5 V
- Outputs Sink or Source 8mA at V_{CC} = 4.5V
- CMOS Low Power Consumption
- Schmitt Trigger Action at All Inputs
- Typical hysteresis of 1V allows for transformation of slow changing input signals to sharply defined, fast transition output signals.
- Inputs can be driven by 3.3V or 5.5V allowing for voltage translation applications.
- ESD Protection Exceeds JESD 22
 - 200-V Machine Model (A115-A)
 - 2000-V Human Body Model (A114-A)
 - Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 250mA per JESD 78, Class II
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

- General Purpose Logic
- Wide array of products such as:
 - PCs, Networking, Notebooks, Netbooks
 - Computer Peripherals, Hard Drives, CD/DVD ROM
 - TV, DVD, DVR, Set Top Box

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

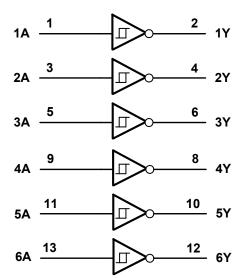
Click here for ordering information, located at the end of datasheet



Pin Descriptions

Pin Number	Pin Name	Function
1	1A	Data Input
2	1Y	Data Output
3	2A	Data Input
4	2Y	Data Output
5	3A	Data Input
6	3Y	Data Output
7	GND	Ground
8	4Y	Data Output
9	4A	Data Input
10	5Y	Data Output
11	5A	Data Input
12	6Y	Data Output
13	6A	Data Input
14	Vcc	Supply Voltage

Logic Diagram



Function Table

Input	Output
Α	Υ
L	Н
Н	L

Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range	-0.5 to +7.0	V
lık	Input Clamp Current V _I < -0.5V	-20	mA
I _{OK}	Output Clamp Current V _O < -0.5V	-20	mA
lok	Output Clamp Current V _O > V _{CC} +0.5V	25	mA
Io	Continuous Output Current -0.5V < V _O V _{CC} +0.5V	+/- 25	mA
Icc	Continuous Current Through V _{CC}	75	mA
I _{GND}	Continuous Current Through GND	-75	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
P _{TOT}	Total Power Dissipation	500	mW

Note:

- Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values. V_{CC} to the extent the maximum clamp current is exceeded.
 Unused inputs should be held at V_{CC} or Ground.



Recommended Operating Conditions (Note 5) @T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
Vcc	Supply Voltage		2.0	5.5	V
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	V _{CC}	V
T _A	Operating Free-Air Temperature		-40	+125	°C

Note: 5. Unused inputs should be held at V_{CC} or Ground.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Cumb al	Donomoton	Took Conditions	.,	T _A = -40°0	C to +85°C	T _A = -40°C	to +125°C	11!4
Symbol	Parameter	Test Conditions	V _{CC}	Min	Max	Min	Max	Unit
	Positive-Going		3V		2.2		2.2	
V_{T+}	Input Threshold		4.5V		3.15		3.15	V
	Voltage		5.5V		3.85		3.85	
	Negative-Going		3V	0.9		0.9		
V_{T-}	Input Threshold		4.5V	1.35		1.35		V
	Voltage		5.5V	1.65		1.65		
	Hystorosis		3V	0.3	1.2	0.25	1.2	
ΔV_T	Hysteresis (V _{T+} - V _{T-)}		4.5V	0.4	1.4	0.35	1.4	V
	(V + - V -)		5.5V	0.5	1.6	0.45	1.6	
		I _{OH} = -50μA	2.0V	1.9		1.9		_ _ _ v
	l	I _{OH} = -50μA	3.0V	2.9		2.9		
V_{OH}	High Level Output Voltage	I _{OH} = -50μA	4.5V	4.4		4.4		
	Voltage	I _{OH} = -4mA	3.0V	2.48		2.40		
		I _{OH} = -8mA	4.5V	3.80		3.70		
		I _{OL} = 50μA	2.0V		0.1		0.1	
		$I_{OL} = 50\mu A$	3.0V		0.1		0.1	
V_{OL}	Low-level Output Voltage	I _{OL} = 50μA	4.5V		0.1		0.1	V
	Voltage	I _{OL} = 4mA	3.0V		0.44		0.55	1
		I _{OL} = 8mA	4.5V		0.44		0.55	
Ιį	Input Current	V _I = GND to 5.5V	3.6V		±1		±2	μΑ
Icc	Supply Current	$V_I = GND \text{ or } V_{CC_1} I_O = 0$	3.6V		20		40	μA

Operating Characteristics

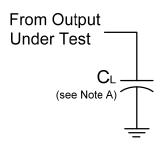
Parameter		Test Conditions	V _{CC} = 2.0V Typ	V _{CC} = 3.3V Typ	V _{CC} = 5V Typ	Unit
C _{pd}	Power Dissipation Capacitance per Gate	f = 1MHz	10.4	12.3	13	pF
C _i	Input Capacitance	$V_i = V_{CC} - \text{or GND}$	4.0	4.0	4.0	pF



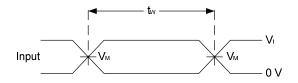
Switching Characteristics

Symbol	Parameter	Test	V _{CC}	7	Γ _A = +25°C	;	-40°C to	+85°C	-40°C to	+125°C	Unit
Syllibol	Farailletei	Conditions	V CC	Min	Тур	Max	Min	Max	Min	Max	Oilit
		Figure 1	3.0V to 3.6V	0.5	4.3	12.8	0.5	15.0	0.5	16.0	
	Propagation	$C_L = 15pF$	4.5V to 5.5V	0.5	3.2	8.6	0.5	10.0	0.5	11.0	
t _{PD}	Delay A _N to Y _N	Figure 1	3.0V to 3.6V	0.5	5.8	16.3	0.5	18.0	0.5	20.5	ns
		$C_L = 50pF$	4.5V to 5.5V	0.5	4.2	10.6	0.5	12.0	0.5	13.5	

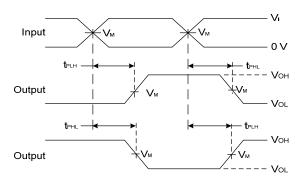
Parameter Measurement Information



V	Inp	outs	.,		
Vcc	VI	t _r /t _f	V _M	CL	
3.3V -3.6V	Vcc	3ns	V _{CC} /2	15pF, 50pF	
4.5V to 5.5V	V _{CC}	3ns	V _{CC} /2	15pF, 50pF	



Voltage Waveform Pulse Duration



Voltage Waveform
Propagation Delay Times
Inverting and Non Inverting Outputs

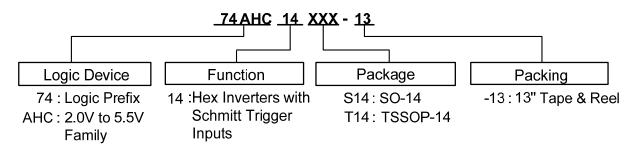
Figure 1 Load Circuit and Voltage Waveforms

Notes: A . Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD} .



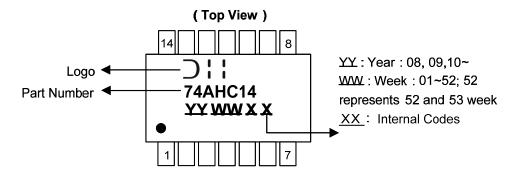
Ordering Information



	Device	Backage Code	Pookoging	7" Tape a	and Reel
	Device	Package Code	Packaging	Quantity	Part Number Suffix
Pby Lead-free Green	74AHC14S14-13	S14	SO-14	2500/Tape & Reel	-13
Pby Lead-free Green	74AHC14T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Marking Information

(1) SO-14, TSSOP-14



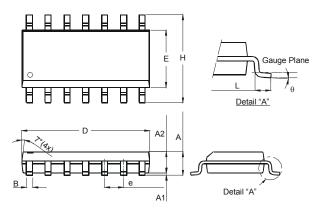
Part Number	Package
74AHC14S14	SO-14
74AHC14T14	TSSOP-14



Package Outline Dimensions (All dimensions in mm.)

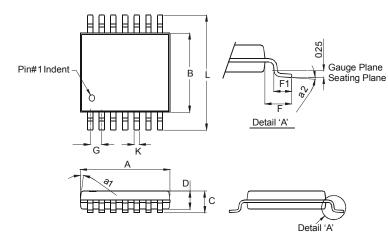
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

Package Type: SO-14



	SO-14				
Dim	Min	Max			
Α	1.47	1.73			
A1	0.10	0.25			
A2	1.45	Тур			
В	0.33	0.51			
D	8.53	8.74			
Е	3.80	3.99			
е	1.27	Тур			
Н	5.80	6.20			
٦	0.38	1.27			
θ	0°	8°			
All Di	mensions	s in mm			

Package Type: TSSOP-14



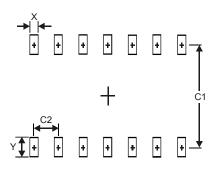
	TSSOP-1	4			
Dim	Min	Max			
a1	7° (4X)			
a2	0°	8°			
Α	4.9	5.10			
В	4.30	4.50			
O		1.2			
D	8.0	1.05			
F	1.00	Тур			
F1	0.45	0.75			
O	0.65 Typ				
K	0.19 0.30				
L 6.40 Typ					
All Din	nensions	s in mm			



Suggested Pad Layout

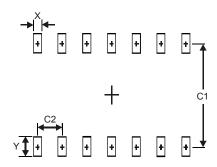
Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

Package Type: SO-14



Dimensions	Value (in mm)
Х	0.60
Υ	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
X	0.45
Y	1.45
C1	5.9
C2	0.65



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NL17SZU04P5T5G 74LVC06ADTR2G 74LVC04ADR2G NLV37WZ04USG NLX3G14FMUTCG NL17SZ04P5T5G NL17SG14P5T5G
NLV27WZU04DFT2G NLV17SG14DFT2G NLVHC1G04DFT2G MC14069UBD NLU3G14CMX1TCG NLX2G14BMX1TCG
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74HC14DN 74HC14DM/TR 74HC125M/TR HG74HC04MT/TR 74HC14DMT/TR 74HC04DN HT74HC04ARZ HT40106ARZ 74HC14-HXY IW4069UBN RS1GT04XC5 RS6G14XP RS2G17XH6 RS2G14XC6 RS2G04XC6