



74AHC1G07Q

SINGLE BUFFER/DRIVER WITH OPEN DRAIN OUTPUT

Description

The 74AHC1G07Q is an automotive compliant single buffer gate with an open drain output. The device is designed for operation with a power supply range of 2.0V to 5.5V. The open-drain output can be connected to other open drain outputs to implement active-low wired-OR or active-high wired-AND functions. The gate performs the positive Boolean function:



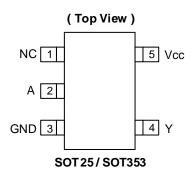
A pull up resistor is required to achieve a HIGH state.

Features

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Supply Voltage Range from 2.0V to 5.5V
- 8mA Output Sink at V_{CC} = 4.5V
- CMOS Low-Power Consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time
- Inputs not Limited by V_{CC}
- Balanced Propagation Delays
- Balanced Drive Capability
- ESD Protection Tested per AEC-Q100
- Exceeds 2000-V Human Body Model (AEC-Q100-002)
- Exceeds 1000-V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74AHC1G07Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Pin Assignments



Applications

- General Purpose Logic
- Wide Array of Products, such as:
 - Automotive Applications within Grade 1 Temperature Range
 - Industrial Computing/Controls/Automation
 - High Reliability Networking/Communications
 - Industrial/Agricultural Equipment

Notes:

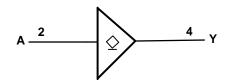
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Pin Descriptions

Pin Name	Description
NC	No Connection
Α	Data Input
GND	Ground
Υ	Data Output
Vcc	Supply Voltage

Logic Diagram



Function Table

Input	Output
Α	Y
Н	Z
L	L

Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
Vı	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to 6.5	V
lıĸ	Input Clamp Current (V _I < 0)	-20	mA
lok	Output Clamp Current (V _O < 0)	-20	mA
lo	Continuous Output Current (Vo = 0 to Vcc)	+25	mA
Icc	Continuous Current Through Vcc	75	mA
IGND	Continuous Current Through GND	-75	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
PD	Total Power Dissipation (Note 6)	250	mW

Notes:

- 4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.
- 5. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.
- 6. This will need to be derated at higher operating temperatures to prevent exceeding maximum T_J. Refer to package thermal characteristics section.



Recommended Operating Conditions (Note 7)

Symbol		Parameter	Min	Max	Unit
Vcc	Operating Voltage	_	2	5.5	V
		Vcc = 2V	1.5	_	
VIH	High-Level Input Voltage	Vcc = 3V	2.1	_	V
		Vcc = 5.5V	3.85	_	
		Vcc = 2V	_	0.5	
VIL	Low-Level Input Voltage	V _{CC} = 3V	_	0.9	V
		V _{CC} = 5.5V	_	1.65	
Vı	Input Voltage		0	5.5	V
Vo	Output Voltage		0	5.5	V
		V _{CC} = 2V	_	-50	μΑ
Іон	High-Level Output Current	$V_{CC} = 3.3V \pm 0.3V$	_	-4	4
		$V_{CC} = 5V \pm 0.5V$	_	-8	mA
		Vcc = 2V	_	50	μΑ
loL	Low-Level Output Current	$V_{CC} = 3.3V \pm 0.3V$	_	4	
		Vcc = 5V ± 0.5V	_	8	mA
A+/A>/	Input Transition Rise or Fall	$V_{CC} = 3.3V \pm 0.3V$	_	100	0./
Δt/ΔV	Rate	$V_{CC} = 5V \pm 0.5V$	_	20	ns/V
TA	Ambient Temperature	_	-40	+125	°C

Note:

7. Unused inputs should be held at V_{CC} or Ground.



Electrical Characteristics (All typical values are at $V_{CC} = 3.3V$, $T_A = +25$ °C)

	. .	T 10 111	V		+25°C		-40°C to	o +85°C	-40°C to	+125°C	11. %
Symbol	Parameter	Test Conditions	Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit
		., .,	2V	_	_	0.1	_	0.1		0.1	
		VI = VIL	3V	_	_	0.1	_	0.1		0.1	
	Lavel and Output	I _{OL} = 50μA	4.5V		_	0.1		0.1		0.1	
VoL	Low Level Output Voltage	Output $V_{I} = V_{IL}$ $I_{OL} = 4mA$	3V	_	_	0.36	_	0.44	_	0.55	V
		VI = VIL IOL = 8mA	4.5V	1	_	0.36	_	0.44	l	0.55	
l _l	Input Current	V _I = 5.5V or GND	0 to 5.5V	_	_	±0.1	_	±1	_	±2	μΑ
loz	OFF-State Output Current	V _I = 5.5V V _O = 0V or 5V	5.5V	_	_	±0.25	_	±2.5	_	±10	μΑ
Icc	Supply Current	$V_I = 5.5V$ or GND $I_O = 0$	5.5V	_	_	1	_	10		40	μΑ
Cı	Input Capacitance	V _I = V _{CC} or GND	5.5V	1	2.0	10	_	10		10	pF

Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Тур	Max	Unit
0	Thermal Resistance	SOT25	Nata 0	-	184	_	2011
θ JA	Junction-to-Ambient	SOT353	Note 8	_	385	_	°C/W
	Thermal Resistance	SOT25	Nata 0	_	62	_	0000
θЈС	Junction-to-Case	SOT353	Note 8	1	164	_	°C/W

Note: 8. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

(see Figure 1)

Parameter	From To		Vcc	Test		+25°C		-40°C to +85°C		-40°C to +125°C		Unit		
	(Input)	(Output)		Conditions	Min	Тур	Max	Min	Max	Min	Max			
			0.01/ . 0.01/	C _L = 15pF	1.0	3.5	5.6	1.0	6.3	1.0	7.0	ns		
4	_		V	$3.3V \pm 0.3V$	C _L = 50pF	1.0	5.0	8.0	1.0	9.0	1.0	10.0	ns	
tpzL	Α	Y	E 0)/ . 0 E)/	C _L = 15pF	1.0	2.5	3.9	1.0	4.6	1.0	4.9	ns		
			5.0V ± 0.5V	C _L = 50pF	1.0	3.6	5.5	1.0	6.5	1.0	7.0	ns		
			0.01/ . 0.01/	C _L = 15pF	1.0	5.8	7.9	1.0	8.4	1.0	8.9	ns		
4	•	_			$3.3V \pm 0.3V$	C _L = 50pF	1.0	8.3	11.5	1.0	12.0	1.0	12.5	ns
tpLZ	Α	Y	E 0)/ . 0 E)/	C _L = 15pF	1.0	4.2	5.1	1.0	5.6	1.0	6.1	ns		
				$5.0V \pm 0.5V$	C _L = 50pF	1.0	6.0	7.5	1.0	8.0	1.0	8.5	ns	

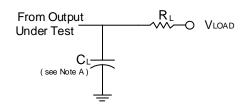


Operating Characteristics

 $T_A = +25$ °C

Parameter		Test Conditions	Тур	Unit
CPD	Power Dissipation Capacitance	$V_{CC} = 5.0V$, $f = 1MHz$ $C_L = 50pF$ $V_I = GND \text{ to } V_{CC}$	6.5	pF

Measurement Information



V	Inputs		V	V	0.	D.	
Vcc	Vı	t _R /t _F	VM	VLOAD	C∟	R∟	V Δ
3.3V±0.3V	Vcc	≤2ns	Vcc/2	Vcc	15pF	1kΩ	0.3V
5V±0.5V	Vcc	≤2.5ns	Vcc/2	Vcc	15pF	1kΩ	0.3V
3.3V±0.3V	Vcc	≤2.5ns	V _{CC} /2	Vcc	50pF	1kΩ	0.3V
5V±0.5V	Vcc	≤2.5ns	V _{CC} /2	Vcc	50pF	1kΩ	0.3V

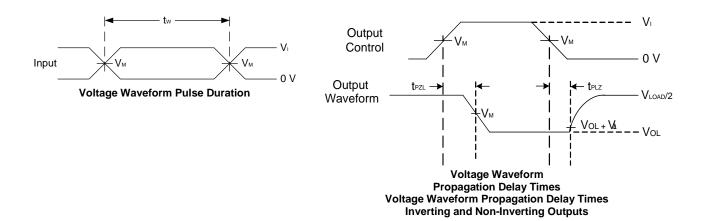


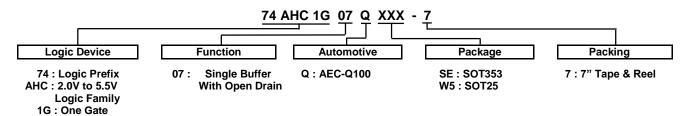
Figure 1. Load Circuit and Voltage Waveforms

A. Includes test lead and test apparatus capacitance. Notes:

- R. All pulses are supplied at pulse repetition rate ≤ 10MHz.
 C. The inputs are measured one at a time with one transition per measurement.
- D. For the open drain device t_{PLZ} and t_{PZL} are the same as t_{PD} .
- E. t_{PZL} is measured at V_M.
- F. t_{PLZ} is measured at V_{OL} + V_{Δ} .



Ordering Information (Note 9)



Part Number	Package	Package	Package Size	7" Tape	and Reel
Fait Number	Code	(Notes 10 & 11)	Fackage Size	Quantity	Part Number Suffix
74AHC1G07QSE-7	SE	SOT353	2.15 mm \times 2.1 mm \times 1.1 mm 0.65 mm lead pitch	3000/Tape & Reel	-7
74AHC1G07QW5-7	W5	SOT25	3.0 mm \times 2.8 mm \times 1.2 mm 0.95 mm lead pitch	3000/Tape & Reel	-7

Notes:

9. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

10. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.

11. The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

Marking Information

(Top View)

YWX 1 2 3

XXX: Identification Code Year 0~9

Week: $A \sim Z$ $1 \sim 26$ week a~z 27~52 week

z represents week 52 and 53

X : A~ Z: Internal Code

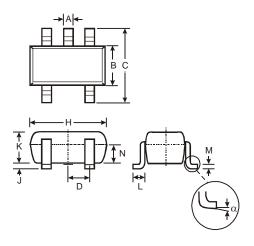
SOT25 / SOT353

Part Number	Package	Identification Code
74AHC1G07QW5-7	SOT25	YKQ
74AHC1G07QSE-7	SOT353	YKQ



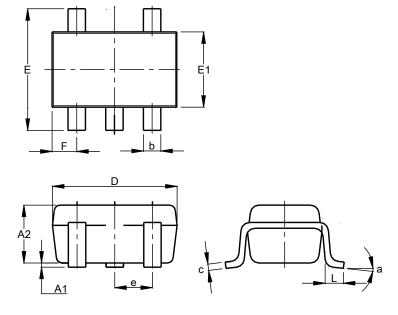
Package Outline Dimensions

(1) Package Type: SOT25



	SOT25									
Dim	Dim Min Max Typ									
Α	0.35	0.50	0.38							
В	1.50	1.70	1.60							
С	2.70	3.00	2.80							
D	-	-	0.95							
Н	2.90	3.10	3.00							
J	0.013	0.10	0.05							
K	1.00	1.30	1.10							
L	0.35	0.55	0.40							
M	0.10	0.20	0.15							
N	0.70	0.80	0.75							
α	0°	8°	-							
All D	imensi	ons in	mm							

(2) Package Type: SOT353



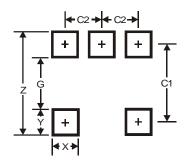
SOT353				
Dim	Min	Max	Тур	
A1	0.00	0.10	0.05	
A2	0.90	1.00	0.95	
b	0.10	0.30	0.25	
С	0.10	0.22	0.11	
D	1.80	2.20	2.15	
Е	2.00	2.20	2.10	
E1	1.15	1.35	1.30	
е	0.650 BSC			
F	0.40	0.45	0.425	
L	0.25	0.40	0.30	
а	0°	8°		
All Dimensions in mm				



Suggested Pad Layout

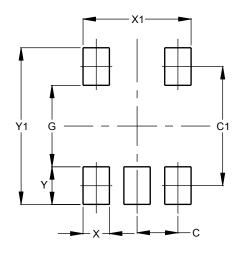
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95

(2) Package Type: SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Υ	0.600
Y1	2.500

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208@3
- Weight: 0.0158 grams (Approximate)

SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.0064 grams (Approximate)



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RHRXH162244K1 74AUP1G34FW5-7 74AUP1G07FW5-7 74LVC1G126FW4-7 74LVC2G126RA3-7 NLX2G17CMUTCG
74LVCE1G125FZ4-7 Le87501NQC 74AUP1G126FW5-7 TC74HC4050AP(F) 74LVCE1G07FZ4-7 NLX3G16DMUTCG
NLX2G06AMUTCG NLVVHC1G50DFT2G LE87100NQC LE87100NQCT LE87290YQC LE87290YQCT LE87511NQC LE87511NQCT
LE87557NQC LE87557NQCT LE87614MQC LE87614MQCT 74AUP1G125FW5-7 NLU2G16CMUTCG MC74LCX244MN2TWG