

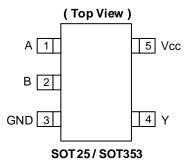
SINGLE 2-INPUT POSITIVE EXCLUSIVE OR GATE

Description

The 74AHC1G86Q is an automotive compliant single, two-input positive Exclusive OR gate with a standard push-pull output. The device is designed for operation with a power supply range of 2.0V to 5.5V. The gate performs the positive Boolean function:

$$Y = A \oplus B$$
 or $Y = \overline{A}B + A\overline{B}$

Pin Assignments



Features

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Supply Voltage Range from 2.0V to 5.5V
- ±8mA Output Drive at 5.0V
- CMOS Low-Power Consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time
- Inputs Not Limited by Vcc
- Balanced Propagation Delays
- Balanced Drive Capability
- ESD Protection Tested per AEC-Q100
- Exceeds 2000-V Human Body Model (AEC-Q100-002)
- Exceeds 1000-V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74AHC1G86Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Applications

- General Purpose Logic
- Wide Array of Products, such as:
 - Automotive Applications within Grade 1 Temperature Range
 - Industrial Computing/Controls/Automation
 - High Reliability Networking/Communications
 - Industrial/Agricultural Equipment

Notes:

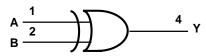
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Pin Descriptions

Pin Name	Description
Α	Data Input
В	Data Input
GND	Ground
Y	Data Output
Vcc	Supply Voltage

Logic Diagram



Function Table

Inp	Output	
Α	В	Υ
Н	Н	L
L	Н	Н
Н	L	Н
L	L	L

Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
Vı	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V _{CC} + 0.5	V
l _{IK}	Input Clamp Current V _I < 0	-20	mA
Іок	Output Clamp Current (Vo < 0 or Vo > Vcc)	±20	mA
lo	Continuous Output Current (Vo = 0 to Vcc)	±25	mA
Icc	Continuous Current Through Vcc	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
P _D	Total Power Dissipation (Note 6)	250	mW

Notes:

- 4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should
- be within recommend values.

 5. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum dispersion of both current and voltage must be maintained within the controlled range.
- 6. This will need to be derated at higher operating temperatures to prevent exceeding maximum T_J. Refer to package thermal characteristics section.



Recommended Operating Conditions (Note 7)

Symbol		Parameter	Min	Max	Unit
Vcc	Operating Voltage	_	2	5.5	V
		Vcc = 2V	1.5	_	
VIH	High-Level Input Voltage	Vcc = 3V	2.1	_	V
		Vcc = 5.5V	3.85	_	
		Vcc = 2V	_	0.5	
VIL	Low-Level Input Voltage	Vcc = 3V	_	0.9	V
		Vcc = 5.5V	_	1.65	
Vı	Input Voltage		0	5.5	V
Vo	Output Voltage		0	Vcc	V
		Vcc = 2V	_	-50	μΑ
Іон	High-Level Output Current	$Vcc = 3.3V \pm 0.3V$	_	-4	^
		$Vcc = 5V \pm 0.5V$	_	-8	mA
		Vcc = 2V	_	50	μΑ
loL	Low-Level Output Current	$Vcc = 5V \pm 0.5V$	_	4	A
		Vcc = 3V	_	8	mA
	Input Transition Rise or Fall	$Vcc = 3.3V \pm 0.3V$		100	
Δt/ΔV	Rate	$VCC = 5V \pm 0.5V$	_	20	ns/V

Note:

7. Unused inputs should be held at V_{CC} or Ground.

Electrical Characteristics (All typical values are at Vcc = 3.3V, TA = +25°C.)

Cumbal	Donomotor	Test Conditions	Vaa		+25°C		-40°C to	+85°C	-40°C to	+125°C	Unit
Symbol	Parameter	rest Conditions	Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit
			2V	1.9	2	_	1.9	_	1.9	_	
		Іон = -50μΑ	3V	2.9	3	_	2.9	_	2.9	_	
Voн	High Level		4.5V	4.4	4.5	_	4.4	_	4.4	_	V
	Output Voltage	I _{OH} = -4mA	3V	2.58	_	_	2.48	_	2.40	_	
		I _{OH} = -8mA	4.5V	3.94	_	_	3.8	_	3.70	_	
			2V	_	_	0.1	_	0.1	_	0.1	
		I _{OL} = 50μA	3V	_	_	0.1	_	0.1		0.1	
Vol	Low Level Output		4.5V	_	_	0.1	_	0.1	_	0.1	V
	Voltage	IoL = 4mA	3V	_	_	0.36	_	0.44	_	0.55	
		I _{OL} = 8mA	4.5V	_	_	0.36	_	0.44	_	0.55	
II	Input Current	V _I = 5.5V or GND	0 to 5.5V	_	_	±0.1	_	±1	_	±2	μΑ
Icc	Supply Current	V _I = 5.5V or GND I _O = 0	5.5V	l	1	1	ı	10	1	40	μΑ
Сі	Input Capacitance	V _I = V _{CC} or GND	5.5V	_	2.0	10	_	10	_	10	pF



Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Тур	Max	Unit
0	Thermal Resistance	SOT25	Note 0	1	184	_	900
θ JA	Junction-to-Ambient	SOT353	Note 8	-	385	_	°C/W
0	Thermal Resistance	SOT25	Nata 0	_	62	_	0044
θυς	Junction-to-Case	SOT353	Note 8	-	164	_	°C/W

Note: 8. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

$Vcc = 3.3V \pm 0.3V$ (See Figure 1)

Parameter	From	То	Test		+25°C		-40°C to	+85°C	-40°C to	+125°C	Unit
	(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max	
4	A == D	V	C _L = 15pF	0.6	4.0	11.0	0.6	13.0	0.6	14.0	ns
tpD	A or B	Y	C _L = 50pF	0.6	5.8	14.5	0.6	16.5	0.6	18.5	ns

$Vcc = 5V \pm 0.5V$ (See Figure 1)

Parameter	From	То	Test		+25°C		-40°C to	o +85°C	-40°C to	+125°C	Unit
	(Input)	(Output)	Conditions	Min	Тур	Max	Min	Max	Min	Max	
4	A = = D	V	C _L = 15pF	0.6	3.4	6.8	0.6	8.0	0.6	8.5	ns
tPD	A or B	Y	C _L = 50pF	0.6	4.9	8.8	0.6	10.0	0.6	11.5	ns

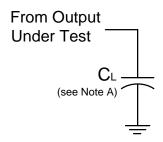
Operating Characteristics

$T_A = +25$ °C

	Parameter	Test Conditions	V _{CC} = 5V Typ	Unit
CPD	Power Dissipation Capacitance	f = 1MHz No Load	10	pF



Measurement Information



Vcc	In	puts	V _M	CL
V CC	Vı	tr/tr	V WI	OL.
3.3V±0.3V	Vcc	≤3ns	Vcc/2	15pF
5V±0.5V	Vcc	≤3ns	Vcc/2	15pF
3.3V±0.3V	Vcc	≤3ns	Vcc/2	50pF
5V±0.5V	Vcc	≤3ns	Vcc/2	50pF

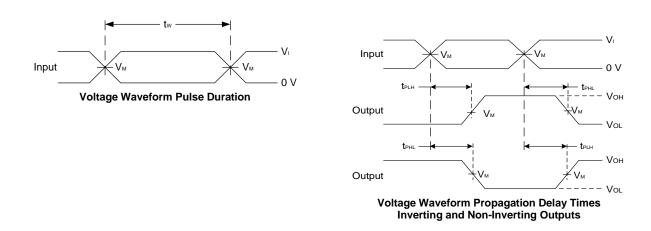


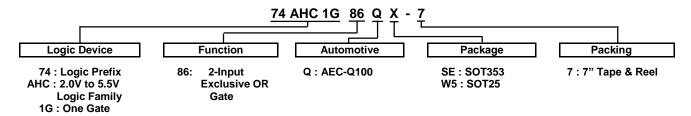
Figure 1. Load Circuit and Voltage Waveforms

Notes:

- A. Includes test lead and test apparatus capacitance.
 B. All pulses are supplied at pulse repetition rate ≤ 1MHz.
 C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD}.



Ordering Information (Note 9)



Part Number	Package	Package	Package Size	7" Tape	and Reel
r art Number	Code	(Notes 10 & 11)	i ackage Size	Quantity	Part Number Suffix
74AHC1G86QSE-7	SE	SOT353	2.15mm × 2.1mm × 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7
74AHC1G86QW5-7	W5	SOT25	3.0 mm \times 2.8 mm \times 1.2 mm 0.95 mm lead pitch	3000/Tape & Reel	-7

9. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

10. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at https://www.diodes.com/package-outlines.html.

11. The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

Marking Information

(Top View)

XXX

XXX: Identification Code : Year 0~9

Week: A~Z 1~26 week a~z 27~52 week z represents week 52 and 53

X: A~ Z: Internal Code

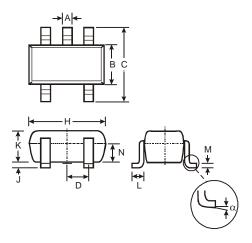
SOT25 / SOT353

Part Number	Package	Identification Code
74AHC1G86QW5-7	SOT25	YXQ
74AHC1G86QSE-7	SOT353	YXQ



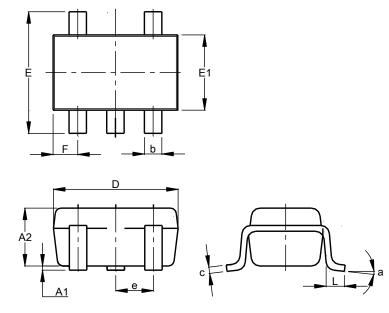
Package Outline Dimensions

(1) Package Type: SOT25



SOT25					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	-	-	0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
M	0.10	0.20	0.15		
N	0.70	0.80	0.75		
α	0°	8°	-		
All Dimensions in mm					

(2) Package Type: SOT353



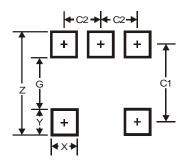
SOT353					
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.10	0.30	0.25		
С	0.10	0.22	0.11		
D	1.80	2.20	2.15		
Е	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
е	0.650 BSC				
F	0.40	0.45	0.425		
Ы	0.25	0.40	0.30		
а	0°	8°			
All Dimensions in mm					



Suggested Pad Layout

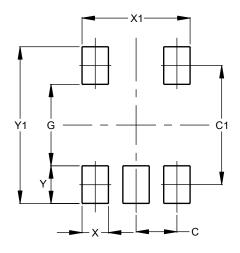
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



Dimensions	Value	
Z	3.20	
G	1.60	
Х	0.55	
Y	0.80	
C1	2.40	
C2	0.95	

(2) Package Type: SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Y	0.600
Y1	2 500

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208@3
- Weight: 0.0158 grams (Approximate)

SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.0064 grams (Approximate)



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NLV27WZ17DFT2G NLV74HC02ADR2G 74HC32S14-13 74LS133 M38510/30402BDA 74LVC1G86Z-7 74LVC2G08RA3-7

NLV74HC08ADTR2G NLV74HC14ADR2G NLV74HC20ADR2G NLX2G86MUTCG 5962-8973601DA 74LVC2G02HD4-7

NLU1G08AMUTCG NLU1G00AMUTCG 74LVC2G32RA3-7 74LVC2G00HD4-7 NL17SG02P5T5G 74LVC2G00HK3-7

74LVC2G86HK3-7 NLX1G99DMUTWG NLVVHC1G00DFT2G NLVHC1G08DFT2G NLV7SZ57DFT2G NLV74VHC04DTR2G

NLV27WZ86USG NLU1G86CMUTCG NLU1G08CMUTCG NL17SZ32P5T5G NL17SZ00P5T5G NL17SH02P5T5G 74AUP2G00RA3-7

NLVVHC1GT00DFT2G NLV74HC02ADTR2G NLX1G332CMUTCG NL17SG86P5T5G