



Description

The Advanced, Ultra Low Power (AUP) CMOS logic family is designed for low power and extended battery life in portable applications.

The 74AUP1G04 is a single inverter gate with a standard push-pull output designed for operation over a power supply range of 0.8V to 3.6V. The device is fully specified for partial power down applications using $I_{\rm OFF}$. The $I_{\rm OFF}$ circuitry disables the output, preventing damaging current backflow when the device is powered down. The gate performs the positive Boolean function:

$$Y = \overline{A}$$

Features

- Advanced Ultra Low Power (AUP) CMOS
- Supply Voltage Range from 0.8V to 3.6V
- ±4 mA Output Drive at 3.0V
- Low Static Power Consumption $I_{CC} < 0.9 \mu A$
 - Low Dynamic Power Consumption

 $C_{PD} = 6.1pF$ (Typical at 3.6V)

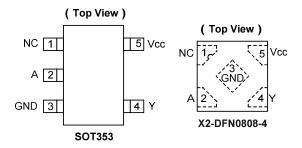
- Schmitt Trigger Action at all inputs makes the circuit tolerant for slower input rise and fall time. The hysteresis is typically 250mV at V_{CC} = 3.0V.
- I_{OFF} Supports Partial-Power-Down Mode Operation
- ESD Protection Exceeds JESD 22

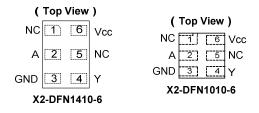
2000-V Human Body Model (A114)

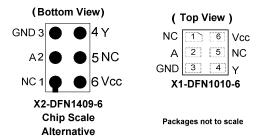
Exceeds 1000-V Charged Device Model (C101)

- Latch-Up Exceeds 100mA per JESD 78, Class I
- Leadless Packages Named per JESD30E
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments







Applications

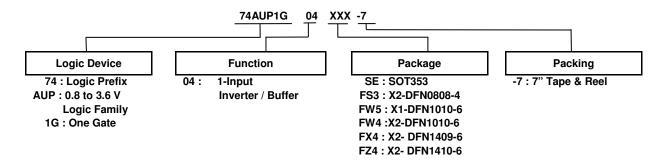
- Suited for Battery and Low Power Needs
- Wide array of products such as:
 - Tablets, E-readers
 - Cell Phones, Personal Navigation / GPS
 - MP3 Players ,Cameras, Video Recorders
 - PCs, Ultrabooks, Notebooks, Netbooks,
 - Computer Peripherals, Hard Drives, SSDs, CD/DVD ROMs
 - TVs, DVDs, DVRs, Set-Top Boxes

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Ordering Information



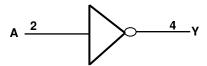
Device	Package	Package	Package	7" Tape	and Reel
Device	Code	(Notes 4 & 5)	Size	Quantity	Part Number Suffix
74AUP1G04SE-7	SE	SOT353	2.0mm x 2.0mm x 1.1mm 0.65 mm lead pitch	3,000/Tape & Reel	-7
74AUP1G04FS3-7	FS3	X2-DFN0808-4	0.8mm x 0.8mm x 0.35mm 0.5 mm pad pitch (diamond)	5,000/Tape & Reel	-7
74AUP1G04FW5-7	FW5	X1-DFN1010-6	1.0mm x 1.0mm x 0.5mm 0.35 mm pad pitch	5,000/Tape & Reel	-7
74AUP1G04FW4-7	FW4	X2-DFN1010-6	1.0mm x 1.0mm x 0.4mm 0.35 mm pad pitch	5,000/Tape & Reel	-7
74AUP1G04FX4-7	FX4	X2-DFN1409-6 Chip Scale Alternative	1.4mm x 0.9mm x 0.4mm 0.5 mm pad pitch	5,000/Tape & Reel	-7
74AUP1G04FZ4-7	FZ4	X2-DFN1410-6	1.4mm x 1.0mm x 0.4mm 0.5 mm pad pitch	5,000/Tape & Reel	-7

Notes: 4. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

Pin Descriptions

Pin Name	Function
NC	No Connection
Α	Data Input
GND	Ground
Υ	Data Output
Vcc	Supply Voltage

Logic Diagram



Function Table

Inputs	Output				
Α	Υ				
Н	L				
L	Н				

^{5.} The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.



Absolute Maximum Ratings (Notes 6 & 7) (@T_A = +25 °C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
V _{CC}	Supply Voltage Range	-0.5 to +4.6	V
VI	Input Voltage Range	-0.5 to +4.6	V
Vo	Voltage applied to output in high or low state	-0.5 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current V _I <0	50	mA
lok	Output Clamp Current (V _O < 0)	50	mA
lo	Continuous Output Current (V _O = 0 to V _{CC})	±20	mA
Icc	Continuous Current Through V _{CC}	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	℃
T _{STG}	Storage Temperature	-65 to +150	∞

Notes:

Recommended Operating Conditions (Note 8) (@T_A = +25 ℃, unless otherwise specified.)

Symbol	Parame	eter	Min	Max	Unit
V _{CC}	Operating Voltage		0.8	3.6	V
VI	Input Voltage		0	3.6	V
Vo	Output Voltage		0	Vcc	V
		$V_{CC} = 0.8V$	_	-20	μΑ
		$V_{CC} = 1.1V$	_	-1.1	
	Lligh Loyal Output Current	$V_{CC} = 1.4V$	_	-1.7	
Іон	High-Level Output Current	V _{CC} = 1.65V	_	-1.9	mA
		V _{CC} = 2.3V	_	-3.1]
		V _{CC} = 3.0V	_	-4]
		$V_{CC} = 0.8V$	_	20	μΑ
		V _{CC} = 1.1V	_	1.1	
	Low-Level Output Current	V _{CC} = 1.4V	_	1.7]
loL	Low-Level Output Current	$V_{CC} = 1.65V$	_	1.9	mA
		$V_{CC} = 2.3V$	_	3.1	
		V _{CC} = 3.0V	_	4]
Δt/ΔV	Input Transition Rise or Fall Rate	V _{CC} = 0.8V to 3.6V	_	200	ns/V
TA	Operating Free-Air Temperature		-40	+125	∞

Note:

8. Unused inputs should be held at V_{CC} or Ground.

^{6.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

^{7.} Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



Electrical Characteristics

Symbol	Parameter	Test Conditions	V	T _A = -	+25℃	T _A = -40℃	C to +85℃	Unit	
Symbol	Parameter	rest Conditions	Vcc	Min	Max	Min	Max	Unit	
		_	0.8V to 1.65V	0.80 x V _{CC}	_	0.80 x V _{CC}	_		
V_{IH}	High-Level Input	_	1.65V to 1.95V	0.65 x V _{CC}	_	0.65 x V _{CC}	_	V	
VIH	Voltage	_	2.3V to 2.7V	1.6	_	1.6	_	V	
		_	3.0V to 3.6V	2.0	_	2.0			
		_	0.8V to 1.65V	_	0.30 x V _{CC}	_	$0.30 \times V_{CC}$		
V_{IL}	Low-Level Input	_	1.65V to 1.95V		0.35 x V _{CC}	_	0.35 x V _{CC}	V	
V IL	Voltage	_	2.3V to 2.7V	_	0.7	_	0.7	V	
		_	3.0V to 3.6V	_	0.9	_	0.9		
		$I_{OH} = -20\mu A$	0.8V to 3.6V	V _{CC} - 0.1	_	$V_{CC} - 0.1$	_		
		$I_{OH} = -1.1 \text{mA}$	1.1V	0.75 x V _{CC}	_	0.7 x V _{CC}	_		
		$I_{OH} = -1.7mA$	1.4V	1.11	_	1.03	_		
	High-Level Output	I _{OH} = -1.9mA	1.65V	1.32	_	1.3	_		
V_{OH}	Voltage	I _{OH} = -2.3mA	0.01/	2.05	_	1.97	_	V	
		I _{OH} = -3.1mA	2.3V	1.9	_	1.85	_		
		I _{OH} = -2.7mA	0)/	2.72	_	2.67	_		
		I _{OH} = -4mA	3V	2.6	_	2.55	_		
		I _{OL} = 20μA	0.8V to 3.6V	_	0.1	_	0.1		
		I _{OL} = 1.1mA	1.1V	_	0.3 x V _{CC}	<u> </u>	0.3 x V _{CC}		
		I _{OL} = 1.7mA	1.4V	_	0.31	_	0.37		
.,	Low-Level Output	I _{OL} = 1.9mA	1.65V	_	0.31	_	0.35	.,	
V_{OL}	Voltage	I _{OL} = 2.3mA	0.01/	_	0.31	_	0.33	V	
		I _{OL} = 3.1mA	2.3V	_	0.44	_	0.45		
		I _{OL} = 2.7mA	0)/	_	0.31	_	0.33		
		I _{OL} = 4mA	3V	_	0.44	_	0.45		
II	Input Current	A or B Input V _I = GND to 3.6V	0V to 3.6V	_	± 0.1	_	± 0.5	μΑ	
loff	Power Down Leakage Current	V_I or $V_O = 0V$ to 3.6V	0	_	0.2	_	0.6	μΑ	
ΔI_{OFF}	Delta Power Down Leakage Current	V_I or $V_O = 0V$ to 3.6V	0V to 0.2V	_	0.2	_	0.6	μΑ	
I _{CC}	Supply Current	$V_I = GND \text{ or } V_{CC}$ $I_O = 0$	0.8V to 3.6V	_	0.5	_	0.9	μΑ	
ΔI_{CC}	Additional Supply Current	Input at V _{CC} -0.6	3.3V	_	40	_	50	μΑ	



Electrical Characteristics (continued)

Symbol	Parameter	Test Conditions	V	T _A = -40 °C	to +125℃	Unit
Syllibol	Parameter	rest Conditions	Vcc	Min	Max	Unit
		_	0.8V to 1.65V	0.80 x V _{CC}	_	
V _{IH}	High-Level Input	_	1.65V to 1.95V	0.70 x V _{CC}	_	V
VIH	Voltage	_	2.3V to 2.7V	1.6	_	v
		_	3.0V to 3.6V	2.0	_	
		_	0.8V to 1.65V	_	0.25 x V _{CC}	
V_{IL}	Low-Level Input	_	1.65V to 1.95V	_	0.30 x V _{CC}	V
V IL	Voltage	_	2.3V to 2.7V	_	0.7	
		_	3.0V to 3.6V	_	0.9	
		$I_{OH} = -20\mu A$	0.8V to 3.6V	$V_{CC} - 0.11$	_	
		I _{OH} = -1.1mA	1.1V	$0.6 \times V_{CC}$	_	
		$I_{OH} = -1.7mA$	1.4V	0.93	_	
V	V _{OH} High-Level Output	I _{OH} = -1.9mA	1.65V	1.17	_	V
VOH	Voltage	$I_{OH} = -2.3mA$	2.3V	1.77	_	
		I _{OH} = -3.1mA	2.3	1.67	_	
		I _{OH} = -2.7mA	01/	2.40	_	
		I _{OH} = -4mA	3V	2.30	_	
		I _{OL} = 20μA	0.8V to 3.6V	_	0.11	
		I _{OL} = 1.1mA	1.1V	_	0.33 x V _{CC}	
		I _{OL} = 1.7mA	1.4V	_	0.41	
.,	Low-Level Output	I _{OL} = 1.9mA	1.65V	_	0.39	٦ ,,
V_{OL}	Voltage	I _{OL} = 2.3mA	2.01/	_	0.36	V
		I _{OL} = 3.1mA	2.3V	_	0.50	
		I _{OL} = 2.7mA	-1.	_	0.36	
		I _{OL} = 4mA	3V	_	0.50	
l _l	Input Current	A or B Input V _I = GND to 3.6V	0V to 3.6V	_	± 0.75	μА
loff	Power Down Leakage Current	V_I or $V_O = 0V$ to 3.6V	0V	_	± 3.5	μΑ
Δl _{OFF}	Delta Power Down Leakage Current	V_I or $V_O = 0V$ to 3.6V	0V to 0.2V	-	± 2.5	μА
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	0.8V to 3.6V	_	3.0	μΑ
ΔI _{CC}	Additional Supply Current	Input at V _{CC} -0.6V Other Inputs at V _{CC} or GND	3.3V	_	75	μA



Switching Characteristics

C_L = 5pF, See Figure 1

Parameter	From Input	TO OUTPUT	V	T _A = +25 ℃			T _A = -40 °C to +85 °C		T _A = -40 °C to +125 °C		Unit
Parameter			V _{CC}	Min	Тур	Max	Min	Max	Min	Max	Oilit
			V8.0	1	16.0	_	_	_	_	_	
			1.2V ± 0.1V	2.4	5.0	10.3	2.1	14.1	2.1	14.1	- ns
	A or B		1.5V ± 0.1V	1.8	3.6	6.4	1.6	7.4	1.6	8.2	
t _{pd}	AUID	Y	1.8V ± 0.15V	1.5	2.9	5.0	1.4	5.9	1.4	6.5	
			$2.5V \pm 0.2V$	1.2	2.4	3.9	1.1	4.5	1.1	5.0	
			$3.3V \pm 0.3V$	1.1	2.1	3.2	1.1	3.9	1.1	4.3	

C_L = 10pF, See Figure 1

Parameter	From Input	To Output	V	T,	T _A = +25 ℃			T _A = -40 °C to +85 °C		T _A = -40 °C to +125 °C	
Tarameter			Vcc	Min	Тур	Max	Min	Max	Min	Max	Unit
			V8.0	_	19.8	_	_	_	_		
	A or B	Υ	1.2V ± 0.1V	2.8	5.9	12.2	2.6	17.0	2.6	17.0	
4 .			1.5V ± 0.1V	2.3	4.2	7.5	2.1	8.7	2.1	9.6	
t _{pd}			1.8V ± 0.15V	2.0	3.5	5.9	1.8	7.0	1.8	7.7	ns
			$2.5V \pm 0.2V$	1.7	2.9	4.6	1.5	5.4	1.5	6.0	
			$3.3V \pm 0.3V$	1.4	2.7	3.8	1.4	4.5	1.4	5.0	

C_L = 15pF, See Figure 1

Parameter	From Input	To Output	V _{CC}	T,	A = +25°C	;	T _A = -40 °C to +85 °C		T _A = -40 °C to +125 °C		Unit
			V CC	Min	Тур	Max	Min	Max	Min	Max	Oill
			V8.0		23.3	_	_		_	_	
		Υ	1.2V ± 0.1V	3.2	8.0	14.0	3.0	26.0	3.0	26.0	- ns
	A or B		1.5V ± 0.1V	2.6	7.5	9.0	2.4	10.0	2.4	11.0	
t _{pd}	AUID		1.8V ± 0.15V	2.3	5.2	6.7	2.1	8.0	2.1	8.8	
			2.5V ± 0.2V	2.1	3.1	5.1	1.8	6.1	1.8	6.8	
			3.3V ± 0.3V	1.8	3.1	4.2	1.8	5.0	1.8	5.5	

 $C_L = 30pF$, See Figure 1

Parameter	From Input	To Output	V	T,	_A = +25℃	;	T _A = -40 °C to +85 °C		T _A = -40 °C to +125 °C		Unit
			Vcc	Min	Тур	Max	Min	Max	Min	Max	Ullit
			V8.0	_	33.6	_	_	_	_	_	
		Y	1.2V ± 0.1V	4.4	13.0	18.0	4.0	27.0	4.0	27.0	ns
	Λ α « D		1.5V ± 0.1V	3.6	6.3	12.0	3.2	13.8	3.2	14.2	
t _{pd}	A or B		1.8V ± 0.15V	3.2	5.3	9.0	2.9	10.5	2.9	11.6	
			2.5V ± 0.2V	2.9	4.5	6.5	2.6	7.6	2.6	8.4	
			$3.3V \pm 0.3V$	2.1	4.2	5.4	2.1	6.2	2.1	6.9	



Operating and Package Characteristics (@T_A = +25 ℃, unless otherwise specified.)

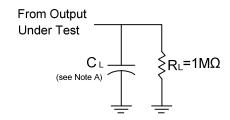
	Parameter	Test Condition	ons	V _{CC}	Тур	Unit
				0.8V	6.5	
				1.2V ± 0.1V	6.3	
_	Power Dissipation	f = 1MH	lz	1.5V ± 0.1V	6.3	"r
C_{pd}	Capacitance	No Loa	d	1.8V ± 0.15V	6.2	pF
				2.5V ± 0.2V	6.2	
				$3.3V \pm 0.3V$	6.1	
Ci	Input Capacitance	$V_i = V_{CC}$ or	GND	0V or 3.3V	1.5	pF
		SOT353		_	371	
		X2-DFN0808-4	(Note 9)	_	430	
0	Thermal Resistance	X1-DFN1010-6		_	435	«C/W
θ_{JA}	Junction-to-Ambient	X2-DFN1010-6		_	445	-C/VV
		X2-DFN1409-6]	_	470	
		X2-DFN1410-6]	_	460	
		SOT353		_	143	
		X2-DFN0808-4		_	240	
_	Thermal Resistance	X1-DFN1010-6	(NI=4= 0)	_	250	00.444
θ_{JC}	Junction-to-Case	X2-DFN1010-6	(Note 9)	_	250	
		X2-DFN1409-6		_	275	
		X2-DFN1410-6		_	265	

Note: 9. Test condition for each of the six package types: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

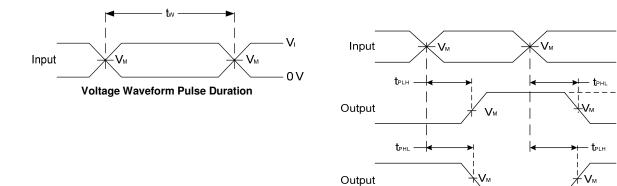
 V_{OL}



Parameter Measurement Information



Vcc	Inputs		V	0
VCC	VI	t _r /t _f	V _M	C_L
0.8V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30pF
1.2V±0.1V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30pF
1.5V±0.1V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30pF
1.8V ±0.15V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30pF
2.5V±0.2V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30pF
3.3V±0.3V	V _{CC}	≤3ns	V _{CC} /2	5, 10, 15, 30pF



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1 Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
 C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as $t_{PD.}$



Marking Information

(1) SOT353

(Top View)

5 4 XX Y WX

2

XX: Identification code

Y: Year 0~9

<u>W</u>: Week: A~Z: 1~26 week;

a~z: 27~52 week; z represents 52 and 53 week

X: A~Z: Internal code

Part Number	Package	Identification Code
74AUP1G04SE	SOT353	XK

(2) X2-DFN0808-4, X1-DFN1010-6, X2-DFN1010-6, X2-DFN1409-6 and X2-DFN1410-6

(Top View)

3

<u>XX</u> <u>Y W X</u> XX: Identification Code

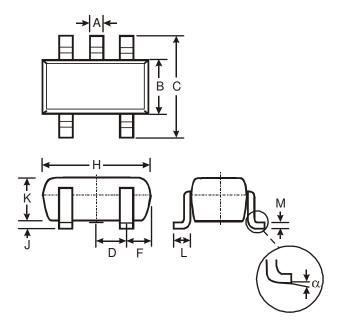
<u>W</u>: Week: A~Z: 1~26 week; a~z: 27~52 week; z represents 52 and 53 week

X: A~Z: Internal code

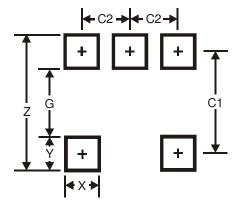
Part Number	Package	Identification Code
74AUP1G04FS3	X2-DFN0808-4	YK
74AUP1G04FW5	X1-DFN1010-6	Q4
74AUP1G04FW4	X2-DFN1010-6	XK
74AUP1G04FX4	X2-DFN1409-6	HC
74AUP1G04FZ4	X2-DFN1410-6	XK



SOT353 Package Outline Dimensions and Suggested Pad Layout



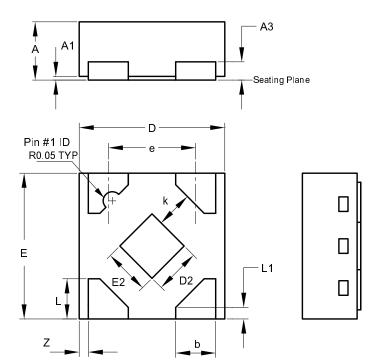
SOT353				
Dim	Min	Max	Тур	
Α	0.10	0.30	0.25	
В	1.15	1.35	1.30	
С	2.00	2.20	2.10	
D		0.65 Typ)	
F	0.40	0.45	0.425	
Н	1.80	2.20	2.15	
J	0	0.10	0.05	
K	0.90	1.00	1.00	
L	0.25	0.40	0.30	
М	0.10	0.22	0.11	
α	0°	8°	-	
All Dimensions in mm				



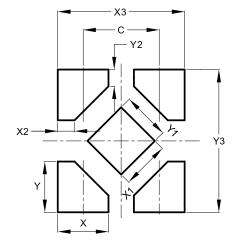
Dimensions	Value (in mm)	
Z	2.5	
G	1.3	
Х	0.42	
Υ	0.6	
C1	1.9	
C2	0.65	



X2-DFN0808-4 Package Outline Dimensions and Suggested Pad Layout



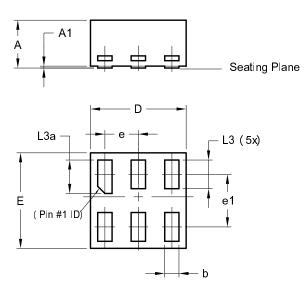
X2-DFN0808-4				
Dim	Min	Max	Тур	
Α	0.25	0.35	0.30	
A 1	0	0.04	0.02	
A3	-	-	0.13	
b	0.17	0.27	0.22	
D	0.75	0.85	0.80	
D2	0.15	0.35	0.25	
Е	0.75	0.85	0.80	
E2	0.15	0.35	0.25	
е	1	-	0.48	
K	0.20	-	-	
L	0.17	0.27	0.22	
L1	0.02	0.12	0.07	
Z	-	-	0.05	
All Dimensions in mm				



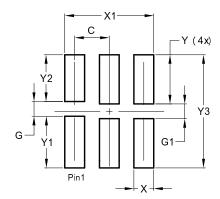
Dimensions	Value
С	0.480
Х	0.320
X1	0.300
X2	0.106
Х3	0.800
Y	0.320
Y1	0.300
Y2	0.106
Y3	0.900



X1-DFN1010-6 (Type B) Package Outline Dimensions and Suggested Pad Layout



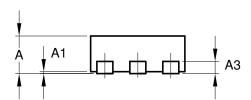
X1-DFN1010-6						
D:	(Type B)					
Dim	Min	Max	Тур			
Α	1	0.50	0.39			
A 1	-	0.04	-			
b	0.12	0.20	0.15			
D	0.95	1.050	1.00			
Е	0.95	1.050	1.00			
е	0.35 BSC					
e1	0.55 BSC					
L3	0.27	0.30	0.30			
L3a	0.32	0.40	0.35			
All	All Dimensions in mm					

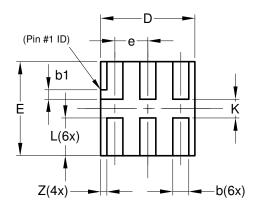


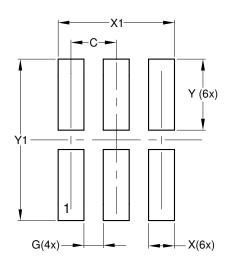
Dimensions	Value	
Dillielisions	(in mm)	
С	0.350	
G	0.150	
G1	0.150	
Х	0.200	
X1	0.900	
Υ	0.500	
Y1	0.525	
Y2	0.475	
Y3	1.150	



X2-DFN1010-6 Package Outline Dimensions and Suggested Pad Layout





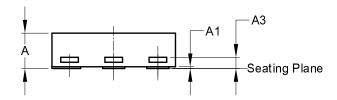


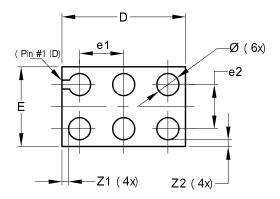
X2-DFN1010-6				
Dim	Min	Max	Тур	
Α	_	0.40	0.39	
A 1	0.00	0.05	0.02	
A3	_		0.13	
b	0.14	0.20	0.17	
b1	0.05	0.15	0.10	
D	0.95	1.05	1.00	
E	0.95	1.05	1.00	
е	_	_	0.35	
L	0.35	0.45	0.40	
K	0.15			
Z			0.065	
All Dimensions in mm				

Dimensions	Value (in mm)	
С	0.350	
G	0.150	
X	0.200	
X1	0.900	
Υ	0.550	
Y1	1.250	

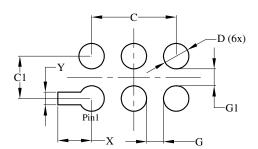


X2-DFN1409-6 Package Outline Dimensions and Suggested Pad Layout





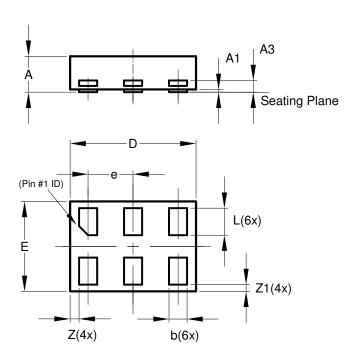
X2-DFN1409-6				
Dim	Min	Max	Тур	
Α	-	0.40	0.39	
A1	0	0.05	0.02	
A3	-	-	0.13	
Ø	0.20	0.30	0.25	
D	1.35	1.45	1.40	
Е	0.85	0.95	0.90	
e1	-	1	0.50	
e2	-	-	0.50	
Z 1	-	-	0.075	
Z2	-	-	0.075	
All Dimensions in mm				



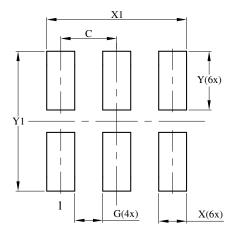
Dimensions	Value (in mm)
С	1.000
C1	0.500
D	0.300
G	0.200
G1	0.200
X	0.400
Υ	0.150



X2-DFN1410-6 Package Outline Dimensions and Suggested Pad Layout



X2-DFN1410-6			
Dim	Min	Max	Тур
Α	_	0.40	0.39
A1	0.00	0.05	0.02
A3	_	_	0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
Е	0.95	1.05	1.00
е			0.50
L	0.25	0.35	0.30
Z			0.10
Z 1	0.045	0.105	0.075
All Dimensions in mm			



Dimensions	Value (in mm)
С	0.500
G	0.250
Х	0.250
X1	1.250
Y	0.525
Y1	1.250



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2015, Diodes Incorporated

www.diodes.com

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Inverters category:

Click to view products by Diodes Incorporated manufacturer:

Other Similar products are found below:

5962-8550101CA E5-652Z NL17SGU04P5T5G NL17SZ14P5T5G NLX2G04BMX1TCG 412327H 022413E NL17SG14AMUTCG NLU2G04AMUTCG NLU2G04AMUTCG NLV14049UBDR2G NLV14069UBDTR2G NLV17SZ14DFT2G NLVVHC1G05DFT2G 74LVC2G17FW4-7 NLU2G04CMX1TCG NLV17SZ06DFT2G NLV27WZ04DFT2G NLV74HCT14ADTR2G NLX2G14CMUTCG NLU1G04AMX1TCG SNJ54ACT14W SNJ54AC04W NCV1729SN35T1G TC74VHC04FK(EL,K) NLV74HC04ADTR2G NLV17SZ04DFT2G 74AUP2G04FW3-7 NLU1G04AMUTCG NLX2G04CMUTCG NLX2G04AMUTCG NLV74ACT00DR2G NLV74AC14DR2G NLV37WZ14USG NLV27WZ04DFT1G NLV14106BDG NLU1GU04CMUTCG NLU1GT14AMUTCG NLU1G04CMUTCG NL17SZ04DFT5G NL17SZ14DFT2G 74LVC06ADTR2G 74LVC04ADR2G TC7SZ04AFS,L3J NLU1GT04AMUTCG NLV37WZ04USG NLX3G14FMUTCG NL17SZ04P5T5G NL17SG14P5T5G NLV27WZU04DFT2G