



Pin Assignments

Applications

Voltage Level Shifting

General Purpose Logic Power-Down Signal Isolation

Range

Wide Array of Products such as:

74LVC1G00Q

SINGLE 2 INPUT POSITIVE NAND GATE

Description

The 74LVC1G00Q is an automotive compliant, single, 2-input positive NAND gate with a standard push-pull output. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed-voltage environment. The device is fully specified for partial power-down applications using IOFF. The IOFF circuitry disables the output preventing damaging current backflow when the device is powered down.

The gate performs the positive Boolean function:

$$Y = \overline{A \bullet B}$$
 or $Y = \overline{A} + \overline{B}$

(Top View) A 1 5 Vcc B 2 6ND 3 4 Y

SOT 25 / SOT 353

Automotive Applications within Grade 1 Temperature

Industrial Computing/Controls/Automation High-Reliability Networking/Communications

Industrial/Agricultural Equipment

Features

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Wide Supply Voltage Range from 1.65V to 5.5V
- ±24mA Output Drive at 3.3V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs Accept up to 5.5V
- ESD Protection Tested per AEC-Q100
- Exceeds 2000V Human Body Model (AEC-Q100-002)
- Exceeds 1000V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74LVC1G00Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Notes:

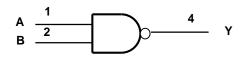
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Pin Descriptions

Pin Name	Description
А	Data Input
В	Data Input
GND	Ground
Y	Data Output
Vcc	Supply Voltage

Logic Diagram



Function Table

Inp	Output	
Α	В	Y
Н	Н	L
L	Х	Н
Х	L	Н

Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to Vcc +0.5	V
Ік	Input Clamp Current VI < 0	-50	mA
Іок	Output Clamp Current	-50	mA
lo	Continuous Output Current	±50	mA
ICC, IGND	Continuous Current Through V _{CC} or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
Tstg	Storage Temperature	-65 to +150	°C

Notes: 4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



Recommended Operating Conditions (Note 6)

Symbol	Pa	rameter	Min	Max	Unit	
Mara	Operating Voltage	Operating	1.65	5.5	V	
Vcc	Operating voltage	Data Retention Only	1.5	—	V	
		Vcc = 1.65V to 1.95V	$0.65 \times Vcc$	—		
Max		Vcc = 2.3V to 2.7V	1.7	—	V	
Vін	High-Level Input Voltage	$V_{CC} = 3V$ to 3.6V	2	—	v	
		V _{CC} = 4.5V to 5.5V	$0.7 \times V_{CC}$	—		
		V _{CC} = 1.65V to 1.95V	—	$0.35 \times V_{CC}$		
		V _{CC} = 2.3V to 2.7V	_	0.7		
VIL	Low-Level Input Voltage	V _{CC} = 3V to 3.6V	_	0.8	V	
		V _{CC} = 4.5V to 5.5V	_	$0.3 \times V_{CC}$		
VI	Input Voltage	<u>.</u>	0	5.5	V	
Vo	Output Voltage		0	Vcc	V	
			Vcc = 1.65V	—	-4	
		Vcc = 2.3V	_	-8		
Let.	High-Level Output Current	Vcc = 2.7V	—	-12	mA	
Юн		<u>)/ </u>	_	-16	ma	
		$V_{CC} = 3V$	—	-24		
		$V_{CC} = 4.5V$	—	-32		
		$V_{CC} = 1.65 V$	—	4		
		$V_{CC} = 2.3 V$	—	8		
Iol	Low-Level Output Current	Vcc = 2.7V	—	12	mA	
IOL		Vcc = 3V	_	16	IIIA	
		VCC = 3V	—	24		
		$V_{CC} = 4.5V$	—	32		
		$V_{CC} = 1.8V \pm 0.15V, 2.5V \pm 0.2V$	—	20		
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 3.3V \pm 0.3V$	—	10	ns/V	
		$V_{CC} = 5V \pm 0.5V$	—	5		
TA	Operating Free-Air Temperature	—	-40	+125	°C	

Note: 6. Unused inputs should be held at V_{CC} or Ground.



Electrical Characteristics (All typical values are at V_{CC} = 3.3V, T_A = +25°C)

Currente e l	Devenueter	Test Ca			-40°	C to +125	°C	11
Symbol	Parameter	Test Co	onditions	tions Vcc		Тур	Max	Unit
			Іон = -100µА	1.65V to 5.5V	Vcc – 0.1	-		
			Iон = -4mA	1.65V	0.95	-		
Mari	High Level Output Voltage	VI = VIH or VIL	Iон = -8mA	2.3V	1.7	-		v
Vон			Іон = -12mA	2.7V	1.9	—		v
			Іон = -24mA	3V	2.0	—	—	
			I _{OH} = -32mA	4.5V	3.4	—	—	
		v Level Output Voltage VI = VIH or VIL	I _{OL} = 100μA	1.65V to 5.5V	—	—	0.10	
			IoL = 4mA	1.65V	_	_	0.70	v
.,			IoL = 8mA	2.3V	—	—	0.45	
Vol	Low Level Output Voltage		$I_{OL} = 12mA$	2.7V	—	—	0.60	
			$I_{OL} = 24mA$	3V	—	_	0.80	
			IoL = 32mA	4.5V			0.80	
h	Input Current	VI = 5.5V or GN	ID	0 to 5.5V	_	±0.1	±1	μA
IOFF	Power Down Leakage Current	V_1 or $V_0 = 5.5V$		0V	—	_	±2	μA
Icc	Supply Current	V _I = 5.5V or GND Io = 0		5.5V	_	0.1	4	μA
ΔΙcc	Additional Supply Current	One input at $V_{CC} - 0.6V$ Other inputs at V_{CC} or GND		3V to 5.5V	—	_	500	μA
Cı	Input Capacitance	$V_I = GND$ to V_C	c	3.3V	_	5.0	_	pF

Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Тур	Max	Unit
0	Thermal Resistance	SOT25	Nata 7	_	184		9 0 AA/
θ_{JA}	Junction-to-Ambient	SOT353	Note 7	—	385	_	°C/W
0.5	Thermal Resistance	SOT25	Nata 7	_	62	_	9 0 AA
θις	Junction-to-Case	SOT353	Note 7	_	164	_	°C/W

Note: 7. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

Figure 1 Typical Values at $T_A = +25^{\circ}C$ and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V.

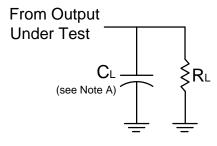
Parameter	From	То	Vcc	T _A = -40°C to +125°C			Unit
Faranieter	Input	Output	VCC	Min	Тур	Max	Onit
			1.8V ± 0.15V	1.0	3.3	10.5	
			$2.5V \pm 0.2V$	0.5	2.2	7.0	
tPD	A or B	Y	2.7V	0.5	2.6	7.5	ns
			3.3V ± 0.3V	0.5	2.2	6.0	
			$5.0V \pm 0.5V$	0.5	1.8	5.5	

Operating Characteristics

T_A = +25°C Vcc = 1.8V Vcc = 2.5V Vcc = 3.3V Vcc = 5VTest Parameter Unit Conditions Тур Тур Тур Тур Power Dissipation pF CPD f = 10MHz15 16 16 16 Capacitance



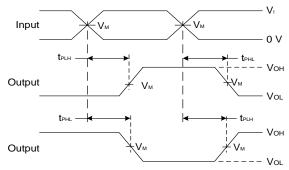
Measurement Information



Vcc	In	puts	VM	CL	RL
۷CC	VI	tr/tr	٧M	OL .	κ <u>ι</u>
1.8V ± 0.15V	Vcc	≤2ns	Vcc/2	30pF	1kΩ
2.5V ± 0.2V	Vcc	≤2ns	Vcc/2	30pF	500Ω
2.7V	Vcc	≤2.5ns	1.5V	50pF	500Ω
3.3V ± 0.3V	3.0V	≤2.5ns	1.5V	50pF	500Ω
5.0V ± 0.5V	Vcc	≤2.5ns	Vcc/2	50pF	500Ω







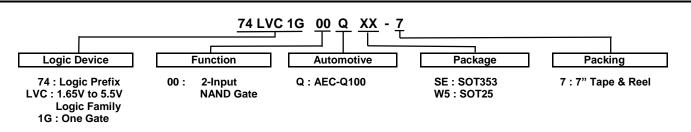
Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

- Notes: A. Includes test lead and test apparatus capacitance.
 - B. All pulses are supplied at pulse repetition rate \leq 10MHz.
 - C. Inputs are measured separately one transition per measurement.
 - D. t_{PLH} and t_{PHL} are the same as t_{PD} .



Ordering Information (Note 8)



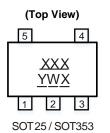
Part Number	Package	Package	Package	7" Tape	and Reel
Fait Nulliber	Code	(Notes 9 & 10)	Size	Quantity	Part Number Suffix
74LVC1G00QSE-7	SE	SOT353	2.0mm × 2.0mm × 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7
74LVC1G00QW5-7	W5	SOT25	3.0mm × 2.8mm × 1.2mm 0.95mm lead pitch	3000/Tape & Reel	-7

8. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.
 The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

Marking Information

Notes:



 $\begin{array}{rcl} \underline{XXX} & : & \text{Identification Code} \\ \underline{Y} & : & \text{Year } 0 \text{-}9 \\ \underline{W} & : & \text{Week} : A \text{-} Z & 1 \text{-} 26 & \text{week} \\ & a \text{-} z & 27 \text{-} 52 & \text{week} \\ & z & \text{represents week} 52 & \text{and} 53 \\ \underline{X} & : & \text{A} \text{-} Z : & \text{Internal Code} \end{array}$

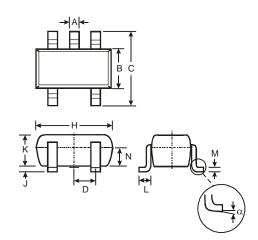
Part Number	Package	Identification Code
74LVC1G00QW5-7	SOT25	USQ
74LVC1G00QSE-7	SOT353	USQ



Package Outline Dimensions

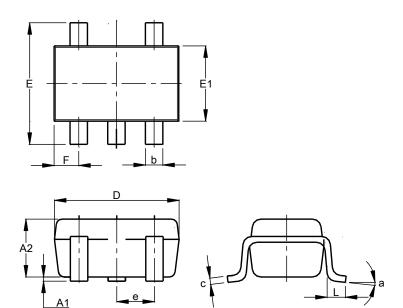
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



	SO	25				
Dim	Min	Min Max T				
Α	0.35	0.50	0.38			
В	1.50	1.70	1.60			
С	2.70	3.00	2.80			
D	-	-	0.95			
Н	2.90	3.10	3.00			
J	0.013	0.10	0.05			
К	1.00	1.30	1.10			
L	0.35	0.55	0.40			
М	0.10	0.20	0.15			
Ν	0.70	0.80	0.75			
α	0°	8°	-			
All D)imensi	ons in	mm			

(2) Package Type: SOT353



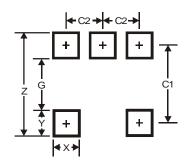
	SC	T353	
Dim	Min	Max	Тур
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.10	0.30	0.25
Ċ	0.10	0.22	0.11
D	1.80	2.20	2.15
Ш	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0).650 B	SC
F	0.40	0.45	0.425
L	0.25	0.40	0.30
а	0°	8°	
All	Dimen	sions	in mm



Suggested Pad Layout

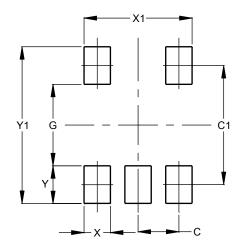
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95

(2) Package Type: SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Y	0.600
Y1	2.500

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 3
- Weight: 0.0158 grams (Approximate)

SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 3
- Weight: 0.0064 grams (Approximate)



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