



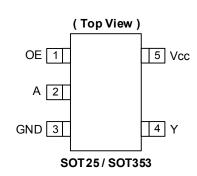
## 74LVC1G126Q

#### SINGLE BUFFER GATE WITH 3-STATE OUTPUT

### Description

The 74LVC1G126Q is an automotive-compliant, single, non-inverting buffer/bus driver with a 3-state output. The output enters a high-impedance state when a LOW level is applied to the output enable (OE) pin. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V, allowing this device to be used in a mixed-voltage environment. The device is fully specified for partial power down applications using I<sub>OFF</sub>. The I<sub>OFF</sub> circuitry disables the output preventing damaging current backflow when the device is powered down.

#### **Pin Assignments**



### Features

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Wide Supply Voltage Range from 1.65V to 5.5V
- ±24mA Output Drive at 3.3V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs Accept up to 5.5V
- ESD Protection Tested per AEC-Q100
  - Exceeds 2000V Human Body Model (AEC-Q100-002)
  - Exceeds 1000V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The 74LVC1G126Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

#### Applications

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products, such as:
  - Automotive Applications within Grade 1 Temperature Range
  - Industrial Computing/Controls/Automation
  - High Reliability Networking/Communications
  - Industrial/Agricultural Equipment

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

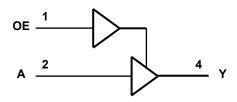
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



## **Pin Descriptions**

Pin Name	Description	
OE	Output Enable Active HIGH	
А	Data Input	
GND	Ground	
Y	Data Output	
V <sub>CC</sub>	Supply Voltage	

# Logic Diagram



# **Function Table**

Inp	Output	
OE	Α	Y
Н	Н	Н
Н	L	L
L	Х	Z

## Absolute Maximum Ratings (Notes 4 & 5)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
V <sub>CC</sub>	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.5 to V <sub>CC</sub> +0.5	V
lıк	Input Clamp Current VI < 0	-50	mA
Іок	Output Clamp Current	-50	mA
lo	Continuous Output Current	±50	mA
I <sub>CC</sub> , I <sub>GN</sub>	Continuous Current Through V <sub>CC</sub> or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C

Notes: 4. Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

5. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



# Recommended Operating Conditions (Note 6)

Symbol		Parameter	Min	Мах	Unit
V	Operating Voltage	Operating	1.65	5.5	V
V <sub>CC</sub>	Operating voltage	Data Retention Only	1.5	—	V
		V <sub>CC</sub> = 1.65V to 1.95V	$0.65 \times V_{CC}$	_	
V	High-Level Input Voltage	V <sub>CC</sub> = 2.3V to 2.7V	1.7	_	V
VIH	High-Level liput voltage	$V_{CC}$ = 3V to 3.6V	2	—	v
		V <sub>CC</sub> = 4.5V to 5.5V	$0.7 \times V_{CC}$	—	
		V <sub>CC</sub> = 1.65V to 1.95V	—	$0.35 \times V_{CC}$	
		V <sub>CC</sub> = 2.3V to 2.7V	_	0.7	
VIL	Low-Level Input Voltage	V <sub>CC</sub> = 3V to 3.6V	_	0.8	V
		V <sub>CC</sub> = 4.5V to 5.5V	_	$0.3  imes V_{CC}$	
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	Vcc	V
		V <sub>CC</sub> = 1.65V	_	-4	
		V <sub>CC</sub> = 2.3V	—	-8	
	High-Level Output Current	V <sub>CC</sub> = 2.7V	—	-12	mA
I <sub>OH</sub>		$\gamma = 2\gamma$	_	-16	IIIA
		V <sub>CC</sub> = 3V	_	-24	
		$V_{CC} = 4.5V$	—	-32	
		V <sub>CC</sub> = 1.65V	—	4	
		$V_{CC} = 2.3V$	—	8	
L.	Low-Level Output Current	V <sub>CC</sub> = 2.7V	—	12	mA
I <sub>OL</sub>		$\lambda = -2\lambda $	_	16	IIIA
		V <sub>CC</sub> = 3V		24	
		$V_{CC} = 4.5V$	—	32	
		$V_{CC}$ = 1.8V ± 0.15V, 2.5V ± 0.2V	—	20	
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{\rm CC} = 3.3 \text{V} \pm 0.3 \text{V}$	—	10	ns/V
		$V_{CC} = 5V \pm 0.5V$	—	5	
T <sub>A</sub>	Operating Free-Air Temperature	—	-40	+125	°C

Note: 6. Unused inputs should be held at V<sub>CC</sub> or Ground.



# **Electrical Characteristics** (All typical values are at $V_{CC}$ = 3.3V, $T_A$ = +25°C)

Queen had	Devenueter	Test	u diti o u o	Vcc	-40°	C to +125	°C	11
Symbol	Parameter	Test Co	Test Conditions		Min	Тур	Max	Unit
			I <sub>OH</sub> = -100µА	1.65V to 5.5V	V <sub>CC</sub> -0.1	_	_	
			I <sub>OH</sub> = -4mA	1.65V	0.95	_	_	
V	High Level Output Voltage	$\lambda = \lambda = r \lambda$	I <sub>OH</sub> = -8mA	2.3V	1.7	-	_	V
V <sub>OH</sub>		$V_I = V_{IH} \text{ or } V_{IL}$	I <sub>OH</sub> = -12mA	2.7V	1.9	—	-	v
			I <sub>OH</sub> = -24mA	3V	2.0	_	_	
			I <sub>OH</sub> = -32mA	4.5V	3.4	—	—	
			I <sub>OL</sub> = 100μA	1.65V to 5.5V	—	—	0.10	
		w Level Output Voltage $V_I = V_{IH}$ or $V_{IL}$	I <sub>OL</sub> = 4mA	1.65V	—	—	0.70	v
.,			I <sub>OL</sub> = 8mA	2.3V	—	-	0.45	
V <sub>OL</sub>	Low Level Output Voltage		I <sub>OL</sub> = 12mA	2.7V	—	—	0.60	
			I <sub>OL</sub> = 24mA	3V	—	—	0.80	
			I <sub>OL</sub> = 32mA	4.5V	_		0.80	
lı –	Input Current	VI = 5.5V or GN	ID	0 to 5.5V	—	±0.1	±1	μA
I <sub>OFF</sub>	Power Down Leakage Current	$V_1$ or $V_0$ = 5.5V		0V	—	_	±2	μA
I <sub>OZ</sub>	Z-State Leakage Current	V <sub>O</sub> = Ground to	5.5V	3.6V	_	—	±2	μA
Icc	Supply Current	V <sub>I</sub> = 5.5V or GND I <sub>O</sub> = 0		5.5V	—	0.1	4	μA
ΔI <sub>CC</sub>	Additional Supply Current	One input at $V_{CC} - 0.6V$ Other inputs at $V_{CC}$ or GND		3V to 5.5V	—	—	500	μA
CI	Input Capacitance	$V_I = GND$ to $V_C$	C	3.3V	_	5.0	_	pF

# Package Characteristics

Symbol	Parameter Package Test Conditions		Min	Тур	Мах	Unit	
0	Thermal Resistance	SOT25	N ( 7	_	184	_	°C/W
θ <sub>JA</sub> J	Junction-to-Ambient	SOT353	Note 7	_	385	—	
0	Thermal Resistance	SOT25		_	62	_	°C/W
θJC	Junction-to-Case	SOT353	Note 7	_	164	_	

Note: 7. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



# **Switching Characteristics**

Boromotor	Parameter From To Input Output	N .	1	Unit					
Parameter		V <sub>CC</sub>	Min	Тур	Max	Onic			
			1.8V ± 0.15V	1.0	3.0	10.5			
			2.5V ± 0.2V	0.5	2.1	7.0			
t <sub>PD</sub>	А	Y	2.7V	0.5	2.3	7.0	ns		
			3.3V ± 0.3V	0.5	2.0	6.0			
			5.0V ± 0.5V	0.5	1.7	5.5			
		1.8 V ± 0.15V	1.0	3.2	12.0				
			2.5V ± 0.2V	0.5	2.2	8.5			
t <sub>EN</sub>	OE	OE	OE	Y	2.7V	0.5	2.4	8.5	ns
			3.3V ± 0.3V	0.5	2.1	7.0			
			5.0V ± 0.5V	0.5	1.6	6.5			
			1.8V ± 0.15V	1.0	4.3	12.0			
			2.5V ± 0.2V	0.5	2.7	7.0			
t <sub>DIS</sub>	t <sub>DIS</sub> OE	Y	2.7V	0.5	3.4	7.0	ns		
			3.3V ± 0.3V	0.5	3.0	7.0			
			5.0V ± 0.5V	0.5	2.2	5.5			

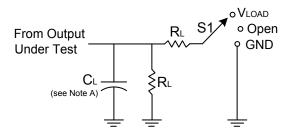
# **Operating Characteristics**

T<sub>A</sub> = +25°C

	Parameter		Test Conditions	V <sub>CC</sub> = 1.8V Typ	V <sub>CC</sub> = 2.5V Typ	V <sub>CC</sub> = 3.3V Typ	V <sub>CC</sub> = 5V Typ	Unit
<u> </u>	Power Dissipation	Outputs Enabled	f - 10MH-7	19	19	19	21	pF
C <sub>PD</sub>	Capacitance	Outputs Disabled	f = 10MHz -	2	2	3	4	μL

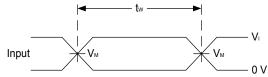


### **Parameter Measurement Information**

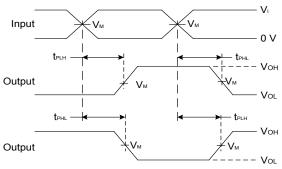


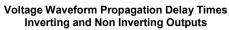
TEST	S1
t <sub>PLH</sub> /t <sub>PHL</sub>	Open
t <sub>PLZ</sub> /t <sub>PZL</sub>	V <sub>LOAD</sub>
t <sub>PHZ</sub> /t <sub>PZH</sub>	GND

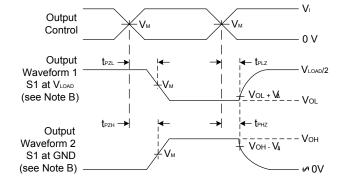
N N	Inputs				6		N/A
V <sub>cc</sub>	VI	t <sub>R</sub> /t <sub>F</sub>	VM	V <sub>LOAD</sub>	C∟	RL	VΔ
1.8V±0.15V	Vcc	≤2ns	V <sub>CC</sub> /2	$2 \times V_{CC}$	30pF	1kΩ	0.15V
2.5V±0.2V	V <sub>CC</sub>	≤2ns	V <sub>CC</sub> /2	$2 \times V_{CC}$	30pF	500Ω	0.15V
2.7V	2.7V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
3.3V±0.3V	3V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
5V±0.5V	Vcc	≤2.5ns	V <sub>CC</sub> /2	$2 \times V_{CC}$	50pF	500Ω	0.3V



Voltage Waveform Pulse Duration







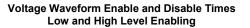


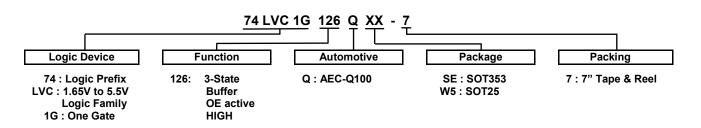
Figure 1. Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate  $\leq$  10MHz.
- C. Inputs are measured separately one transition per measurement.
- D.  $t_{\text{PLZ}}$  and  $t_{\text{PHZ}}$  are the same as  $t_{\text{DIS.}}$
- E. t<sub>PZL</sub> and t<sub>PZH</sub> are the same as t<sub>EN.</sub>
- F.  $t_{\text{PLH}}$  and  $t_{\text{PHL}}$  are the same as  $t_{\text{PD.}}$



#### Ordering Information (Note 8)

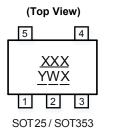


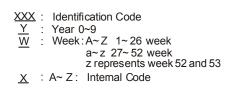
Part Number	Package	Package	Package	7" Tape and Reel		
Fait Nulliber	Code	(Notes 9 & 10)	Size	Quantity	Part Number Suffix	
74LVC1G126QSE-7	SE	SOT353	2.15mm × 2.1mm × 1.1mm 0.65mm lead pitch	3000/Tape & Reel	-7	
74LVC1G126QW5-7	W5	SOT25	3.0mm × 2.8mm × 1.2mm 0.95mm lead pitch	3000/Tape & Reel	-7	

Notes:

For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.
 Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.
 The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.

## **Marking Information**





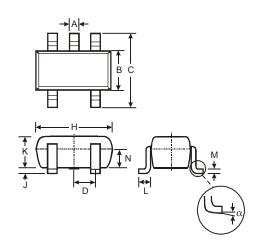
Part Number	Package	Identification Code
74LVC1G126QW5-7	SOT25	UZQ
74LVC1G126QSE-7	SOT353	UZQ



# **Package Outline Dimensions**

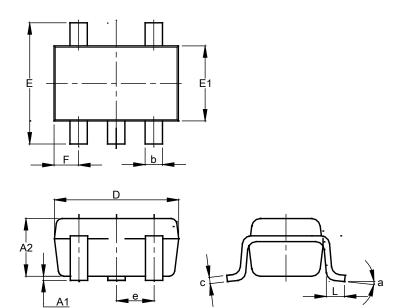
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOT25



SOT25				
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
С	2.70	3.00	2.80	
D	-	-	0.95	
н	2.90	3.10	3.00	
J	0.013	0.10	0.05	
κ	1.00	1.30	1.10	
L	0.35	0.55	0.40	
м	0.10	0.20	0.15	
Ν	0.70	0.80	0.75	
α	0°	8°	-	
All Dimensions in mm				

#### (2) Package Type: SOT353



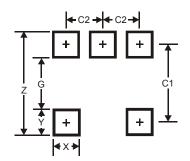
SOT353				
Dim	Min	Max	Тур	
A1	0.00	0.10	0.05	
A2	0.90	1.00	0.95	
b	0.10	0.30	0.25	
С	0.10	0.22	0.11	
D	1.80	2.20	2.15	
Е	2.00	2.20	2.10	
E1	1.15	1.35	1.30	
е	0.650 BSC			
F	0.40	0.45	0.425	
L	0.25	0.40	0.30	
а	0°	8°		
All Dimensions in mm				



# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### (1) Package Type: SOT25



 Dimensions
 Value

 Z
 3.20

 G
 1.60

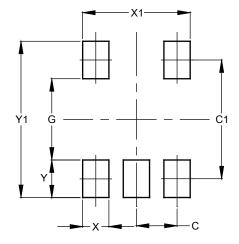
 X
 0.55

 Y
 0.80

 C1
 2.40

 C2
 0.95

#### (2) Package Type: SOT353



Dimensions	Value	
Dimensions	(in mm)	
С	0.650	
C1	1.900	
G	1.300	
Х	0.420	
X1	1.720	
Ŷ	0.600	
Y1	2.500	

## **Mechanical Data**

#### SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208
- Weight: 0.0158 grams (Approximate)

#### SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208<sup>(3)</sup>
- Weight: 0.0064 grams (Approximate)



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