



ADC114YUQ

NPN PRE-BIASED SMALL SIGNAL DUAL SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction
- Built-In Biasing Resistors
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

R ₁ (NOM)	R ₂ (NOM)
10kΩ	47kΩ

SOT363



Top View

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (@3)
- Weight: 0.006 grams (Approximate)

Device Schematic

Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ADC114YUQ-7	Automotive	1Y7	7	8	3,000
ADC114YUQ-13	Automotive	1Y7	13	8	10,000

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

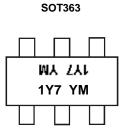
 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen - and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/quality/product_compliance_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



1Y7 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: E = 2017) M = Month (ex: 9 = September)

Date Code Key												
Year	2016	2017	2018	2019	9 202	20 20)21	2022	2023	2024	2025	2026
Code	D	E	F	G	Н			J	K	L	М	Ν
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage	V _{CC}	50	V
Input Voltage	V _{IN}	-6 to +40	V
Output Current	I _{C(MAX)}	100	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

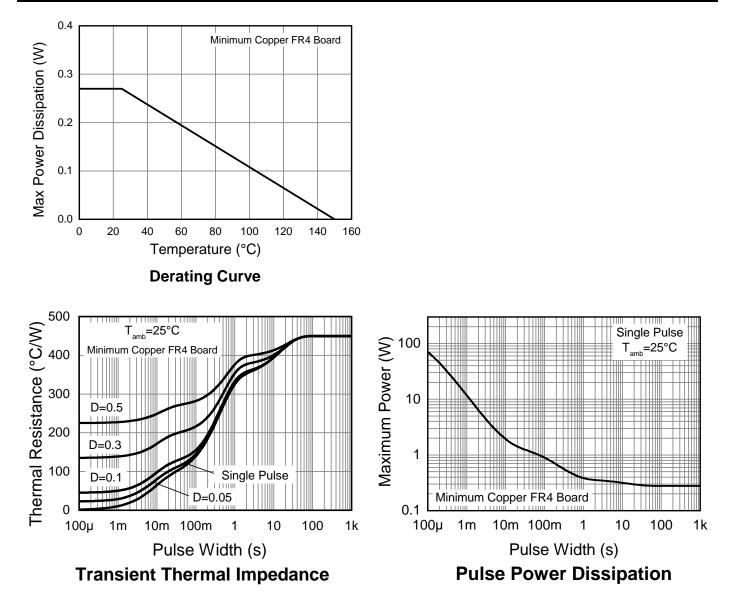
Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 6 & 7)	PD	270	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	R _{θJA}	450	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes: 6. Mounted on FR4 PC Board with minimum recommended pad layout.

7. 150mW per element must not be exceeded.



Thermal Characteristics and Derating Information



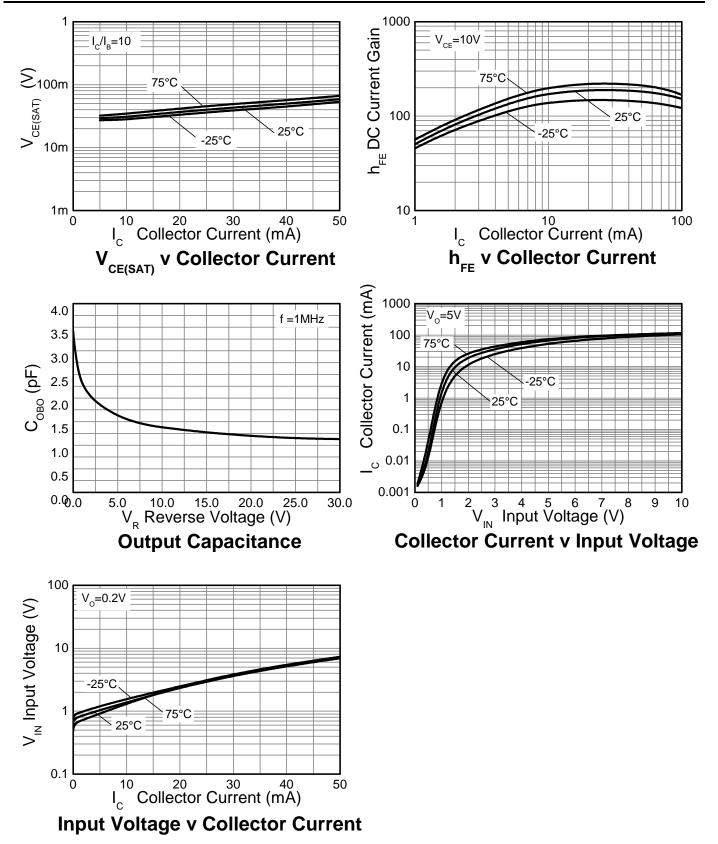


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Input Voltage	V _{L(OFF)}	0.3	_	—	V	$V_{CC} = 5V, I_{O} = 100\mu A$
input voltage	V _{L(ON)}	_	_	1.4	v	$V_0 = 0.3V, I_0 = 1mA$
Output Voltage	V _{O(ON)}	_	0.1	0.3	V	$I_0/I_L = 5mA / 0.25mA$
Input Current	۱L	_	_	0.88	mA	$V_I = 5V$
Output Current	I _{O(OFF)}	_	_	0.5	μΑ	$V_{CC} = 50V, V_{I} = 0V$
DC Current Gain	GL	80	_	—	_	$V_0 = 5V, I_0 = 5mA$
Input Resistor (R1) Tolerance	ΔR_1	-30	—	+30	%	—
Resistance Ratio Tolerance	$\Delta(R_2/R_1)$	-20	_	+20	%	—
Gain-Bandwidth Product	f⊤	_	250		MHz	V _{CE} = 10V, I _E = 5mA, f = 100MHz



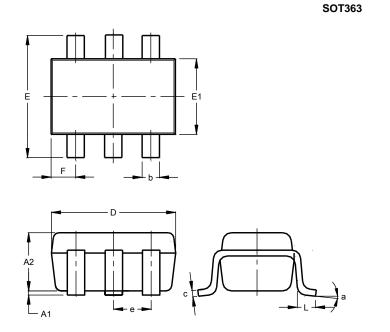
Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)





Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

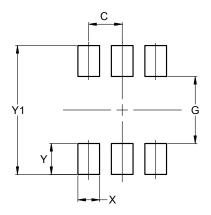


SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	1.00				
b	0.10	0.30	0.25				
С	0.10	0.22	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	e 0.650 BSC						
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500



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