NOT RECOMMENDED FOR NEW DESIGN

AL8805
HIGH EFFICIENCY 36V 1A BUCK LED DRIVER

## Description

The AL8805 is a step-down DC/DC converter designed to drive LEDs with a constant current. The device can drive up to 8 LEDs, depending on the forward voltage of the LEDs, in series from a voltage source of 6 V to 36 V . Series connection of the LEDs provides identical LED currents resulting in uniform brightness and eliminating the need for ballast resistors. The AL8805 switches at frequency up to 1 MHz . This allows the use of small size external components, hence minimizing the PCB area needed.

Maximum output current of AL8805 is set via an external resistor connected between the $\mathrm{V}_{\mathbb{I}}$ and SET input pins. Dimming is achieved by applying either a DC voltage or a PWM signal at the CTRL input pin. An input voltage of 0.4 V or lower at CTRL switches off the output MOSFET simplifying PWM dimming.

## Features

- LED Driving Current up to 1 A
- Better than 5\% Accuracy
- High Efficiency up to $98 \%$
- Operating Input Voltage from 6 V to 36 V
- High Switching Frequency up to 1 MHz
- PWM/DC Input for Dimming Control
- Built-In Output Open-Circuit Protection
- SOT25: Available in "Green" Molding Compound (No Br,Sb) with lead Free Finish/ RoHS Compliant
- Totally Lead-Free \& Fully RoHS Compliant (Notes 1 \& 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Notes: $\quad$ 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) \& 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain $<900 \mathrm{ppm}$ bromine, $<900 \mathrm{ppm}$ chlorine ( $<1500 \mathrm{ppm}$ total $\mathrm{Br}+\mathrm{Cl}$ ) and <1000ppm antimony compounds.

## Typical Applications Circuit



## Pin Descriptions

| Pin Number | Pin Name | Function |
| :---: | :---: | :---: |
| 1 | SW | Switch Pin. Connect inductor/freewheeling diode here, minimizing track length at this pin to reduce EMI. |
| 2 | GND | GND Pin |
| 3 | CTRL | Dimming and On/Off Control Input. <br> - Leave floating for normal operation. <br> $\left(\mathrm{V}_{\text {CTRL }}=\mathrm{V}_{\text {REF }}=2.5 \mathrm{~V}\right.$ giving nominal average output current loutnom $\left.=0.1 / \mathrm{R}_{\mathrm{s}}\right)$ <br> - Drive to voltage below 0.4 V to turn off output current <br> - Drive with DC voltage ( $0.5 \mathrm{~V}<\mathrm{V}_{\text {CTRL }}<2.5 \mathrm{~V}$ ) to adjust output current from $20 \%$ to $100 \%$ of IouTnom <br> - A PWM signal (low level $\leq 0.4 \mathrm{~V}$ and high level $>2.6$; transition times less than 1 us) allows the output current to be adjusted below the level set by the resistor connected to SET input pin. |
| 4 | SET | Set Nominal Output Current Pin. Configure the output current of the device. |
| 5 | $\mathrm{V}_{\text {IN }}$ | Input Supply Pin. Must be locally decoupled to GND with $\geq 2.2 \mu \mathrm{~F} \times 7 \mathrm{R}$ ceramic capacitor-see applications section for more information. |

## Absolute Maximum Ratings $\left(@ T_{A}=+25^{\circ} \mathrm{C}\right.$, unless otherwise specified.)

| Symbol | Parameter | Ratings | Unit |
| :---: | :--- | :---: | :---: |
| ESD HBM | Human Body Model ESD Protection | 2.5 | kV |
| ESD MM | Machine Model ESD Protection | 200 | V |
| $\mathrm{~V}_{\text {IN }}$ | Continuous $\mathrm{V}_{\text {IN }}$ Pin Voltage Relative to GND | -0.3 to 40 | V |
| $\mathrm{~V}_{\text {SET }}$ | SET Pin Voltage Relative to $\mathrm{VIN}^{\prime}$ Pin | -5 to +0.3 | V |
| $\mathrm{~V}_{\text {SW }}$ | SW Voltage Relative to GND | -0.3 to 40 | V |
| $\mathrm{~V}_{\text {CTRL }}$ | CTRL Pin Input Voltage | -0.3 to 6 | V |
| $\mathrm{I}_{\text {SW-DC }}$ | DC or RMS Switch Current | 1.25 | A |
| $\mathrm{I}_{\text {SW-PK }}$ | Peak Switch Current (<10\%) | 2.5 | A |
| $\mathrm{~T}_{\mathrm{J}}$ | Junction Temperature | 150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {LEAD }}$ | Lead Temperature Soldering | 300 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\text {ST }}$ | Storage Temperature Range | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |

Caution: Stresses greater than the 'Absolute Maximum Ratings' specified above, may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions exceeding those indicated in this specification is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time.
Semiconductor devices are ESD sensitive and may be damaged by exposure to ESD events. Suitable ESD precautions should be taken when handling and transporting these devices

Recommended Operating Conditions $\left(\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}\right.$, unless otherwise specified.)

| Symbol | Parameter | Min | Max | Unit |
| :---: | :--- | :---: | :---: | :---: |
| $\mathrm{V}_{\text {IN }}$ | Operating Input Voltage relative to GND | 6.0 | 36 | V |
| $\mathrm{~V}_{\text {CTRLH }}$ | Voltage High for PWM Dimming Relative to GND | 2.6 | 5.5 | V |
| $\mathrm{~V}_{\text {CTRLD }}$ | Voltage Range for 20\% to 100\% DC Dimming Relative to GND | 0.5 | 2.5 | V |
| $\mathrm{~V}_{\text {CTRLL }}$ | Voltage Low for PWM Dimming Relative to GND | 0 | 0.4 | V |
| $\mathrm{I}_{\text {SW }}$ | Continuous Switch Current | - | 1 | A |
| $\mathrm{~T}_{\mathrm{J}}$ | Junction Temperature Range | -40 | 125 | ${ }^{\circ} \mathrm{C}$ |

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Electrical Characteristics $\left(\mathrm{V}_{\mathbb{N}}=12, @ T_{A}=+25^{\circ} \mathrm{C}\right.$, unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\text {INSU }}$ | Internal Regulator Start Up Threshold | $\mathrm{V}_{\text {IN }}$ rising |  |  | 5.9 | V |
| $\mathrm{V}_{\text {INSH }}$ | Internal Regulator Hysteresis Threshold | $\mathrm{V}_{\text {IN }}$ falling | 100 |  | 300 | mV |
| $\mathrm{I}_{\mathrm{a}}$ | Quiescent Current | Output not switching (Note 4) |  |  | 350 | $\mu \mathrm{A}$ |
| $I_{s}$ | Input Supply Current | CTRL pin floating $\mathrm{f}=250 \mathrm{kHz}$ |  | 1.8 | 5 | mA |
| $\mathrm{V}_{\text {TH }}$ | Set Current Threshold Voltage |  | 95 | 100 | 105 | mV |
| $\mathrm{V}_{\text {TH-H }}$ | Set Threshold Hysteresis |  |  | $\pm 20$ |  | mV |
| $\mathrm{I}_{\text {SET }}$ | SET Pin Input Current | $\mathrm{V}_{\text {SET }}=\mathrm{V}_{\text {IN }}-0.1$ |  | 16 | 22 | $\mu \mathrm{A}$ |
| $\mathrm{R}_{\text {ctRL }}$ | CTRL Pin Input Resistance | Referred to internal reference |  | 50 |  | $\mathrm{k} \Omega$ |
| $\mathrm{V}_{\text {REF }}$ | Internal Reference Voltage |  |  | 2.5 |  | V |
| $\mathrm{R}_{\text {DS(on) }}$ | On Resistance of SW MOSFET | $\mathrm{I}_{\text {SW }}=1 \mathrm{~A}$ |  | 0.25 | 0.4 | $\Omega$ |
| Isw_Leakage | Switch Leakage Current | $\mathrm{V}_{\text {IN }}=30 \mathrm{~V}$ |  |  | 0.5 | $\mu \mathrm{A}$ |
| $\mathrm{f}_{\text {osc }}$ | Switching Frequency |  |  |  | 1 | MHz |
| $\theta_{\text {JA }}$ | Thermal Resistance Junction-to- <br> Ambient (Note 5) | SOT25 (Note 6) |  | 250 |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\Psi_{\text {JB }}$ | Thermal Resistance Junction-to-Lead (Note 7) | SOT25 |  | 50 |  |  |

Notes: 4. AL8805 does not have a low power standby mode but current consumption is reduced when output switch is inhibited: VSENSE $=0$ V. Parameter is tested with $\mathrm{V}_{\mathrm{C} T R L} \leq 2.5 \mathrm{~V}$
5. Refer to figure 34 for the device derating curve.
6. Test condition for SOT25: Device mounted on FR-4 PCB ( $25 \mathrm{~mm} \times 25 \mathrm{~mm} 10 z$ copper, minimum recommended pad layout on top layer and thermal vias to bottom layer ground plane. For better thermal performance, larger copper pad for heat-sink is needed.
7. As SOT25 doesn't have an exposed tab or exposed pad the majority of heat flow is though pin 2 down to ground.

## Typical Performance Characteristics (@T $\mathrm{A}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)



Figure 1 Supply Current (not switching) vs. Input Voltage


Figure 3 LED Current vs. $V_{\text {CTRL }}$


Figure 5. $\mathrm{V}_{\text {CTRL }}$ vs. Input Voltage (CTRL Pin Open Circuit)


Figure 2 Switching Frequency vs. $V_{\text {CTRL }}$


Figure $4 I_{\text {CTRL }}$ vs. $V_{\text {CTRL }}$


Figure $6 \mathrm{~V}_{\text {CTRL }}$ vs. Temperature

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## Typical Performance Characteristics (cont.) ( $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)



Figure 7 Led vs. PWM Duty Cycle


Figure $9 \mathrm{SW}_{\mathrm{DS}(\mathrm{ON})}$ Vs. Temperature



Figure $8 \mathrm{SW} \mathrm{R}_{\mathrm{DS}(\mathrm{ON})}$ vs. Input Voltage


Figure 10 SW Output Switching Characteristics


Figure 12 Duty Cycle vs. Input Voltage

## Typical Performance Characteristics (cont.) (@T $\mathrm{A}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)



Figure 13 Efficiency vs. Input Voltage


Figure 15 Switching Frequency vs. Input Voltage


Figure 17 1A LED Current vs. Input Voltage

## Typical Performance Characteristics (cont.) ( 670 mA LED Current) ( $@ \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)

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Figure 18 LED Current Deviation vs. Input Voltage


Figure 20 LED Current Deviation vs. Input Voltage


Figure 22 LED Current Deviation vs. Input Voltage


Figure 19 Switching Frequency vs. Input Voltage


Figure 21 Switching Frequency vs. Input Voltage


## Typical Performance Characteristics (cont.) (1A LED Current) (@T $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)



Figure 24 LED Current Deviation vs. Input Voltage


Figure 26 LED Current Deviation vs. Input Voltage


Figure 28 LED Current Deviation vs. Input Voltage


Figure 25 Switching Frequency vs. Input Voltage


Figure 27 Switching Frequency vs. Input Voltage


Figure 29 Switching Frequency vs. Input Voltage

## Application Information

## AL8805 Operation

In normal operation, when voltage is applied at $+\mathrm{V}_{\mathbb{I N}}$, the AL8805 internal switch is turned on. Current starts to flow through sense resistor $\mathrm{R}_{1}$, inductor L1, and the LEDs. The current ramps up linearly, and the ramp rate is determined by the input voltage + Vin and the inductor L1.

This rising current produces a voltage ramp across $\mathrm{R}_{1}$. The internal circuit of the AL8805 senses the voltage across $\mathrm{R}_{1}$ and applies a proportional voltage to the input of the internal comparator.

When this voltage reaches an internally set upper threshold, the internal switch is turned off. The inductor current continues to flow through $\mathrm{R}_{1}$, L1, the LEDs and the schottky diode D1, and back to the supply rail, but it decays, with the rate of decay determined by the forward voltage drop of the LEDs and the schottky diode.

This decaying current produces a falling voltage at $R_{1}$, which is sensed by the AL8805. A voltage proportional to the sense voltage across $R_{1}$ is applied at the input of the internal comparator. When this voltage falls to the internally set lower threshold, the internal switch is turned on again. This switch-on-and-off cycle continues to provide the average LED current set by the sense resistor $\mathrm{R}_{1}$.

## LED Current Control

The LED current is controlled by the resistor $\mathrm{R}_{1}$ in Figure 30.
Connected between $\mathrm{V}_{\mathrm{IN}}$ and SET the nominal average output current in the LED(s) is defined as:

$$
L_{\text {LED }}=\frac{V_{T H D}}{R_{S E T}}
$$

If the CTRL pin is driven by an external voltage (higher than 0.4 V and lower than 2.5 V ), the average LED current is:

$$
\mathrm{I}_{\mathrm{LED}}=\frac{\mathrm{V}_{\mathrm{CTRL}}}{\mathrm{~V}_{\text {REF }}} \frac{\mathrm{V}_{\mathrm{THD}}}{\mathrm{R}_{\mathrm{SET}}}
$$

For example for a desired LED current of 660 mA and a default voltage $\mathrm{V}_{\mathrm{CTRL}}=2.5 \mathrm{~V}$ the resulting resistor is:

$$
R_{\mathrm{SET}}=\frac{\mathrm{V}_{\text {THD }}}{\mathrm{l}_{\mathrm{LED}}} \frac{\mathrm{~V}_{\mathrm{CTRL}}}{\mathrm{~V}_{\text {REF }}}=\frac{0.1}{0.66} \frac{2.5}{2.5} \approx 150 \mathrm{~m} \Omega
$$



Figure 30 Typical Application Circuit

## DC Dimming

The CTRL pin can be driven by an external DC voltage ( $\mathrm{V}_{\text {CTRL }}$ ), to adjust the output current to a value below the nominal average value defined by RSET. The LED current decreases linearly with the CTRL voltage when $0.5 \mathrm{~V} \leq \mathrm{V}_{\text {cTRL }} \leq 2.5 \mathrm{~V}$, as in Figure 2 for 4 different current levels.

When the CTRL voltage falls below the threshold, 0.4 V , the output switch is turned off which allows PWM dimming.
Note that $100 \%$ brightness setting corresponds to $\mathrm{V}_{\mathrm{CTRL}}=\mathrm{V}_{\mathrm{REF}}$, nominally 2.5 V . For any voltage applied on the CTRL pin that is higher than $\mathrm{V}_{\mathrm{REF}}$, the device will not overdrive the LED current and will still set the current according to the equation $\mathrm{V}_{\mathrm{CTRL}}=\mathrm{V}_{\mathrm{REF}}$.

## Application Information (cont.)

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## PWM Dimming

LED current can be adjusted digitally, by applying a low frequency Pulse Width Modulated (PWM) logic signal to the CTRL pin to turn the device on and off. This will produce an average output current proportional to the duty cycle of the control signal. In particular, a PWM signal with a max resolution of 10bit can be applied to the CTRL pin to change the output current to a value below the nominal average value set by resistor $\mathrm{R}_{\text {SET }}$. To achieve this resolution the PWM frequency has to be lower than 500 Hz , however higher dimming frequencies can be used, at the expense of dimming dynamic range and accuracy.

Typically, for a PWM frequency of 500 Hz the accuracy is better than $1 \%$ for PWM ranging from $1 \%$ to $100 \%$.


Figure 32 Low Duty Cycle PWM Dimming at 500Hz

The CTRL pin is designed to be driven by both 3.3 V and 5 V logic levels directly from a logic output with either an open drain output or push-pull output stage.


## Application Information (cont.)

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## Soft Start

The AL8805 does not have in-built soft-start action - this provides very fast turn off of the output the stage improving PWM dimming accuracy; nonetheless, adding an external capacitor from the CTRL pin to ground will provide a soft-start delay. This is achieved by increasing the time taken for the CTRL voltage to rise to the turn-on threshold and by slowing down the rate of rise of the control voltage at the input of the comparator. Adding a capacitor increases the time taken for the output to reach $90 \%$ of its final value, this delay is $0.1 \mathrm{~ms} / \mathrm{nF}$, but will impact on the PWM dimming accuracy depending on the delay introduced.


Figure 33 Soft Start with $22 n F$ Capacitor on CTRL Pin ( $\mathrm{V}_{\mathrm{IN}}=36 \mathrm{~V}, \mathrm{I}_{\mathrm{LED}}=667 \mathrm{~mA}, 1$ LED)

## Reducing Output Ripple

Peak to peak ripple current in the LED(s) can be reduced, if required, by shunting a capacitor C 2 across the LED(s) as shown already in the circuit schematic.
A value of $1 \mu \mathrm{~F}$ will reduce the supply ripple current by a factor three (approx.). Proportionally lower ripple can be achieved with higher capacitor values. Note that the capacitor will not affect operating frequency or efficiency, but it will increase start-up delay, by reducing the rate of rise of LED voltage. By adding this capacitor the current waveform through the LED(s) changes from a triangular ramp to a more sinusoidal version without altering the mean current value.

## Capacitor Selection

The small size of ceramic capacitors makes them ideal for AL8805 applications. X5R and X7R types are recommended because they retain their capacitance over wider voltage and temperature ranges than other types such as Z5U.

A $2.2 \mu \mathrm{~F}$ input capacitor is sufficient for most intended applications of AL8805; however a $4.7 \mu \mathrm{~F}$ input capacitor is suggested for input voltages approaching 36 V .

## Application Information (cont.)

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## Diode Selection

For maximum efficiency and performance, the rectifier (D1) should be a fast low capacitance Schottky diode with low reverse leakage at the maximum operating voltage and temperature. The Schottky diode also provides better efficiency than silicon PN diodes, due to a combination of lower forward voltage and reduced recovery time.

It is important to select parts with a peak current rating above the peak coil current and a continuous current rating higher than the maximum output load current. In particular, it is recommended to have a diode voltage rating at least $15 \%$ higher than the operating voltage to ensure safe operation during the switching and a current rating at least $10 \%$ higher than the average diode current. The power rating is verified by calculating the power loss through the diode.

Schottky diodes, e.g. B240 or B140, with their low forward voltage drop and fast reverse recovery, are the ideal choice for AL8805 applications.

## Thermal and Layout Considerations

For continuous conduction mode of operation, the absolute maximum junction temperature must not be exceeded. The maximum power dissipation depends on several factors: the thermal resistance of the IC package $\theta_{\mathrm{JA}}, \mathrm{PCB}$ layout, airflow surrounding the $I C$, and difference between junction and ambient temperature.

The maximum power dissipation can be calculated using the following formula:

$$
P_{D(\operatorname{MAX})}=\left(T_{J(\text { MAX })}-T_{A}\right) / \theta_{J A}
$$

where
$\mathrm{T}_{J(\operatorname{MAX})}$ is the maximum operating junction temperature,
$T_{A}$ is the ambient temperature, and $\theta_{\mathrm{JA}}$ is the junction to ambient thermal resistance.

The recommended maximum operating junction temperature, $T_{J}$, is $+125^{\circ} \mathrm{C}$ and so maximum ambient temperature is determined by the AL8805's junction to ambient thermal resistance, $\theta_{\mathrm{JA}}$.
$\theta_{\mathrm{JA}}$, is layout dependent and the AL8805's $\theta_{\mathrm{JA}}$ on a $25 \times 25 \mathrm{~mm}$ single layer PCB with $10 z$ copper standing in still air is approximately $+250^{\circ} \mathrm{C} / \mathrm{W}$ $\left(+160^{\circ} \mathrm{C} / \mathrm{W}\right.$ on a four-layer PCB).

The maximum power dissipation at $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ can be calculated by the following formulas:
$\mathrm{P}_{\mathrm{D}(\text { MAX })}=\left(+125^{\circ} \mathrm{C}-+25^{\circ} \mathrm{C}\right) /\left(250^{\circ} \mathrm{C} / \mathrm{W}\right)=0.4 \mathrm{~W}$ for single-layer PCB
$P_{D(\operatorname{MAX})}=\left(+125^{\circ} \mathrm{C}-+25^{\circ} \mathrm{C}\right) /\left(160^{\circ} \mathrm{C} / \mathrm{W}\right)=0.625 \mathrm{~W}$ for standard four-layer PCB
Figure 34, shows the power derating of the AL8805 on two (one single-layer and four-layer) different $25 \times 25 \mathrm{~mm}$ PCB with $10 z$ copper standing in still air.


Figure 34 Derating Curve for Different PCB

## Application Information (cont.)

AL8805

## Thermal and Layout Considerations

Figure 35 gives details about the PCB layout suggestions:

1. the capacitor C 1 has to be placed as close as possible to $\mathrm{V}_{\mathrm{IN}}$
2. The sense resistor R 1 has to be placed as close as possible to $\mathrm{V}_{\mathbb{N}}$ and SET
3. The D1 anode, the SW pin and the inductor have to be placed as close as possible to avoid ringing.


Figure 35 Recommended PCB Layout

## Application Example

Typical application example for the AL8805 is the MR16 lamp. They typically operate from $12 \mathrm{~V}_{\mathrm{DC}}$ or $12 \mathrm{~V}_{\mathrm{AC}}$, using conventional electromagnetic transformers or electronic transformers.

As a replacement in some halogen lamp applications LEDs offer a more energy efficient solution - providing no radiated heat and no Ultra Violet light.
This application example is intended to fit into the base connector space of an MR16 style LED lamp. The design has been optimized for part count and thermal performance for a single 3W LED in the Lens section.


An inductor choice of $33 \mu \mathrm{H}$ with saturation current higher than 1.1 A , will limit the frequency variation between 230 kHz and 350 kHz over the whole input voltage variation ( 8 V to 18 V ), and therefore represent the best choice for an MR16 solution also taking into account the size constraint of the lamp.
The AL8805 guarantee high level of performance both with $12 \mathrm{~V}_{\mathrm{AC}}$ and $12 \mathrm{~V}_{\mathrm{DC}}$ power supply.
The efficiency is generally higher than $81 \%$ and current regulation is better than $0.1 \mathrm{~mA} / \mathrm{V}$ in for a DC input voltage in the range from 8 V to 18 V . In table 1 can be found the bill of material of the MR16 application example.

## Application Information (cont.)

In Figures 37 and 38 are displayed the top layer and the bottom layer of a typical PCB design for an MR16 solution.

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Figure 37 Top Layer


Figure 38 Bottom Layer

Table1 MR16 Application Example Bill of Material

| Quantity | PCB <br> Ident | Value | Description | Suggested Sources |
| :---: | :---: | :---: | :---: | :---: |
| 1 | U1 | AL8805 | LED Driver IC | Diodes Zetex |
| 1 | D1, | DFLS240L | freewheeling diode | Diodes Zetex |
| 4 | D2, D3, D4, D5 | SBR2A40 | Input bridge | Diodes Zetex |
| 1 | R1 | OR15 | Resistor, 0805, +l-1\% <+l-300ppm Generic KOA SR732ATTDR150F | Kemet |
| 1 | C1 | 150uF 20V | SMD tantalum Kemet D case, T491X157K020AT | Kemet |
| 0 | C2 | - | Not fitted |  |
| 1 | C3 | $100 \mathrm{nF}>=25 \mathrm{~V}$ | X7R 0805 Generic Kemet C0805C104K5RAC (50v) NIC NMC0805X7R104K50TRPF (50v) | Kemet <br> NIC Components |
| 1 | C4 | $1 u F>=25 V$ | X7R 1206 Generic Kemet C1206105K5RAC7800 (50v) NIC NMC1206X7R105K50F (50v) | Kemet <br> NIC Components |
| 1 | L1 | $33 \mu \mathrm{H}$ | LPS6235-333MLB | Coilcraft |

## Ordering Information




## Marking Information

## (Top View)



XX : Identification code
$\underline{Y}$ : Year 0~9
W : Week A~Z: 1~26 week; a~z: 27-52 week; z represents 52 and 53 week X : A~Z Internal code

| Part Number | Package | Identification Code |
| :---: | :---: | :---: |
| AL8805W5-7 | SOT25 | A6 |

## Package Outline Dimensions (All dimensions in mm.)



## Suggested Pad Layout

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| Dimensions | Value (in mm) |
| :---: | :---: |
| $\mathbf{Z}$ | 3.20 |
| $\mathbf{G}$ | 1.60 |
| $\mathbf{X}$ | 0.55 |
| $\mathbf{Y}$ | 0.80 |
| $\mathbf{C 1}$ | 2.40 |
| $\mathbf{C 2}$ | 0.95 |



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