

### 1A DROPOUT POSITIVE ADJUSTABLE OR FIXED-MODE REGULATOR

### Description

AP1117 is a low dropout positive adjustable or fixed-mode regulator with 1A output current capability. The product is voltage specifically designed to provide well-regulated supply for low IC applications such as high-speed bus termination and low current 3.3V logic supply. AP1117 is also well suited for other applications such as VGA cards. AP1117 is guaranteed to have lower than 1.4V dropout at full load current making it ideal to provide well-regulated outputs of 1.25 to 5.0 with 6.4V to 18V input supply. AP1117 is available both in commercial temperature grade (AP1117) and in industrial temperature range (AP1117I) (TO252 and SOT223).

### **Features**

- 1.4V Maximum Dropout at Full Load Current
- Fast Transient Response
- Output Current Limiting
- Built-in Thermal Shutdown
- Good Noise Rejection
- 3-Terminal Adjustable or Fixed 1.5V, 1.8V, 2.5V, 3.3V, 5.0V
- Operating Junction Temperature range
  - 0°C ≤ T<sub>J</sub> ≤ +125°C (All packages)
  - $-20^{\circ}C \le T_J \le +125^{\circ}C$  (see ordering information)
  - -40°C ≤ T<sub>J</sub> ≤ +125°C (see ordering information)
- Lead-Free Packages: SOT223, TO252, SOT89-3, TO263 and TO220-3
  - Totally Lead-Free; RoHS Compliant (Notes 1 & 2)
- SOT223, TO252, SOT89-3, TO263 and TO220-3: Available in "Green" Molding Compound (No Br, Sb)
  - Halogen and Antimony Free. "Green" Device (Note 3)

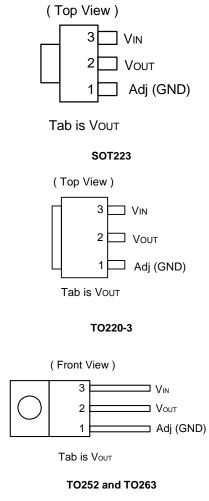
### Applications

- PC Peripheral
- Communication

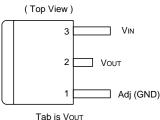
Notes:

- Consumer Equipment
  - 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
    - See http://www.diodes.com quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
    - 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

# **Pin Assignments**

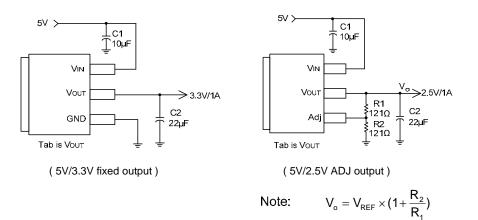


SOT89-3





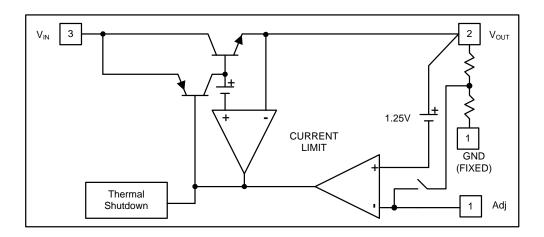
# **Typical Applications Circuit**



# **Pin Descriptions**

Pin Number	I/O	Pin Name	Function
1	I	Adj (GND)	A resistor divider from this pin to the $V_{OUT}$ pin and ground sets the output voltage (Ground only for Fixed-Mode).
2	0	V <sub>OUT</sub>	The output of the regulator. A minimum of $4.7\mu$ F capacitor ( $0.15\Omega \le ESR \le 0.5\Omega$ ) must be connected from this pin to ground to insure stability.
3	I	V <sub>IN</sub>	The input pin of regulator. Typically a large storage capacitor is connected from this pin to ground to insure that the input voltage does not sag below the minimum dropout voltage during the load transient response. This pin must always be 1.3V higher than $V_{OUT}$ in order for the device to regulate properly. A minimum of $4.7\mu$ F capacitor ( $0.15\Omega \le ESR \le 0.5\Omega$ ) must be connected from this pin to ground to insure stability.

# **Functional Block Diagram**





# Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
V <sub>IN</sub>	DC Supply Voltage	-0.3 to +18	V
T <sub>MJ</sub>	Maximum Junction Temperature	+150	٥°
P <sub>D</sub>	Power Dissipation SOT89-3 SOT223 TO220-3 TO252 TO263	Internally limited by maximum junction temperature of +150°C (Note 4)	mW
T <sub>ST</sub>	Storage Temperature	-65 to +150	۵°

Notes: 4. AP1117 contains an internal thermal limiting circuit that is designed to protect the regulator in the event that the maximum junction temperature exceeded. When activated, typically at +150°C, the regulator output switches off and then back on as the die cools.

### Recommended Operating Conditions (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter		Min	Max	Unit
т	Operating Junction Temperature Dance	AP1117	0	+125	°C
I OP	Operating Junction Temperature Range	AP1117I	-40	+125	

11. 22uF is suggested for Cin/Cout value when application is required to work under 0°C. Note:

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Parameter		Conditions	Min	Тур	Max	Unit
Reference Voltage	AP1117-ADJ	$T_A = +25^{\circ}C$ , $(V_{IN}-OUT) = 1.5V$ $I_O = 10mA$		1.250	1.275	V
	AP1117-1.5	$I_{OUT}$ = 10mA, $T_A$ = +25°C, 3V ≤ V <sub>IN</sub> ≤ 12V	1.470	1.500	1.530	V
	AP1117-1.8	$I_{OUT}$ = 10mA, $T_A$ = +25°C, 3.3V ≤ $V_{IN}$ ≤ 12V	1.764	1.800	1.836	V
Output Voltage	AP1117-2.5	$I_{OUT}$ = 10mA, $T_A$ = +25°C, 4V ≤ V <sub>IN</sub> ≤ 12V	2.450	2.500	2.550	V
	AP1117-3.3	$I_{OUT} = 10mA$ , $T_A = +25^{\circ}C$ , 4.8V $\leq V_{IN} \leq 12V$	3.235	3.300	3.365	V
	AP1117-5.0	$I_{OUT} = 10mA$ , $T_A = +25^{\circ}C$ , $6.5V \le V_{IN} \le 12V$	4.900	5.000	5.100	V
Line Regulation	AP1117-XXX	$I_{O} = 10$ mA, $V_{OUT} + 1.5V < V_{IN} < 12V$ , $T_{A} = 25$ °C	_	_	0.2	%
	AP1117-ADJ	$V_{IN} = 3.3V, V_{ADJ} = 0, 0mA < I_O < 1A,$ $T_A = +25^{\circ}C$ (Notes 5, 6)	—	—	1	%
Lood Dogulation	AP1117-1.5	$V_{IN} = 3V$ , 0mA < I <sub>O</sub> < 1A, T <sub>A</sub> = +25°C (Notes 5, 6)	_	12	15	mV
Load Regulation	AP1117-1.8	$V_{IN} = 3.3V$ , 0mA < I <sub>O</sub> < 1A, T <sub>A</sub> = +25°C (Notes 5, 6)	_	15	18	mV
	AP1117-2.5	$V_{IN} = 4V$ , 0mA < I <sub>O</sub> < 1A, T <sub>A</sub> = +25°C (Notes 5, 6)	_	20	25	mV

 See thermal regulation specifications for changes in output voltage due to heating effects. Line and load regulation are measured at a constant junction temperature by low duty cycle pulse testing. Load regulation is measured at the output lead = 1/18" from the package.
Line and load regulation are guaranteed up to the maximum power dissipation of 15W. Power dissipation is determined by the difference between input and output differential and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range. Notes:



# Electrical Characteristics (cont.) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Parameter		Conditions		Min	Тур	Max	Unit
Load Regulation	AP1117-3.3	$V_{IN} = 5V, 0 \le I_{OUT} \le 1$ $T_A = +25^{\circ}C$ (Notes 5,	,	—	26	33	mV
(cont.)	AP1117-5.0	$V_{IN} = 8V, 0 \le I_{OUT} \le 1$ $T_A = +25^{\circ}C$ (Notes 5	,	—	40	50	mV
Dropout Voltage (V <sub>IN</sub> -V <sub>OUT</sub> )	AP1117-ADJ/1.5/1.8 2.5/3.3/5.0	$I_{OUT} = 800$ mA, $\Delta V_{OU}$ $V_{OUT} 0^{\circ}$ C $\leq T_{J} \leq +12$		_	1.2	1.3	V
Dropout Voltage	AP1117-ADJ/1.5/1.8 2.5/3.3/5.0	louт = 1А,	$0^{\circ}C \le T_{J} \le +125^{\circ}C$		1.3	1.4	V
(V <sub>IN</sub> -V <sub>OUT</sub> )	AP1117I-ADJ/1.5/1.8 2.5/3.3/5.0	$\Delta V_{OUT}$ = 1% V <sub>OUT</sub>	$-40^{\circ}\mathrm{C} \leq \mathrm{T_{J}} \leq +125^{\circ}\mathrm{C}$		1.5	1.4	v
Current Limit	AP1117-ADJ/1.5/1.8 2.5/3.3/5.0	$(V_{IN}-V_{OUT}) = 5V$		1. 1	_	—	А
Minimum Load Current (Note 7)	AP1117-XXX	0°C ≤ T <sub>J</sub> ≤ +125°C		—	5	10	mA
Thermal Regulation	$T_A = +25^{\circ}C$ , 30ms pulse	е		—	0.008	0.040	%/W
Dinale Dejection	F = 180Hz, C <sub>OUT</sub> = 25µF Tantalum, I <sub>OUT</sub> = 1A			—	_	—	
Ripple Rejection	AP1117-XXX	/ <sub>IN</sub> = V <sub>OUT</sub> +3V		_	60	70	dB
Temperature Stability	I <sub>O</sub> = 10mA				0.5	_	%
$\theta_{JA}$ Thermal Resistance Junction-to-Ambient $\theta_{JA}$	SOT223: Control Circuit TO252: Control Circuitr TO220-3: Control Circu	SOT89-3: Control Circuitry/Power Transistor (Note 8) SOT223: Control Circuitry/Power Transistor (Note 9) TO252: Control Circuitry/Power Transistor (Note 8) TO220-3: Control Circuitry/Power Transistor (Note 8) TO263: Control Circuitry/Power Transistor (Note 8)			164 107 73 78 60	 	°C/W
θ <sub>JC</sub> Thermal Resistance Junction-to-Case	SOT223: Control Circuit TO252: Control Circuitr	cuitry/Power Transistor (Note 8) cuitry/Power Transistor (Note 9) itry/Power Transistor (Note 8) cuitry/Power Transistor (Note 8)			42 15 12 3.5 3.5		°C/W

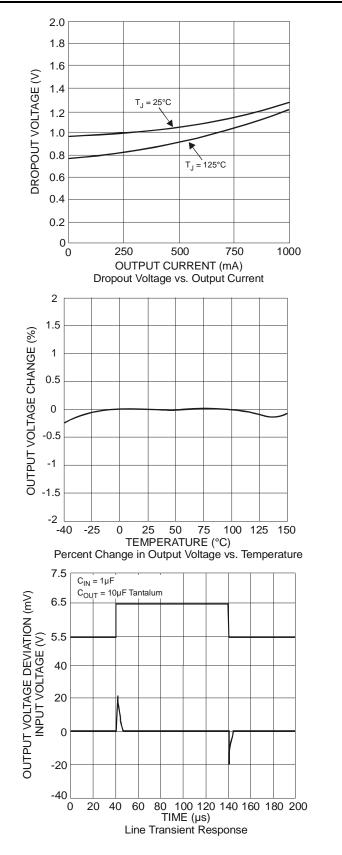
 Quiescent current is defined as the minimum output current required in maintaining regulation. At 12V input/output differential the device is guaranteed to regulate if the output current is greater than 10mA.
Test conditions for SOT89-3, TO220-3, TO252 and TO263: Devices mounted on FR-4 substrate, single sided PC board, 2oz copper,with minimum Notes:

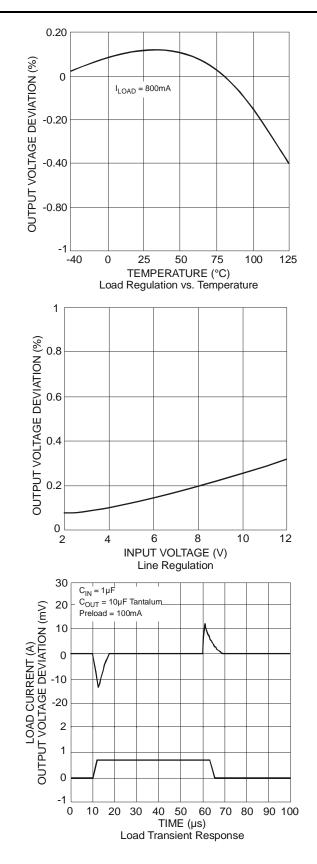
recommended pad layout, no air flow. The case point of  $\theta_{JC}$  is located on the thermal tab.

9. Test conditions for SOT223: Devices mounted on FR-4 substrate, single sided PC board, 2oz copper, with 5mm x 5mm thermal pad layout, no air flow. The case point of  $\theta_{JC}$  is located on the thermal tab.



# **Typical Performance Characteristics**







### **Application Information**

#### Load Regulation

For improved load regulation the AP1117-ADJ should have the upper feedback resistor, R1, connected as close as possible to V<sub>OUT</sub> and the lower resistor, R2, connected as close as possible to the load GND return. This helps reduce any parasitic resistance in series with the load.

#### **Stability and Decoupling Capacitors**

#### **Input Capacitors**

To ensure stable operation, the input supply must be low impedance up to a frequency of a few MHz. This requires a closely placed input decoupling capacitor of  $4.7\mu$ F minimum. This can be either ceramic or solid tantalum. The ESR of this capacitor must be less than  $0.5\Omega$ .

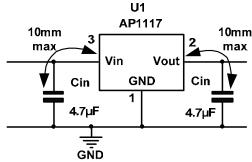
#### **Output Capacitor**

The AP1117 also requires a closely placed output capacitor as part of the device frequency compensation. As part of its improved performance over industry standard 1117, the AP1117 is suitable for use with MLCC (Multi-Layer Ceramic Chip) capacitors. A minimum output capacitor of 4.7 $\mu$ F ceramic X7R or 4.7 $\mu$ F solid tantalum is required. Aluminum electrolytic can be used but a minimum of 47 $\mu$ F is required. When using Aluminum electrolytic it is still recommended to also use a 1 $\mu$ F MLCC in parallel. The ESR of the output capacitors must be less than 0.5 $\Omega$ .

The AP1117 is stable when using the correct value of MLCC capacitors. When using MLCC capacitors X7R dielectric is recommended. Do not use Y5V dielectrics.

#### Capacitor Track Length

Both input and output capacitors must be placed close to the AP1117. PCB traces not longer than 10mm are recommended between the AP1117 and the capacitors.



#### Thermal Considerations

Thermal protection circuitry will shut down the regulator should the junction temperature exceed typically +150°C at the sense point.

The AP1117 is pin compatible with similar '1117 regulators and offers extended temperature range and improved regulation specifications.

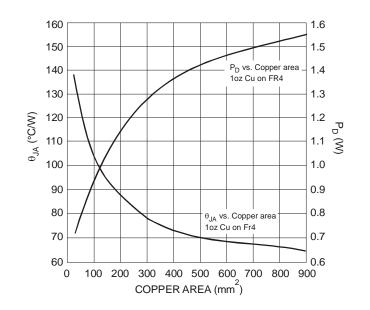
AP1117 series regulators have internal thermal limiting circuitry designed to protect the device during overload conditions. For continuous normal load conditions however, the maximum junction temperature rating of +125°C must not be exceeded.

It is important to give careful consideration to all sources of thermal resistance from junction to ambient. For the SOT223 and TO252 packages, which are designed to be surface mounted, additional heat sources mounted near the device must also be considered. Heat sinking is accomplished using the heat spreading capability of the PCB and its copper traces. The  $\theta_{JC}$  (junction to tab) of the TO252 and SOT223 are +12°C/W and +15°C/W respectively.

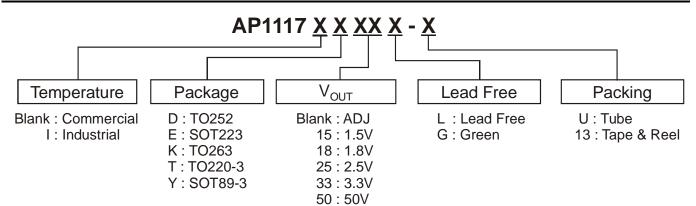
Thermal resistances from tab to ambient can be as low as  $30^{\circ}$ C/W. The total thermal resistance from junction to ambient ( $\theta_{JA}$ ) can be as low as +42 ~ +46°C/W. This requires a reasonable sized PCB with at least one layer of copper to spread the heat across the board and couple it into the surrounding air. Datasheet specifications using 2 oz copper and a 5mm x 5mm pad with T<sub>A</sub> = +27°C, no air flow yielded  $\theta_{JA}$  (junction to tab) of +73°C/W and +107°C/W for TO252 and SOT223 respectively.

The thermal resistance for each application will be affected by thermal interactions with other components on the board. Some experimentation will be necessary to determine the actual value. See graphs of power dissipation and thermal pictures of different size PCB copper area for guidance.





# **Ordering Information**



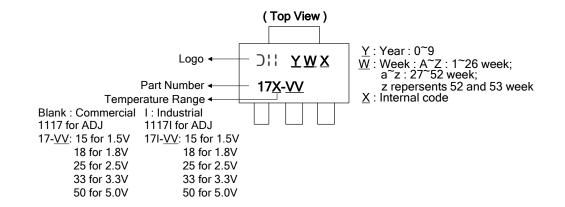
		Junction			Tu	be	13" Tape	and Reel
	Device	Temperature Range	Package Code	Packaging (Note 10)	Quantity	Part Number Suffix	Quantity	Part Number Suffix
Read-Free	AP1117DXXL-13	0 to +125°C	D	TO252	NA	NA	2500/Tape & Reel	-13
Pb,	AP1117DXXG-13	-20 to +125°C	D	TO252	NA	NA	2500/Tape & Reel	-13
Pb,	AP1117IDXXG-13	-40 to +125°C	D	TO252	NA	NA	2500/Tape & Reel	-13
	AP1117EXXL-13	0 to +125°C	E	SOT223	NA	NA	2500/Tape & Reel	-13
Pb,	AP1117EXXG-13	-20 to +125°C	E	SOT223	NA	NA	2500/Tape & Reel	-13
Pb,	AP1117IEXXG-13	-40 to +125°C	E	SOT223	NA	NA	2500/Tape & Reel	-13
	AP1117KXXL-13	0 to +125°C	К	TO263	NA	NA	800/Tape & Reel	-13
Pb,	AP1117KXXG-13	0 to +125°C	К	TO263	NA	NA	800/Tape & Reel	-13
	AP1117TXXL-U	0 to +125°C	Т	TO220-3	50	-U	NA	NA
Pb,	AP1117TXXG-U	0 to +125°C	Т	TO220-3	50	-U	NA	NA
(Pb) and Free	AP1117YXXL-13	0 to +125°C	Y	SOT89-3	NA	NA	2500/Tape & Reel	-13
Pb,	AP1117YXXG-13	0 to +125°C	Y	SOT89-3	NA	NA	2500/Tape & Reel	-13

Notes: 10. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

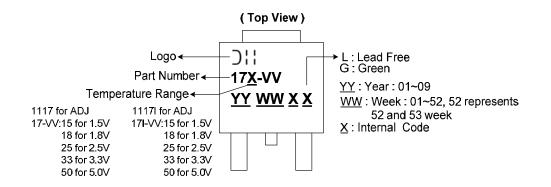
Note: 11. 22uF is suggested for  $C_{in}/C_{out}$  value when application is required to work under 0°C.



### (1) SOT223

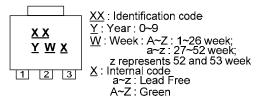


(2) TO252



(3) SOT89-3

(Top View)



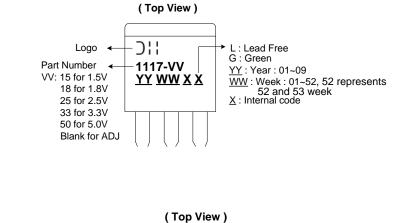
Identification Code	Output Version
DA	AP1117-ADJ
DB	AP1117-1.5V
DC	AP1117-1.8V
DD	AP1117-2.5V
DE	AP1117-3.3V
DF	AP1117-5.0V

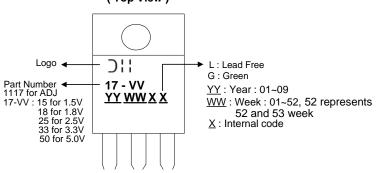


### Marking Information (cont.)

#### (4) TO263

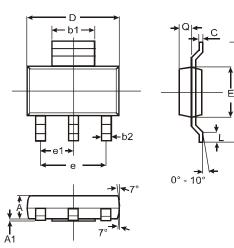
(5) TO220-3





### Package Outline Dimensions (All dimensions in mm.)

### (1) Package Type: SOT223



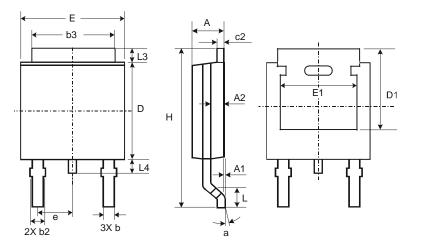
	SOT	223	
Dim	Min	Max	Тур
Α	1.55	1.65	1.60
A1	0.010	0.15	0.05
b1	2.90	3.10	3.00
b2	0.60	0.80	0.70
С	0.20	0.30	0.25
D	6.45	6.55	6.50
E	3.45	3.55	3.50
E1	6.90	7.10	7.00
е			4.60
e1	-	-	2.30
L	0.85	1.05	0.95
Q	0.84	0.94	0.89
All [	Dimens	ions in	mm

E

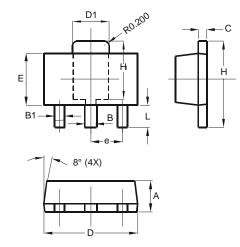


### Package Outline Dimensions (cont.) (All dimensions in mm.)

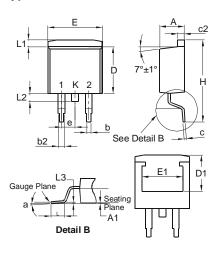
### (2) Package Type: TO252



### (3) Package Type: SOT89



#### (4) Package Type: TO263



TO252					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
c2	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	_		
All	Dimen	sions i	n mm		

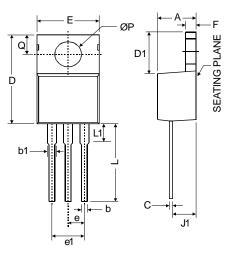
	SOT89	
Dim	Min	Max
Α	1.40	1.60
В	0.44	0.62
B1	0.35	0.54
С	0.35	0.44
D	4.40	4.60
D1	1.62	1.83
E	2.29	2.60
е	1.50	Тур
Н	3.94	4.25
H1	2.63	2.93
L	0.89	1.20
All C	Dimension	s in mm

	TO263	
Dim	Min	Max
Α	4.07	4.82
A1	0.00	0.25
b	0.51	0.99
b2	1.15	1.77
C	0.356	0.73
c2	1.143	1.65
D	8.39	9.65
D1	6.55	
E	9.66	10.66
E1	6.23	
е	2.54	Тур
Н	14.61	15.87
L	1.78	2.79
L1		1.67
L2		1.77
а	0°	8°
All Dim	nensions	in mm



### Package Outline Dimensions (cont.) (All dimensions in mm.)

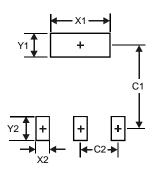
### (5) Package Type: TO220-3



TO220-3				
Dim	Min	Max		
Α	3.55	4.85		
b	0.51	1.14		
b1	1.14	1.78		
С	0.31	1.14		
D	14.20	16.50		
D1	5.84	6.86		
ш	9.70	10.70		
e	2.79	2.99		
e1	4.83	5.33		
F	0.51	1.40		
J1	2.03	2.92		
L	12.72	14.72		
L1	3.66	6.35		
Р	3.53	4.09		
Q	2.54	3.43		
All Dir	nension	s in mm		

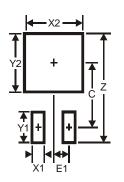
# Suggested Pad Layout

(1) Package Type: SOT223



Dimensions	Value (in mm)
X1	3.3
X2	1.2
Y1	1.6
Y2	1.6
C1	6.4
C2	2.3

(2) Package Type: TO252

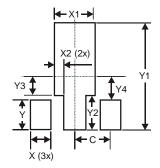


Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
С	6.9
E1	2.3



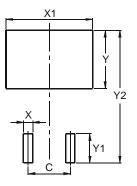
# Suggested Pad Layout (cont.)

### (3) Package Type: SOT89



Dimensions	Value (in mm)
Х	0.900
X1	1.733
X2	0.416
Y	1.300
Y1	4.600
Y2	1.475
Y3	0.950
Y4	1.125
C	1.500

### (4) Package Type: TO263



Dimensions	Value (in mm)
С	5.08
х	1.10
X1	10.41
Y	3.50
Y1	7.01
Y2	15.99



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