



BOOST CONTROLLER

AP3039A

General Description

The AP3039A is a current-mode high voltage low-side N-channel MOSFET controller which is ideal for boost regulators. It contains all the features needed to implement single ended primary topology DC/DC converters.

The input voltage range of AP3039A is from 5V to 27V. Its operation frequency is adjustable from 150kHz to 1MHz.

The AP3039A has UVLO (Under Voltage Lock Out) circuit. It uses two external resistors to set the UVLO voltage. The AP3039A also has an over output voltage protection to limit the output voltage. The OVP voltage can be set through external resistors. If the output voltage is higher than the OVP high threshold point, it will disable the driver, when the output voltage drops to the OVP low threshold point, it will enable the driver. It also features a soft start to reduce the inrush current when power on, the soft start time can be set through an external capacitor.

The AP3039A is available in SOIC-14 package.

Features

- Input Voltage Range 5V to 27V
- 1A Peak/10V MOSFET Gate Driver
- 20ns Quick MOSFET Gate Driver
- Duty Cycle Limit of 90%
- Programmable UVLO
- Programmable Over Voltage Protection
- Cycle-by-cycle Current Limit
- Adjustable Soft-start
- Adjustable Operation Frequency from 150kHz to 1MHz

Applications

- LED Lighting
- Notebook
- LCD Display Modules



Figure 1. Package Type of AP3039A

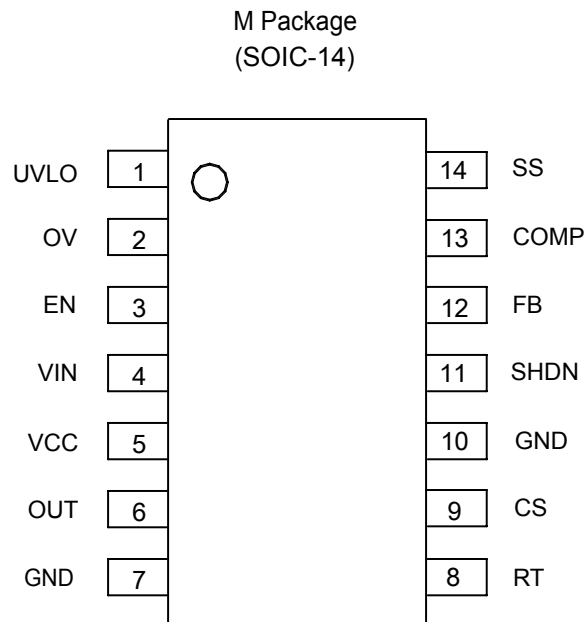
Pin Configuration

Figure 2. Pin Configuration of AP3039A (Top View)

**BOOST CONTROLLER****AP3039A****Pin Description**

Pin Number	Pin Name	Function
1	UVLO	Connect two resistors from this pin to GND and VIN respectively to set the start-up and shutdown level
2	OV	Over output voltage protection pin
3	EN	Enable pin
4	VIN	Input supply pin, must be locally bypassed
5	VCC	This pin must be bypassed to GND pin with a ceramic capacitor
6	OUT	Connect this pin to the gate of external MOSFET. And the OUT pin has 1A peak current capability
7, 10	GND	Ground
8	RT	Connect an external resistor from this pin to GND to set the operating frequency
9	CS	Sense switch current pin, used for current-mode control and current limit setting , The current limit voltage is 500mV
11	SHDN	This pin can be connected to current matched chip and receives error signal used to shut down the system
12	FB	Voltage Feedback pin. The reference voltage is 500mV
13	COMP	Compensation pin. This pin is the output of the internal error amplifier
14	SS	Place an external capacitor C_{SS} charged by an internal 10mA current source from this pin to GND to set the soft-start time



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Functional Block Diagram

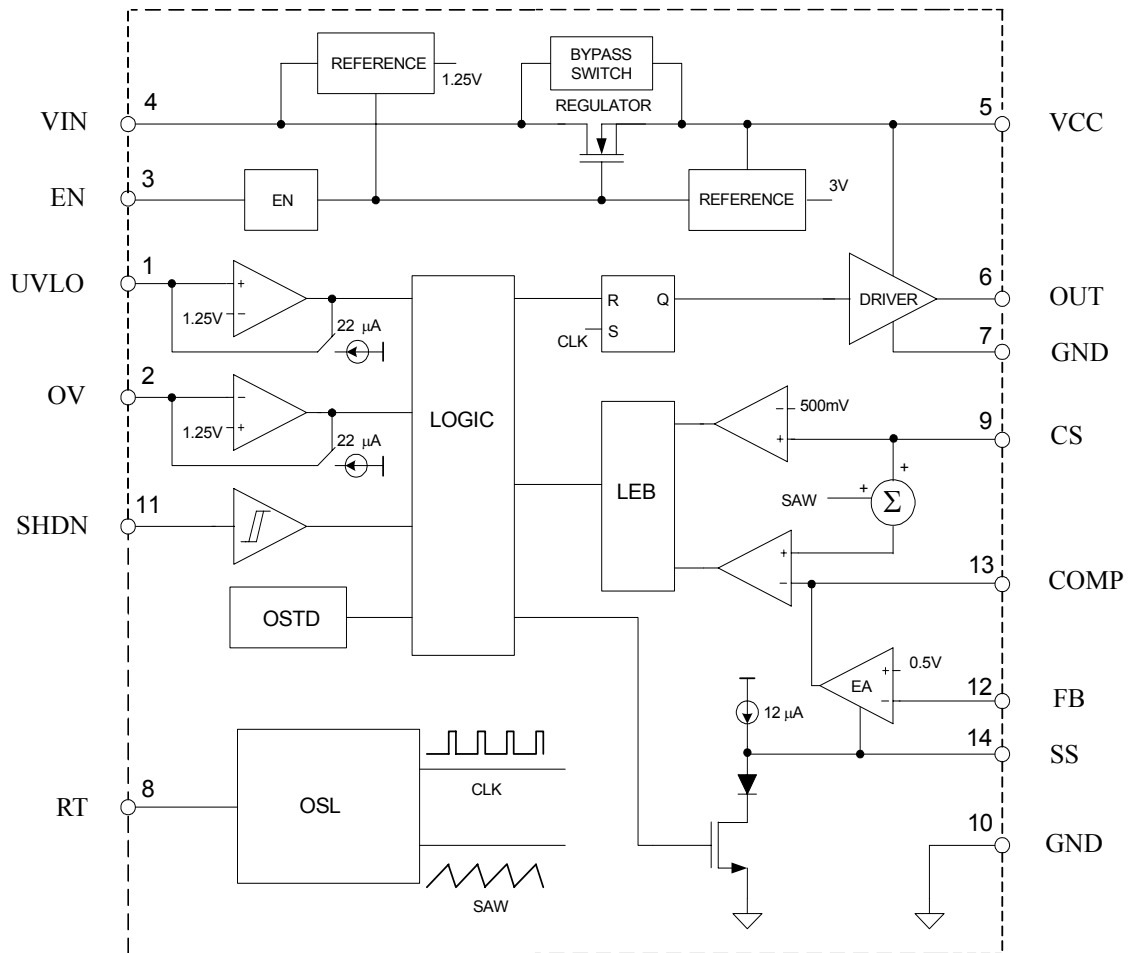


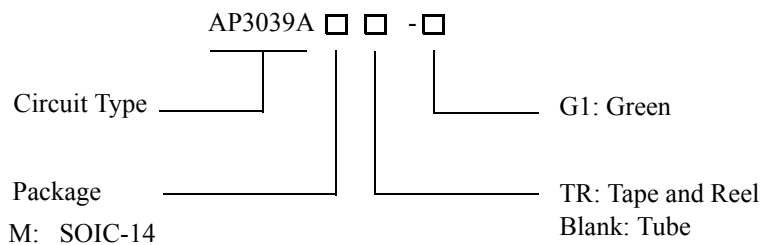
Figure 3. Functional Block Diagram of AP3039A



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Ordering Information



Package	Temperature Range	Part Number	Marking ID	Packing Type
SOIC-14	-40 to 85°C	AP3039AM-G1	AP3039AM-G1	Tube
		AP3039AMTR-G1	AP3039AM-G1	Tape & Reel

BCD Semiconductor's products, as designated with "G1" suffix in the part number, are RoHS compliant and Green.

**BOOST CONTROLLER****AP3039A****Absolute Maximum Ratings (Note 1)**

Parameter	Symbol	Value	Unit
Input Voltage	V_{IN}	30	V
VCC Pin Voltage	V_{CC}	20	V
OUT Pin Voltage	V_{OUT}	20	V
Feedback Pin Voltage	V_{FB}	13	V
UVLO Pin Voltage	V_{UVLO}	7	V
CS Pin Voltage	V_{CS}	7	V
SHDN Pin Voltage	V_{SHDN}	7	V
Enable Pin Voltage	V_{EN}	V_{IN}	
OV Pin Voltage	V_{OV}	7	V
Thermal Resistance (Junction to Ambient, no heat sink)	θ_{JA}	102	$^{\circ}\text{C}/\text{W}$
Operating Junction Temperature	T_J	150	$^{\circ}\text{C}$
Storage Temperature Range	T_{STG}	-65 to 150	$^{\circ}\text{C}$
Lead Temperature (Soldering, 10sec)	T_{LEAD}	260	$^{\circ}\text{C}$
ESD (Machine Model)		200	V
ESD (Human Body Model)		2000	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
Input Voltage	V_{IN}	5	27	V
Operating Frequency	f	150	1000	kHz
Operating Temperature	T_A	-40	85	$^{\circ}\text{C}$



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Electrical Characteristics

($V_{IN}=12V$, $V_{EN}=V_{IN}$, $T_A=25^{\circ}C$, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input Voltage	V_{IN}	$V_{CC}=V_{IN}$	5		6	V
		V_{CC} bypassed to GND through a 0.47 μ F capacitor	6		27	
Feedback Voltage	V_{FB}		490	500	510	mV
FB Pin Bias Current	I_{FB}			3	5	μ A
Quiescent Current	I_Q	No switching		3	5	mA
Shutdown Quiescent Current	I_{SHDN}	$V_{EN}=0V$		1	2	μ A
VCC Voltage	V_{CC}	$13.5V \leq V_{IN} \leq 27V$	9.5	10.0	10.5	V
		$6V \leq V_{IN} < 12.5V$	$V_{IN}-1$			
VCC Current Limit	I_{CC-LIM}			50		mA
Drop Voltage Across Bypass Switch	$V_{IN}-V_{CC}$	$I_{CC}=0mA$, $f_{OSC} \leq 200kHz$, $6V \leq V_{IN} < 12.5V$		200		mV
Bypass Switch Turn-off Threshold	V_{BYP-HI}	V_{IN} increasing		13		V
Bypass Switch Threshold Hysteresis	$V_{BYP-HYS}$	V_{IN} decreasing		260		mV
VCC Pin UVLO Rising Threshold	V_{CC-HI}			4.7		V
VCC Pin UVLO Falling Hysteresis	V_{CC-HYS}			300		mV
Oscillator Frequency	f_{OSC}	Adjustable, $R_T=51k\Omega$ to 470k Ω	150		1000	kHz
UVLO Threshold	V_{UVLO}		1.20	1.25	1.30	V
UVLO Hysteresis Current Source	I_{HYS}			22		μ A
Current Limit Threshold Voltage	V_{CS}		475	500	525	mV
RT Voltage	V_{RT}		1.20	1.25	1.30	V
Error Amplifier Transconductance	G_S			470		μ A/V
EN Pin Threshold Voltage	V_{EH}		2.0			V
	V_{EL}				0.5	
SHDN Pin Threshold Voltage	V_{IH}		2.0			V
	V_{IL}				0.5	

**BOOST CONTROLLER****AP3039A****Electrical Characteristics (Continued)**(V_{IN}=12V, V_{EN}=V_{IN}, T_A=25°C, unless otherwise specified.)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
OV Threshold	V _{OV}		1.20	1.25	1.30	V
OV Hysteresis Current Source	I _{OV-HYS}			22		μA
Maximum Duty Cycle	D _{MAX}	f _{OSC} =500kHz		90		%
Soft Start Current Source	I _{SS}			12		μA
Out Pin Rise Time	t _{RISE}	Out Pin Load =1nF		20		ns
Out Pin Fall Time	t _{FALL}	Out Pin Load =1nF		20		ns
OUT Dropout Voltage (V _{CC} -V _{OUT})	V _{OUT-H}	I _{OUT} =50mA		0.25	0.75	V
OUT Low Voltage Level (V _{OUT})	V _{OUT-L}	I _{OUT} =100mA		0.25	0.75	V
Thermal Shutdown Temperature	T _{OTSD}			160		°C
Thermal Shutdown Hysteresis	T _{HYS}			20		°C



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Typical Performance Characteristics

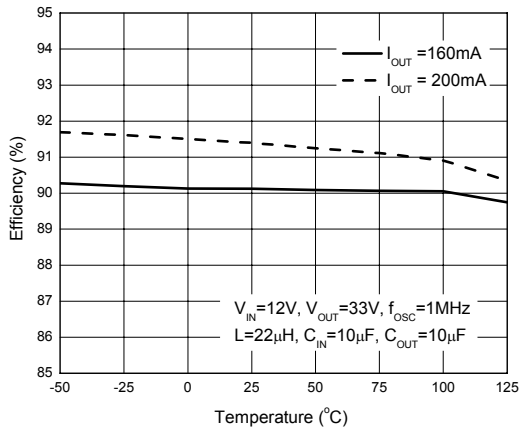


Figure 4. Efficiency vs. Case Temperature

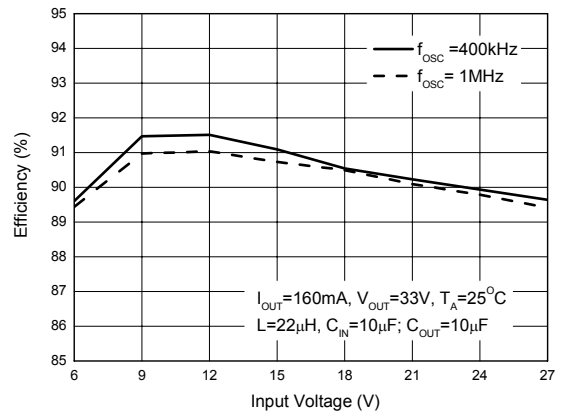


Figure 5. Efficiency vs. Input Voltage

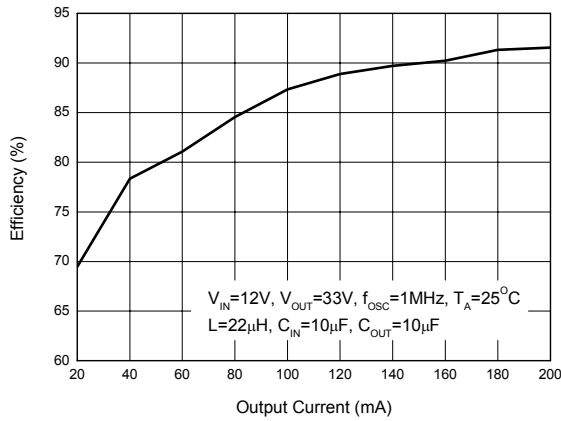


Figure 6. Efficiency vs. Output Current

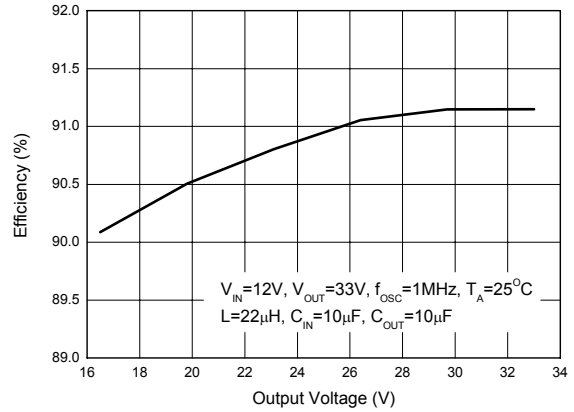


Figure 7. Efficiency vs. Output Voltage



Typical Performance Characteristics (Continued)

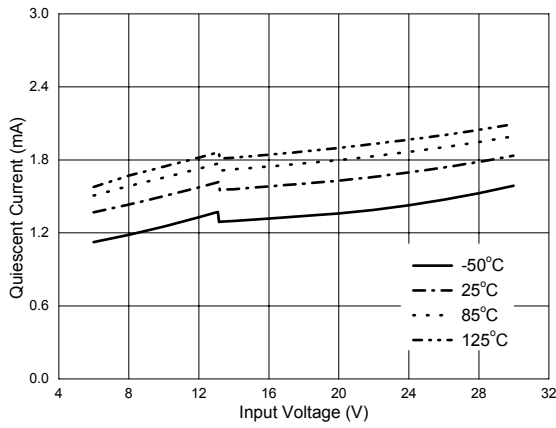


Figure 8. Quiescent Current vs. Input Voltage

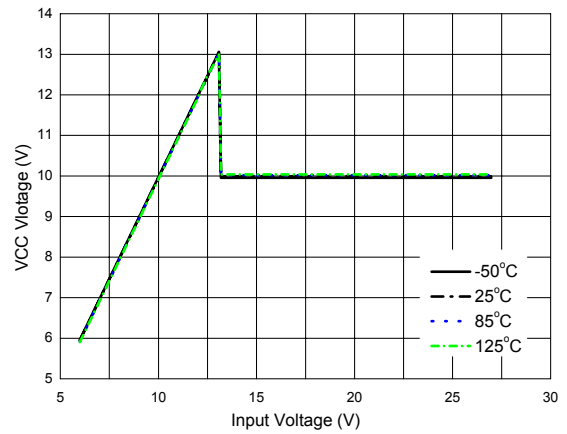


Figure 9. VCC Voltage vs. Input Voltage

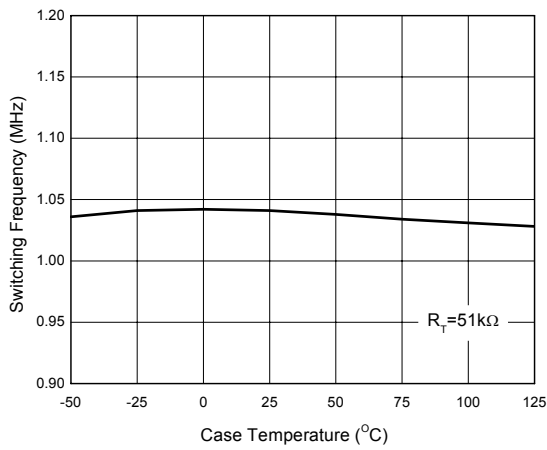


Figure 10. Switching Frequency vs. Case Temperature

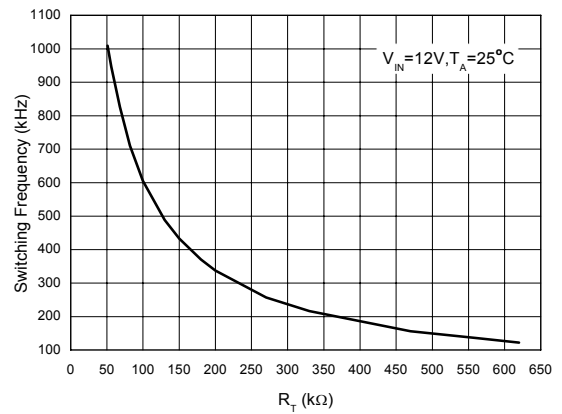


Figure 11. Switching Frequency vs. R_T



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Typical Performance Characteristics (Continued)

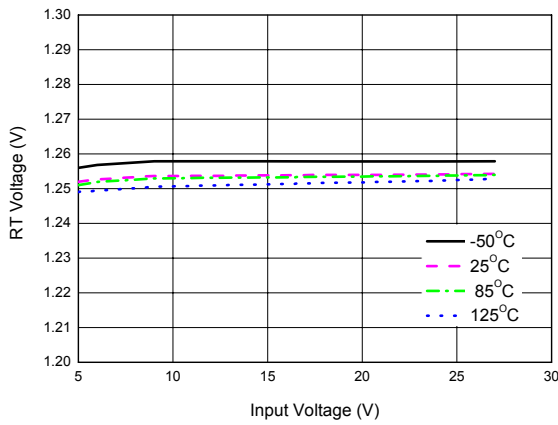


Figure 12. RT Voltage vs. Input Voltage

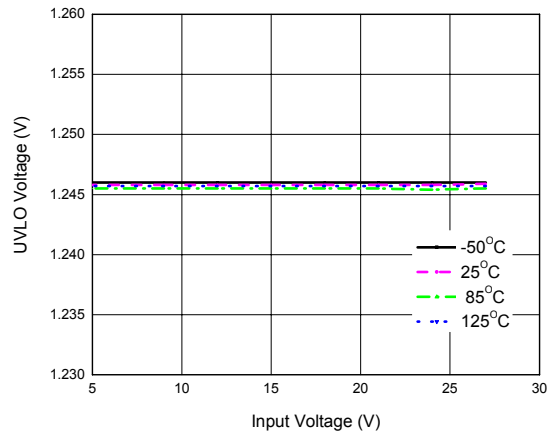


Figure 13. UVLO Voltage vs. Input Voltage

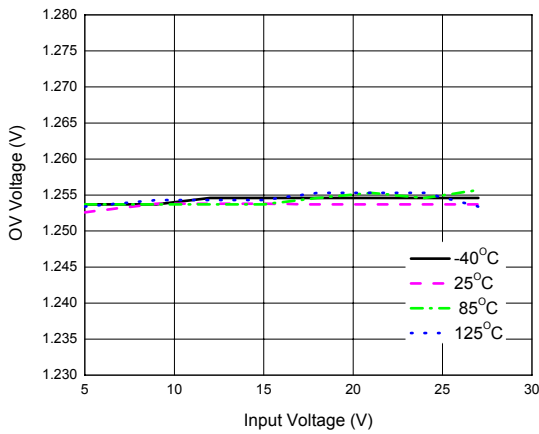


Figure 14. OV Voltage vs. Input Voltage

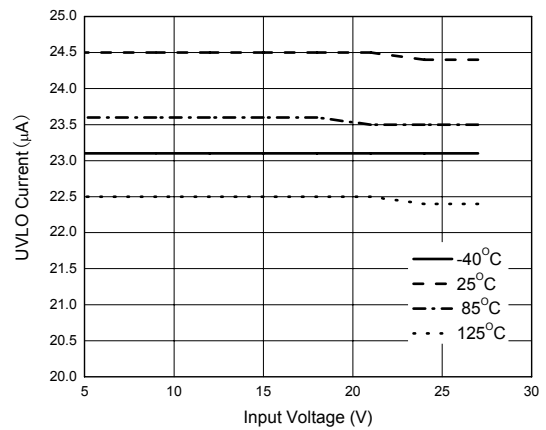


Figure 15. UVLO Current vs. Input Voltage



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Typical Performance Characteristics (Continued)

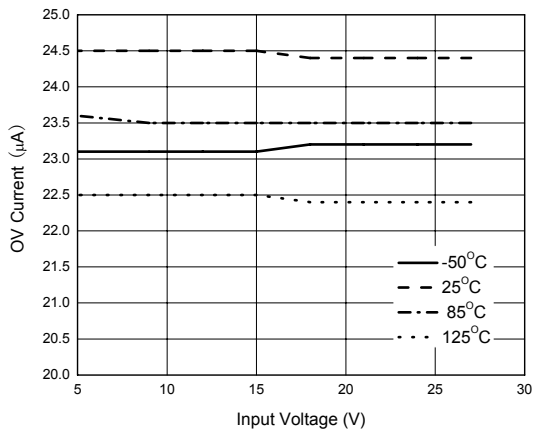


Figure 16. OV Current vs. Input Voltage

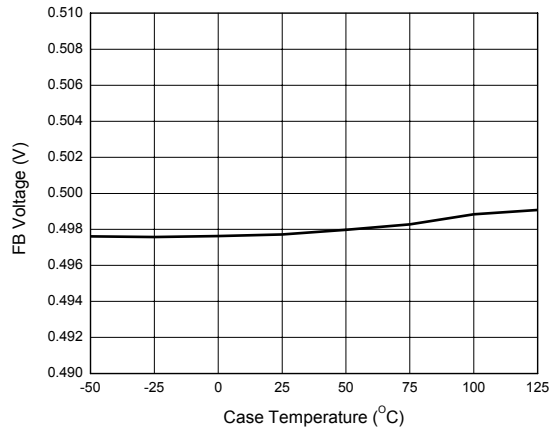


Figure 17. Feedback Voltage vs. Case Temperature

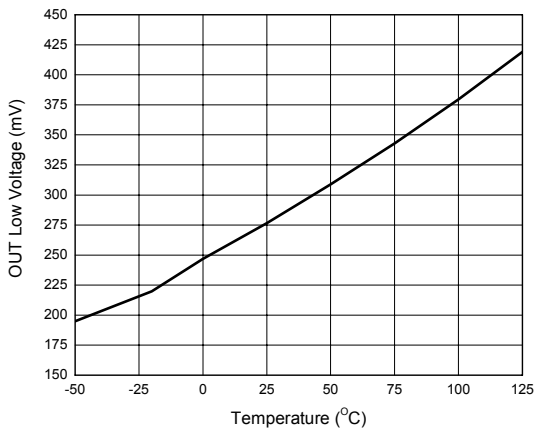


Figure 18. OUT Low Voltage vs. Case Temperature

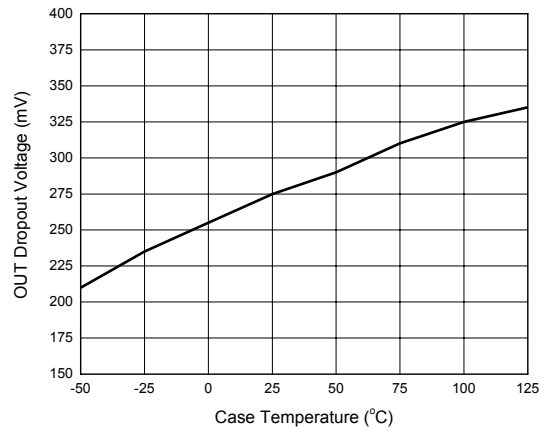


Figure 19. OUT Dropout Voltage vs. Case Temperature

**BOOST CONTROLLER****AP3039A****Application Information
Operation**

AP3039A is a boost DC-DC controller with adjustable operation frequency. Current mode control scheme provides excellent line and load regulation. Operation can be best understood by referring to Figure 3.

At the start of each oscillator cycle, the SR latch is set and external power switch Q1 (see Figure 20) turns on and the switch current will increase linearly. The voltage on external sense resistor R_{CS} (see Figure 20), connected from CS pin to GND, is proportional to the switch current. This voltage is added to a stabilizing ramp and the result is fed into the non-inversion input of the PWM comparator. When this non-inversion input voltage exceeds inversion input voltage of PWM comparator which is the output voltage of the error amplifier EA, the SR latch is reset and the external power switch turns off. The voltage level at inversion input of PWM comparator sets the peak current level to keep the output voltage in regulation. This voltage level is the amplified signal of the voltage difference between feedback voltage and reference voltage of 0.5V. So, a constant output current can be provided by this operation mode.

Input Under-Voltage Detector

AP3039A contains an Under Voltage Lock Out (UVLO) circuit. Two resistors R1 and R2 are connected from UVLO pin to ground and VIN pin respectively (see Figure 20). The resistor divider must be designed such that the voltage on the UVLO pin is higher than 1.25V when VIN is in the desired operating range. If the voltage on the pin is below under voltage threshold, all functions of AP3039A are disabled, but the system will remain in a low power standby state. UVLO hysteresis is accomplished through an internal 22 μ A current source which switched on or off 22 μ A current into the impedance of the set-point divider. When the UVLO threshold is exceeded, the current source is activated to instantly raise the voltage on the UVLO pin. When the UVLO pin voltage falls below the threshold the current source is turned off, causing the voltage on the UVLO pin to fall. The formula for UVLO can be expresses as blow:

For Input Threshold Voltage
 $V_{IN_THRESHOLD}=1.25V*(R1+R2)/R2$

For Input Hysteresis Voltage
 $V_{IN-HYSTERESIS}=22\mu A*R1$

Over Voltage Protection

AP3039A has an over voltage protection (OVP) circuit. The OV Pin is connected to the center tap of R3 and R4 resistor voltage-divider from the high voltage output to GND (see Figure 20). When the loop is open or the output voltage becomes excessive in any case, result the voltage on OV pin exceeds 1.25V, all functions of AP3039A will be disabled, and the output voltage will fall. OVP hysteresis is accomplished with an internal 22 μ A current source and the operation mode is the same as UVLO. The formula for OVP can be expresses as blow:

For OVP Voltage
 $V_{OVP}=1.25V*(R3+R4)/R4$
 For OVP Hysteresis Voltage
 $V_{OVP-HYSTERESIS}=22\mu A*R3$

Frequency Selection

An external resistor R_T , connected from RT pin to GND, is used to set the operating frequency (see Figure 20). Operation frequency range is from 150kHz to 1MHz (see Table 1). High frequency operation optimizes the regulator for the smallest component size, while low frequency operation can reduce the switch losses.

Table 1. Frequency Selection

Resistance of R_T (k Ω)	Operating Frequency (kHz)
470	150
390	200
147	400
95	600
68	800
51	1000

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Application Information (Continued)
Soft Start

AP3039A has a soft start circuit to limit the inrush current during startup. The time of soft start is controlled by an internal $12\mu\text{A}$ current source and an external soft start capacitor C_{SS} connected from SS pin to GND (see Figure 20). The effective C_{SS} voltage for Soft Start is from 0 to 2.3V, the time of Soft Start is:

$$t_{SS} = C_{SS} * 2.3V / 12\mu\text{A}$$

VCC Pin Application Description

The AP3039A includes an internal low dropout linear regulator with the output pin VCC. This pin is used to power internal PWM controller, control logic and MOSFET driver. On the condition that $V_{IN} \geq 13.5V$, the regulator generates a 10V supply. If $6V \leq V_{IN} \leq 12.5V$, the VCC is equal to V_{IN} minus drop voltage across bypass switch. When V_{IN} is less than 6V, connect VCC to V_{IN} .

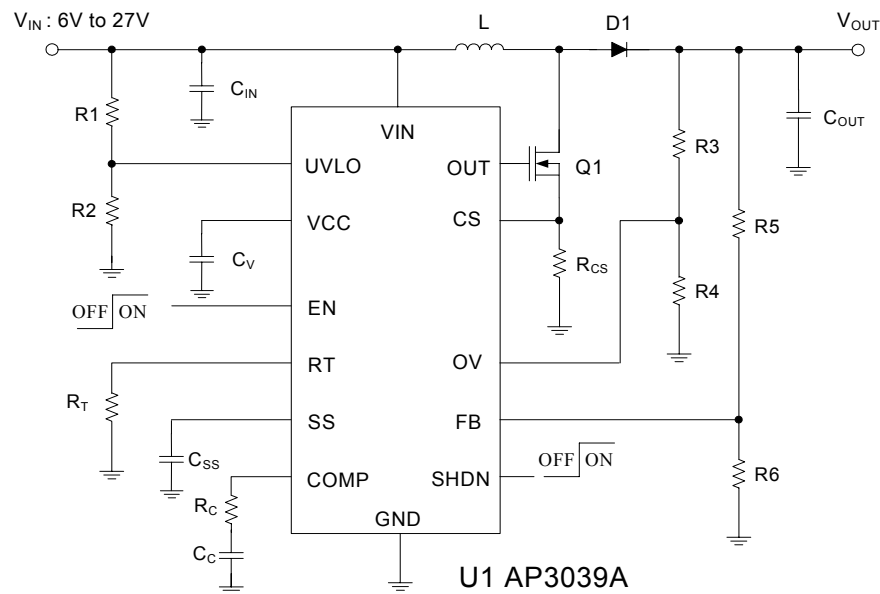
Typical Application


Figure 20. Application Circuit 1 of AP3039A (Note 2)

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Typical Application (Continued)

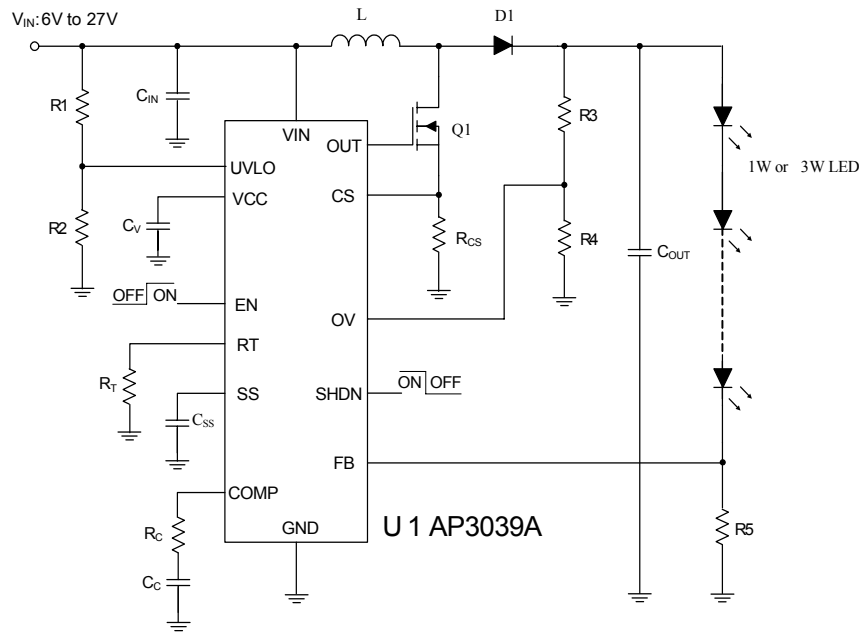


Figure 21. Application Circuit 2 of AP3039A (Driving Single 1W or 3W LED Lighting, Note 3)

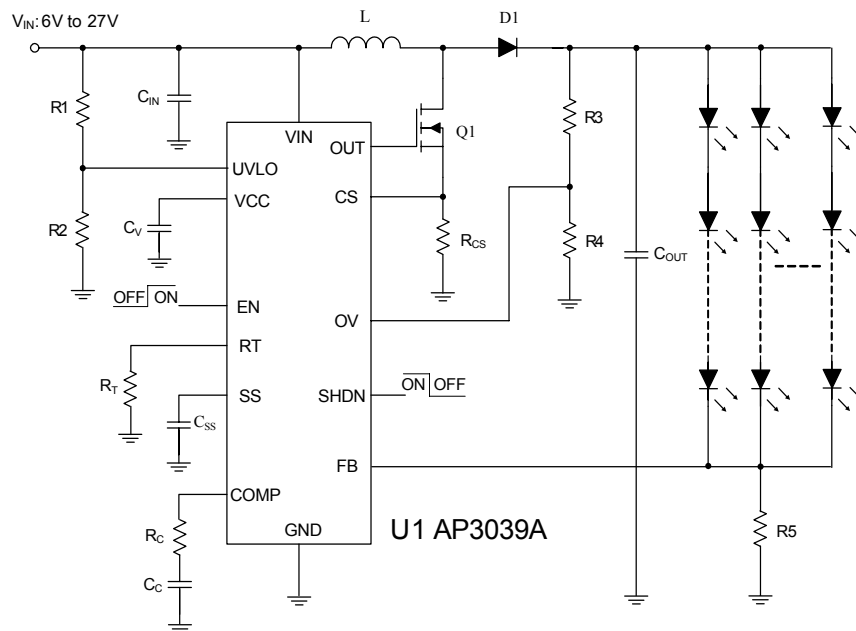


Figure 22. Application Circuit 3 of AP3039A (Backlight Driver, Note 4)

**Typical Application (Continued)**

Note 2: The output voltage is decided by R5, R6 and the internal 0.5V reference. The output voltage accuracy is determined by the accuracy of R5 and R6, for which the precise resistors are preferred.

$$V_{OUT} = \frac{0.5V}{R6} * (R5 + R6)$$

Note 3: In this application, the LED current is controlled by the feedback resistor R5. LEDs current accuracy is determined by regulator's feedback threshold accuracy and is independent of the LEDs' forward voltage variation. So the precise resistors are the better choices. The resistance of R5 is in inverse proportion to the LED current since the feedback reference is fixed at 0.5V. The relation of R5 and the LED current can be expressed as below:

$$R5 = \frac{0.5V}{I_{LED}}$$

Note 4: The summation of LED current is determined by R5 and internal 0.5V reference same as the illustration in Figure 22.

More detailed application information please refer to application note.

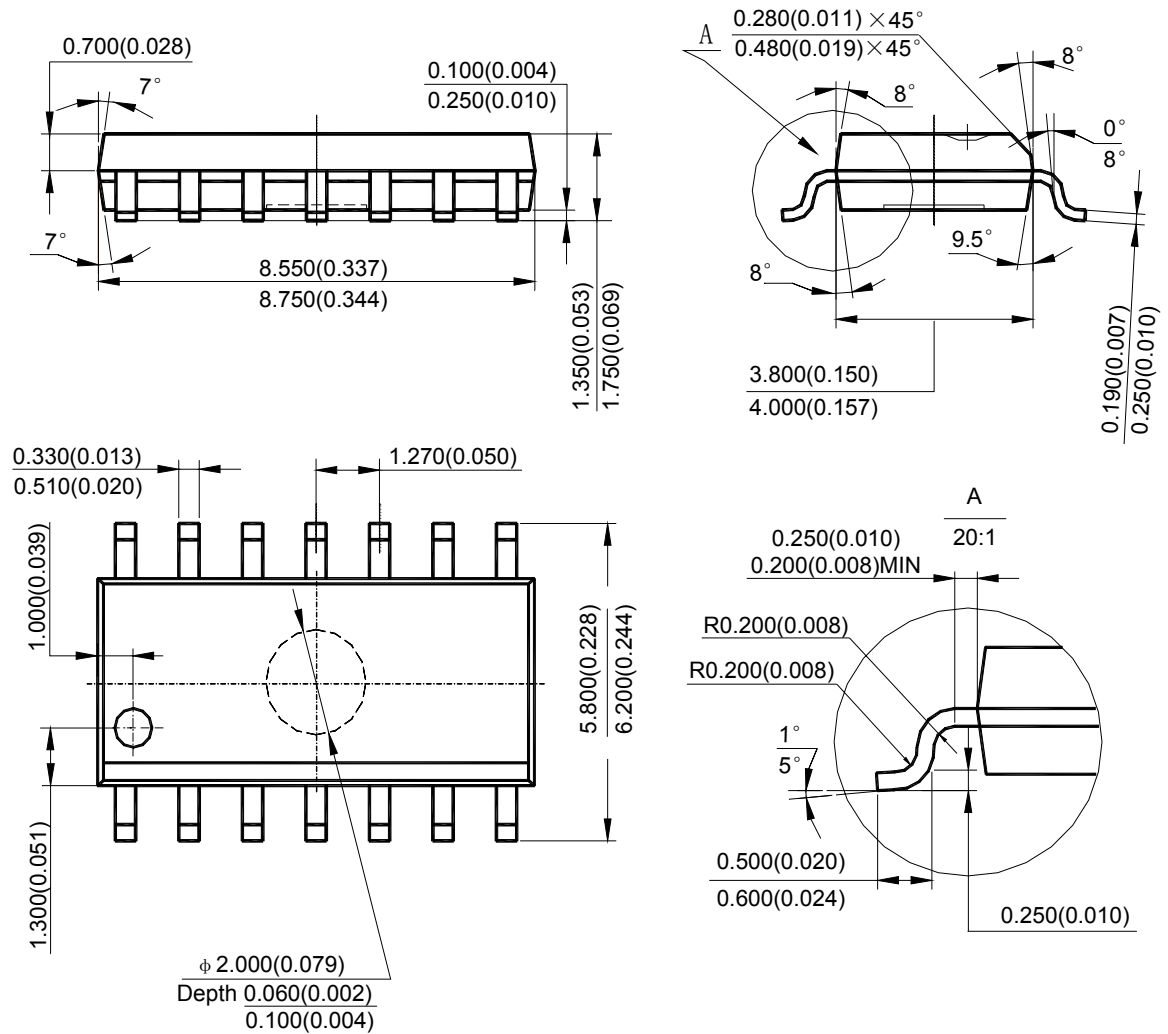
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AP3039A

Mechanical Dimensions

SOIC-14

Unit: mm(inch)



Note: Eject hole, oriented hole and mold mark is optional.



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