

## Description

The AP3427M is a high efficiency step-down dual channel DC-DC voltage converter. The chip operation is optimized by peak-current mode architecture with built-in synchronous power MOSFET switchers. The oscillator and timing capacitors are all built-in providing an internal switching frequency of 1.5MHz that allows the use of small surface mount inductors and capacitors for portable product implementations.

Integrated Soft Start (SS), Under Voltage Lock Out (UVLO), Thermal Shutdown Detection (TSD) and Short Circuit Protection are designed to provide reliable product applications.

The device is available in adjustable output voltage ranging from 0.6V to  $0.9 \times V_{IN}$  when input voltage range is from 2.5V to 5.5V, and is able to deliver up to 1A for each output.

The AP3427M is available in standard U-DFN3030-10 package.

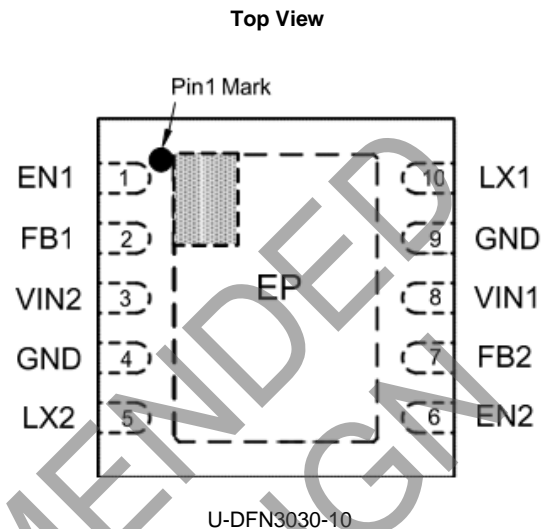
## Features

- High Efficiency Buck Power Converter
- Output Current: 1.0A/1.0A
- Low  $R_{DS(ON)}$  Internal Switches: 200m $\Omega$  ( $V_{IN}=5V$ )
- Adjustable Output Voltages from 0.6V to  $0.9 \times V_{IN}$
- Wide Operating Voltage Range: 2.5V to 5.5V
- Built-In Power Switches for Synchronous Rectification with High Efficiency
- Feedback Voltage: 600mV
- 1.5MHz Constant Frequency Operation
- Thermal Shutdown Protection
- Low Drop-Out Operation at 100% Duty Cycle
- Input Over Voltage Protection
- No Schottky Diode Required
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](#) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

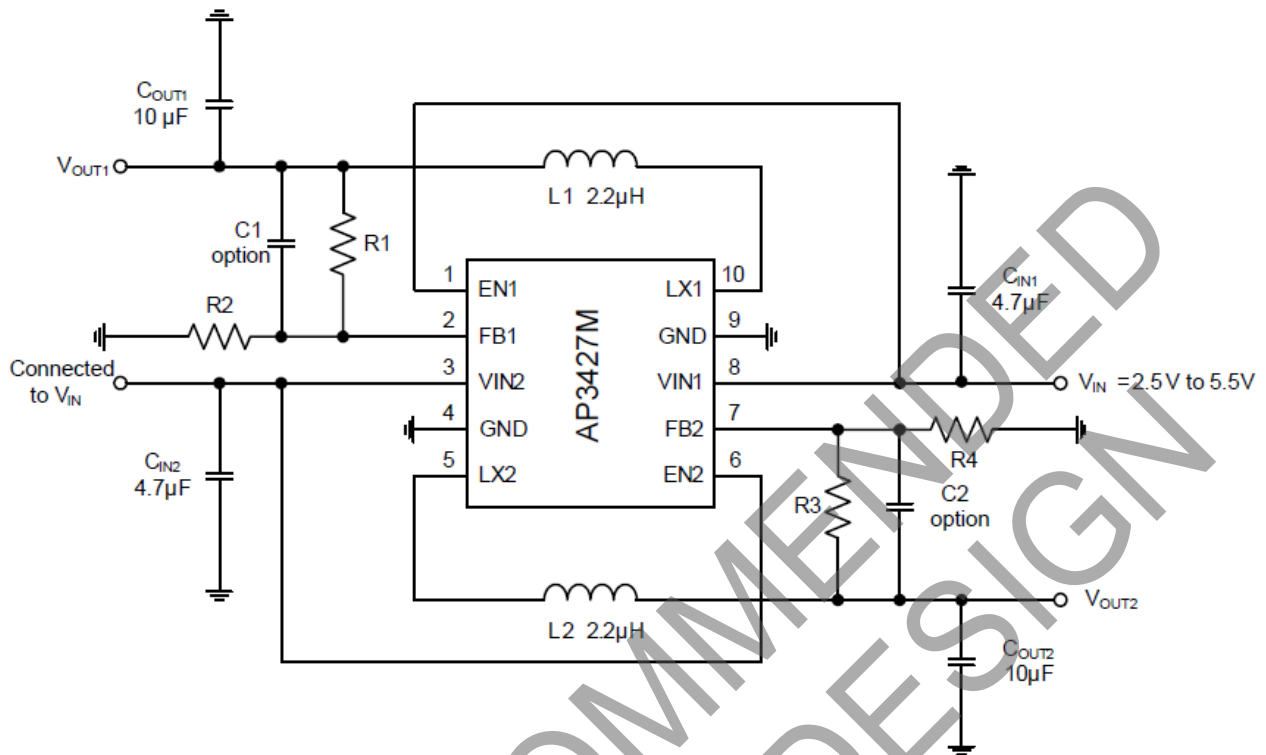
## Pin Assignments



## Applications

- Post DC-DC Voltage Regulation
- PDA and Notebook Computer

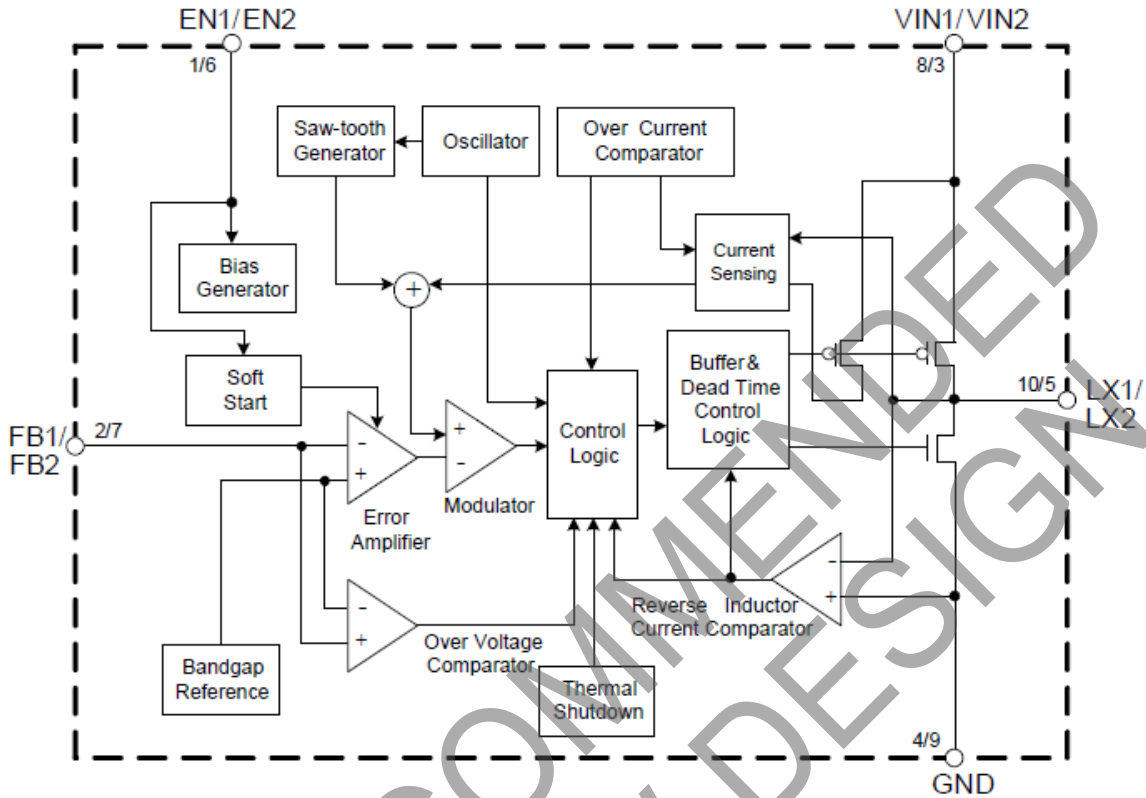
**Typical Applications Circuit**



**Pin Descriptions**

Pin Number	Pin Name	Function
1	EN1	Enable signal input of channel 1, active high
2	FB1	Feedback voltage of channel 1
3	VIN2	Power supply input of channel 2
4, 9, Exposed Pad	GND	GND. It should be connected to system ground
5	LX2	Connected from channel 2's Power MOSFET to inductor
6	EN2	Enable signal input of channel 2, active high
7	FB2	Feedback voltage of channel 2
8	VIN1	Power supply input of channel 1
10	LX1	Connected from channel 1's Power MOSFET to inductor

**Functional Block Diagram**



**Absolute Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.) (Note 4)

Parameter	Symbol	Value	Unit
Supply Input Voltage for the MOSFET Switch	V <sub>IN1</sub> , V <sub>IN2</sub>	0 to 6.5	V
LX Pin Switch Voltage	V <sub>LX1</sub> , V <sub>LX2</sub>	-0.3 to V <sub>IN</sub> +0.3	V
Enable Input Voltage	V <sub>EN2</sub> , V <sub>EN2</sub>	-0.3 to V <sub>IN</sub> +0.3	V
LX Pin Switch Current	I <sub>LX1</sub> , I <sub>LX2</sub>	1.8	A
Power Dissipation (On PCB, T <sub>A</sub> =+25°C)	P <sub>D</sub>	2.44	W
Thermal Resistance (Junction to Ambient, Simulation)	θ <sub>JA</sub>	41	°C/W
Thermal Resistance (Junction to Case, Simulation)	θ <sub>JC</sub>	4.2	°C/W
Operating Junction Temperature	T <sub>J</sub>	+155	°C
Operating Temperature	T <sub>OP</sub>	-40 to +85	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	°C
ESD (Human Body Model)	V <sub>HBM</sub>	2,000	V
ESD (Machine Model)	V <sub>MM</sub>	200	V

Note: 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

**Recommended Operating Conditions** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
V <sub>IN</sub>	Supply Input Voltage	2.5	5.5	V
T <sub>J</sub>	Operating Junction Temperature	-40	+125	°C
T <sub>A</sub>	Ambient Temperature	-40	+80	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.) (V<sub>IN</sub>=V<sub>IN1</sub>=V<sub>IN2</sub>=5V, V<sub>EN1</sub>=V<sub>EN2</sub>=5V, V<sub>FB1</sub>=V<sub>FB2</sub>=0.6V, L<sub>1</sub>=L<sub>2</sub>=2.2μH, C<sub>IN1</sub>=C<sub>IN2</sub>=4.7μF, C<sub>OUT1</sub>=C<sub>OUT2</sub>=10μF, I<sub>MAX</sub>=1A per Channel, unless otherwise specified.)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Input Voltage Range	V <sub>IN</sub>	V <sub>IN</sub> = V <sub>IN1</sub> = V <sub>IN2</sub>	2.5	—	5.5	V
Shutdown Current	I <sub>OFF</sub>	V <sub>EN1</sub> = V <sub>EN2</sub> = 0	—	0.1	1	μA
Active Current	I <sub>ON</sub>	V <sub>FB1</sub> = V <sub>FB2</sub> = 0.7V, I <sub>OUT1</sub> = I <sub>OUT2</sub> = 0A	—	220	—	μA
Regulated Feedback Voltage	V <sub>FB</sub>	For Adjustable Output Voltage	0.588	0.6	0.612	V
Regulated Output Voltage Accuracy	ΔV <sub>OUT</sub> /V <sub>OUT</sub>	V <sub>IN</sub> = 2.5V to 5.5V, I <sub>OUT1</sub> or I <sub>OUT2</sub> = 0 to 1A	-3	—	3	%
Peak Inductor Current	I <sub>PK</sub>	—	1.5	—	—	A
Oscillator Frequency	f <sub>OSC</sub>	V <sub>IN</sub> = 2.5V to 5.5V	1.2	1.5	1.8	MHz
PMOSFET R <sub>DS(ON)</sub>	R <sub>DS(ON)_P</sub>	V <sub>IN</sub> = 5V	—	200	—	mΩ
NMOSFET R <sub>DS(ON)</sub>	R <sub>DS(ON)_N</sub>	V <sub>IN</sub> = 5V	—	200	—	mΩ
EN High-Level Input Voltage	V <sub>EN_H</sub>	—	1.5	—	—	V
EN Low-Level Input Voltage	V <sub>EN_L</sub>	—	—	—	0.4	V
EN Input Current	I <sub>EN</sub>	—	—	—	0.1	μA
Soft Start Time	t <sub>SS</sub>	—	—	400	—	μs
Maximum Duty Cycle	D <sub>MAX</sub>	—	100	—	—	%
Input Over Voltage Protection	V <sub>OIP</sub>	—	—	6.2	—	V
Under Voltage Lock Out Threshold	V <sub>UVLO</sub>	Rising	—	2.3	—	V
		Falling	—	2.1	—	
		Hysteresis	—	0.2	—	
Thermal Shutdown	T <sub>SD</sub>	Hysteresis=+30°C	—	+155	—	°C

**Typical Performance Characteristics**

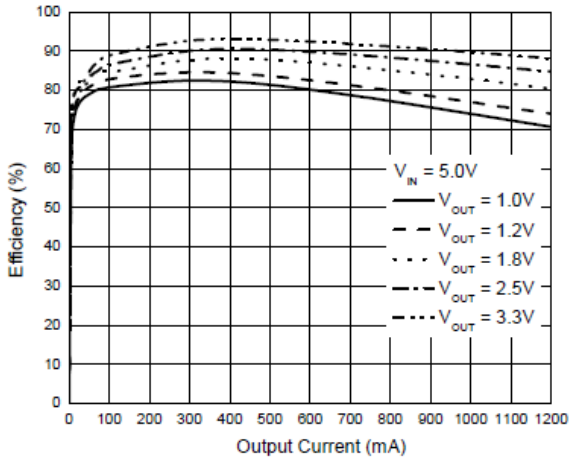


Figure 1 Efficiency vs. Output Current ( $V_{IN} = 5V$ )

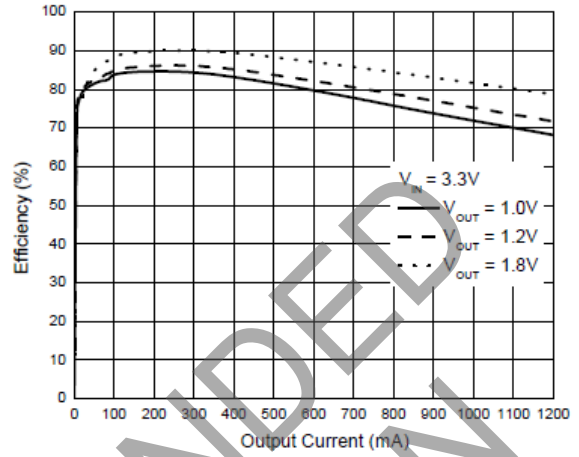


Figure 2 Efficiency vs. Output Current ( $V_{IN} = 3.3V$ )

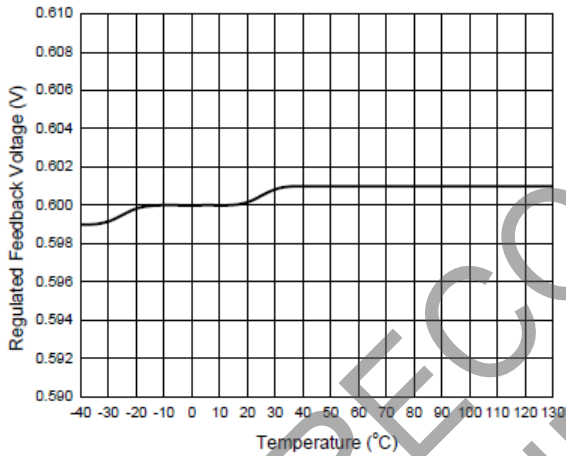


Figure 3 Regulated Feedback Voltage vs. Temperature

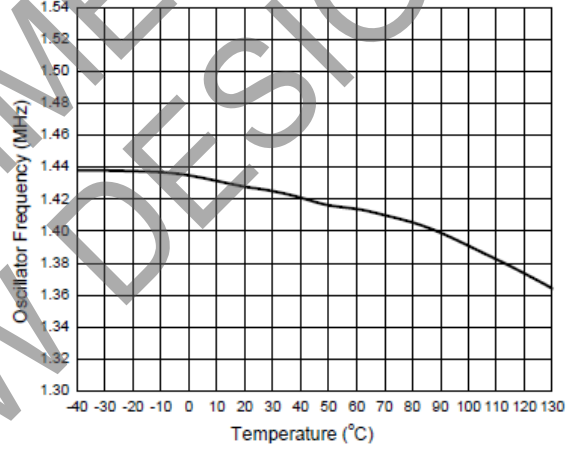


Figure 4 Oscillator Frequency vs. Temperature

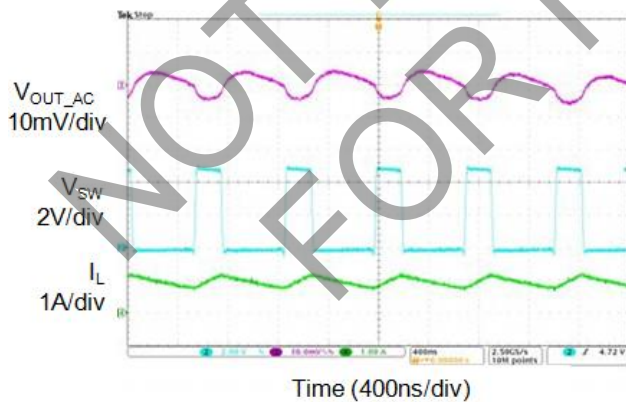


Figure 5 Output Ripple ( $V_{OUT} = 1.2V$ ,  $I_{OUT} = 1A$ )

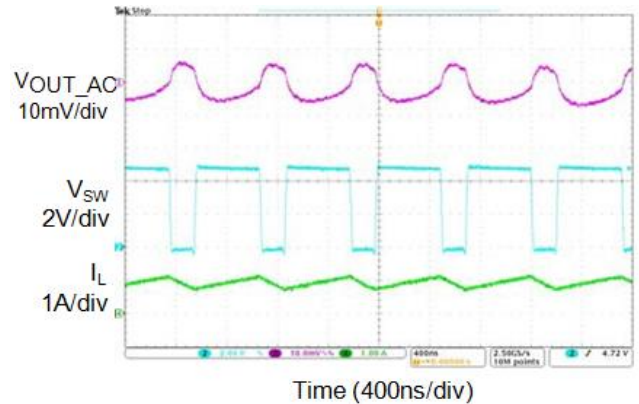
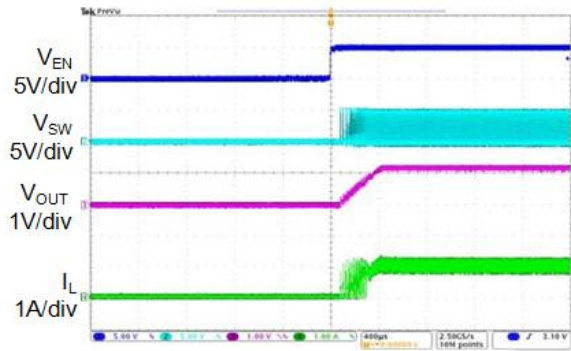
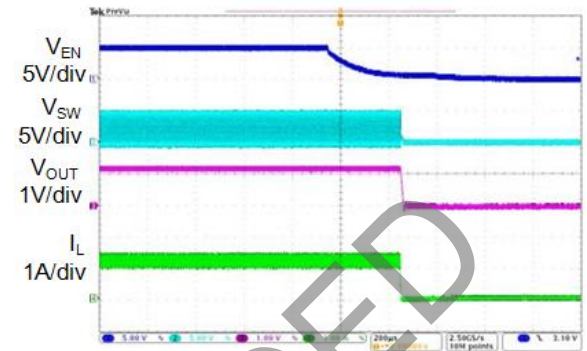


Figure 6 Output Ripple ( $V_{OUT} = 3.3V$ ,  $I_{OUT} = 1A$ )

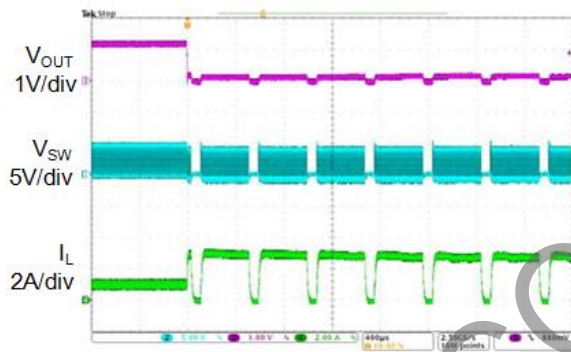
**Typical Performance Characteristics (Cont.)**



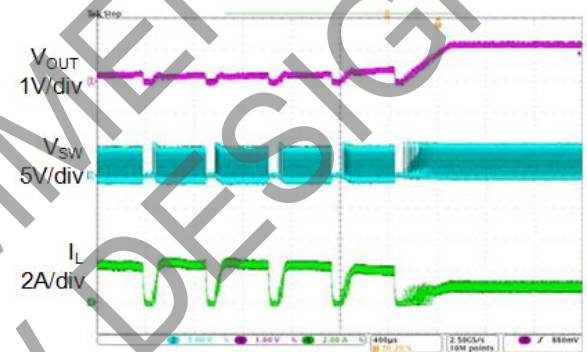
Time (400µs/div)  
Figure 7 Enable Turn On (Iout = 1A)



Time (200µs/div)  
Figure 8 Enable Turn Off (Iout = 1A)



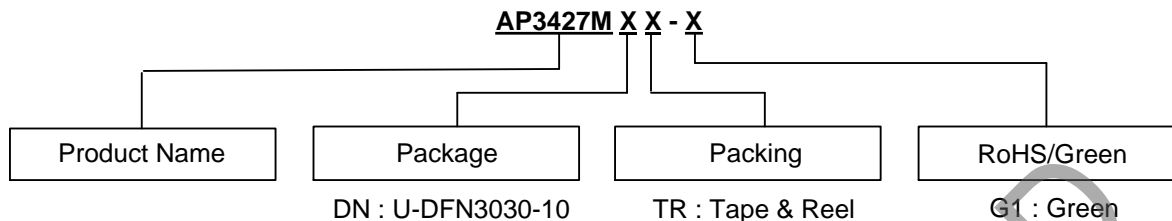
Time (400µs/div)  
Figure 9 Short Circuit Protection (Iout = 1A)



Time (400µs/div)  
Figure 10 Short Circuit Protection Recovery (Iout = 1A)

NOT RECOMMENDED FOR NEW DESIGN

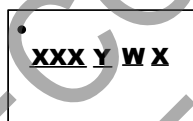
**Ordering Information**



Part Number	Identification Code	Package	Temperature Range	13" Tape and Reel	
				Quantity	Part Number Suffix
AP3427MDNTR-G1	BFE	U-DFN3030-10	-40°C to +80°C	5000/Tape & Reel	-13

**Marking Information**

( Top View )



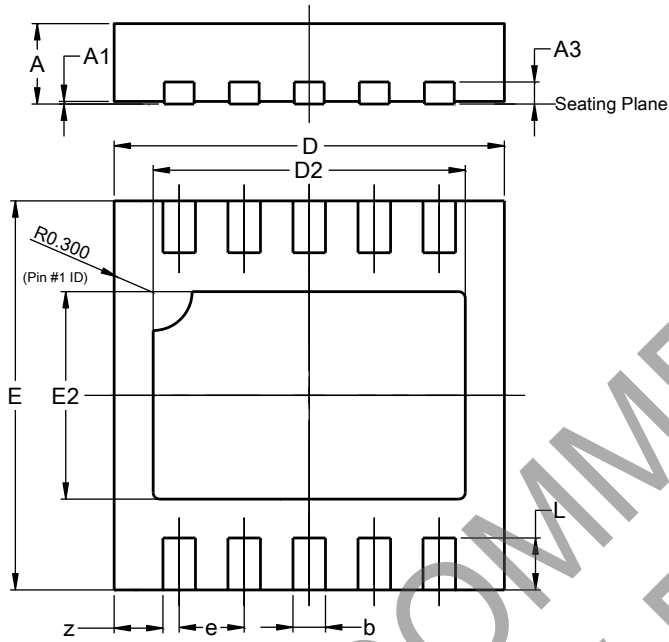
**XXX** : Identification Code  
**Y** : Year 0~9  
**W** : Week : A~Z : 1~26 week;  
a~z : 27~52 week; z represents  
52 and 53 week  
**X** : Internal Code

Part Number	Package	Identification Code
AP3427MDNTR-G1	U-DFN3030-10	BFE

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**U-DFN3030-10**

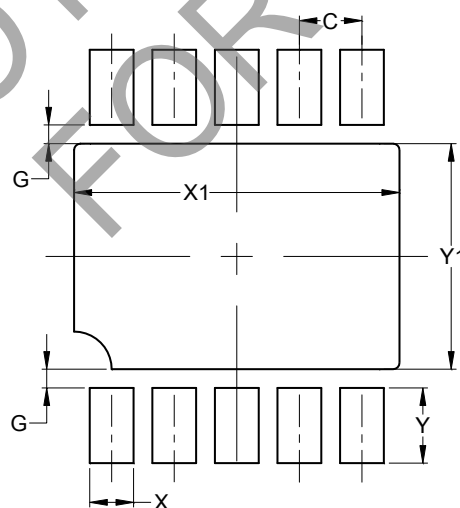


U-DFN3030-10			
Dim	Min	Max	Typ
A	0.57	0.63	0.60
A1	0.00	0.05	0.02
A3	—	—	0.15
b	0.20	0.30	0.25
D	2.90	3.10	3.00
D2	2.30	2.50	2.40
E	2.90	3.10	3.00
E2	1.50	1.70	1.60
e	—	—	0.50
L	0.25	0.55	0.40
z	—	—	0.375
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**U-DFN3030-10**



Dimensions	Value (in mm)
C	0.50
G	0.15
X	0.35
X1	2.60
Y	0.60
Y1	1.80



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