

3A 2MHZ HIGH PERFORMANCE SYNCHRONOUS BUCK CONVERTER

Description

The AP3433 is a current mode, PWM synchronous buck (step-down) DC-DC converter, capable of driving a 3A load with high efficiency, excellent line and load regulation.

The device integrates two N-channel power MOSFETs with low onresistance. Current mode control provides fast transient response and cycle-by-cycle current limit.

The switching frequency of AP3433 can be programmable from 300kHz to 2MHz, which allows small-sized components, such as capacitors and inductors. A standard series of inductors from several different manufacturers are available. This feature greatly simplifies the design of switch-mode power supplies.

Under voltage lockout is internally set at 2.6V, but can be increased by programming the threshold with a resistor network on the enable pin. The output voltage startup ramp is controlled by the soft-start pin. An open drain power good signal indicates the output is within 93% to 107% of its nominal voltage.

The AP3433 is available in QFN-3x3-16 package.

Features

- Input Voltage Range: 2.95V to 5.5V
- 0.827V Reference Voltage with ±3% Precision
- Two 45mΩ(typical) MOSFETs for High Efficiency at 3A Load
- High Efficiency: up to 94%
- Output Current: 3A
- Programmable Frequency:300kHz to 2MHz
- Current Mode Control
- Synchronizes to External Clock
- Adjustable Soft-start
- Soft Start-up into Pre-biased Output
- UV and OV Power Good Output
- Built-in Over Current Protection
- Built-in Thermal Shutdown Function
- Programmable UVLO Function
- Built-in Over Voltage Protection
- Thermally Enhanced 3mm×3mm 16-pin QFN

Pin Assignments



Applications

Low-voltage, High-density Power Systems

Point of Load Regulation for Consumer Applications such as Set Top Boxes, LCD Displays, CPE Equipment



Typical Applications Circuit



Pin Descriptions

Pin Number	Pin Name	Function			
1,2,16	VIN	Supply input pin. A capacitor should be connected between the VIN and GND pin to keep the DC input voltage constant			
3,4	GND	Power ground. This pin should be electrically connected to the power pad under the IC			
5	AGND	Analog ground. This pin should be electrically connected to GND close to the device			
6	FB	Feedback pin. Inverting node of the transconductance error amplifier			
7	COMP	Compensation pin. This pin is the output of the transconductance error amplifier and the input to the current comparator. Connect external compensation elements to this pin to stabilize the control loop			
8	RT/CLK	Resistor timing or external clock input pin			
9	SS	Soft-start pin. An external capacitor connected to this pin sets the output voltage rise time. This pin can also be used for tracking			
10,11,12	SW	Internal power switch output pin. This pin is connected to the inductor and bootstrap capacitor			
13	воот	Bootstrap pin. A bootstrap capacitor is connected between the BOOT pin and SW pin. The voltage across the bootstrap capacitor drives the internal high-side power MOSFET			
14	PGD	Power good indicator output. Asserts low if output voltage is low due to thermal shutdown, over- current, over/under-voltage or EN shut down			
15	EN	Enable pin, internal pull-up current source. Pull below 1.2V to disable. Float to enable. Can be used to set the on/off threshold (adjust UVLO) with two additional resistors			



Functional Block Diagram





Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Value	Unit
V _{IN}	VIN Pin Voltage	-0.3 to 6.5	V
V _{EN}	EN Pin Voltage	-0.3 to 6.5	V
V _{SW}	SW Pin Voltage	-0.3 to V _{IN} +0.3	V
V _{FB}	FB Pin Voltage	-0.3 to 6.5	V
V _{COMP}	COMP Pin Voltage	-0.3 to 6.5	V
V _{PGD}	PGD Pin Voltage	-0.3 to 6.5	V
V _{RT/CLK}	RT/CLK Pin Voltage	-0.3 to 6.5	V
V _{SS}	SS Pin Voltage	-0.3 to 6.5	V
θ _{JA}	Thermal Resistance (Junction to Ambient)	70	°C/W
$\theta_{JC \ (BOTTOM)}$	Thermal Resistance (Junction to Case)	5	°C/W
TJ	Operating Junction Temperature	-40 to +125	°C
T _{STG}	Storage Temperature	-65 to +150	°C
T _{LEAD}	Lead Temperature (Soldering, 10sec)	+260	٥C
_	ESD (Machine Model)	200	V
_	ESD (Human Body Model)	2000	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Мах	Unit
V _{IN}	Input Voltage	2.95	5.5	V
OUT(MAX)	Maximum Output Current	3	-	А
T _A	Operating Ambient Temperature	-40	+85	°C



Electrical Characteristics (V_{IN} =2.95 to 5.5V, T_A =25°C, unless otherwise specified. Specifications with boldface type apply over full operating temperature range from -40 to +85°C.)

Symbol	Parameters	Conditions	Min	Тур	Мах	Unit	
SUPPLY VOLTAGE (VIN PIN)							
V _{IN}	Input Voltage	_	2.95	_	5.5	V	
Ι _Q	Quiescent Current	$\label{eq:VFB} \begin{array}{l} V_{FB} = 0.9 V, V_{IN} = 5 V, \ T_A = 25^{\circ} C, \\ R_T = 400 k \Omega \end{array}$	_	360	575	μA	
I _{SHDN}	Shutdown Supply Current	V _{EN} =0V,T _A =25°C 2.95V≤V _{IN} ≤5.5V	_	2	5	μΑ	
ENABLE AND UVLO	(EN PIN)						
V _{EN_H}		Rising	1.16	1.25	1.37	V	
V _{EN_L}	Enable Infeshold	Falling	-	1.18	-	V	
V _{UVLO}	Internal Under Voltage Lockout Threshold	_	-	2.6	2.8	V	
V _{HYS}	Internal Under Voltage Hysteresis	-	-	150	_	mV	
VOLTAGE REFEREN	ICE (FB PIN)				•		
V _{REF}	Voltage Reference	2.95V≤V _{IN} ≤5.5V	0.802	0.827	0.852	V	
MOSFET							
Ron H	High Side Switch On-resistance	V _{BOOT-SW} =5V	-	45	81	mΩ	
ung.		V _{BOOT-SW} =2.95V	-	64	110	mΩ	
Bould	Low Side Switch On-resistance	V _{IN} =5V	_	42	81	mΩ	
· · · · · ·		V _{IN} =2.95V	-	59	110	mΩ	
CURRENT LIMIT							
I _{LIMIT}	Current Limit Threshold	-	4.2	6.6	-	А	
THERMAL SHUTDOWN							
T _{TSD}	Thermal Shutdown	-	_	+140	_	°C	
_	Hysteresis	-	-	+20	_	°C	



Electrical Characteristics (Cont. V_{IN} =2.95 to 5.5V, T_A =25°C, unless otherwise specified. Specifications with boldface type apply over full operating temperature range from -40 to +85°C.)

Symbol	Parameters	Conditions	Min	Тур	Max	Unit	
TIMING RESISTOR AND EXTERNAL CLOCK (RT/CLK PIN)							
_	Switching Frequency Range (RT Mode)	_	300	_	2000	kHz	
_	Switching Frequency Range (CLK Mode)	_	300	_	2000	kHz	
fs	Switching Frequency	R _T =400kΩ	400	500	600	kHz	
_	Minimum CLK Pulse Width	-	75	-		ns	
_	RT/CLK Voltage	R _T =400kΩ	-	0.5	_	V	
_	RT/CLK High Threshold	_	_	1.6	2.2	V	
_	RT/CLK Low Threshold	-	0.4	0.6		V	
BOOT (BOOT PIN)							
R _{BOOT}	BOOT Charge Resistor	V _{IN} =5V		16	-	Ω	
_	BOOT-SW UVLO	V _{IN} =2.95V	-	2.2	_	V	
SOFT START (SS PIN)							
I _{SS}	Charge Current	V _{SS} =0.4		2.2	_	μA	
V _{SS}	SS to Reference Crossover	98% Nominal	-	1.1	_	V	
POWER GOOD (PGD PIN)							
V _{FB TH}	Feedback Threshold	V _{FB} Falling (Fault)	_	91	_		
		V _{FB} Rising (Good)	_	93	_	%\/PEF	
		V _{FB} Rising (Fault)	_	107	_	70 V KEF	
		V _{FB} Falling (Good)	_	105	_		



Performance Characteristics





Performance Characteristics (Cont.)





Performance Characteristics (Cont.)





Package Outline Dimensions (All dimensions in mm(inch).)

(1) Package Type: QFN-3×3-16









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