

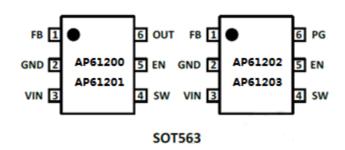
#### **DESCRIPTION**

The AP61203 is a 1A, Synchronous Buck Converter with an input voltage range of 2.3V to 5.5V and fully integrates a  $120m\Omega$  high-side power MOSFET and an  $80m\Omega$  low-side power MOSFET to provide high-efficiency step-down DC-DC conversion.

The AP61203 device is easily used by minimizing the external component count due to its adoption of constant on-time (COT) control to achieve fast transient responses, ease loop stabilization, and low output voltage ripple. Moreover, AP61203 also features force PWM mode control through EN pin.

The device is available in a SOT563 package.

#### **PIN ASSIGNMENTS**



### **FEATURES**

- Input Range: 2.3V to 5.5V
- Wide Output Voltage Range: 0.6V to 3.6
- 1A Continuous Output Current
- 0.6V ± 2% Reference Voltage
- 14µA Ultralow Quiescent Current (Pulse Frequency Modulation)
- 2.2MHz Switching Frequency
- Force Pulse Width Modulation
- Power-Good indicator
- Protection Circuitry
  - Undervoltage Lockout (UVLO)
  - VIN Overvoltage Protection (OVP)
  - Peak Current Limit
  - Valley Current Limit
  - Thermal Shutdown

#### **APPLICATIONS**

- 5V Input Distributed Power Bus Supplies
- White Goods and Small Home Appliances
- FPGA, DSP, and ASIC Supplies
- Network Video Cameras
- Wireless Routers
- Consumer Electronics
- General Purpose Point of Load



## **FUNCTIONAL BLOCK**

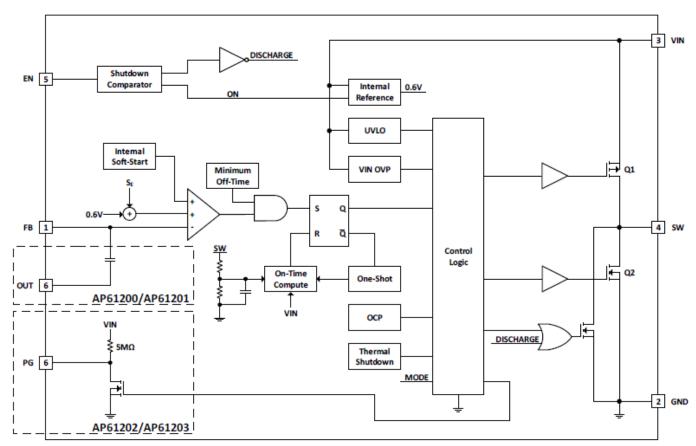


Figure 1. Functional Block Diagram



## ABSOLUTE MAXIMUM RATINGS (Note 1) (At T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit	
VIN	Supply Pin Voltage	-0.3 to +6.5 (DC)	V	
VIIN	Supply Fill Vollage	-0.3 to + 7.0 (400ms)		
VFB	Feedback Pin Voltage	Feedback Pin Voltage -0.3 to VIN + 0.3		
VSW	Switch Din Voltage	-1.0 to VIN + 0.3 (DC)	V	
VSVV	Switch Pin Voltage	-3 to VIN + 2.0 (20ns)		
VEN	Enable Pin Voltage	-0.3 to VIN + 0.3	V	
TST	Storage Temperature	-65 to +150	°C	
TJ	Junction Temperature	mperature +150		
TL	Lead Temperature +260		°C	
ESD Susceptibility (No	ote 2)			
HBM	Human Body Model	el 2000 V		
CDM	Charged Device Model	Charged Device Model 1000		

Notes:

## **RECOMMENDED OPERATING CONDITIONS** (At T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
VIN	Supply Voltage	2.3	5.5	V
VOUT	Output Voltage	0.6	3.6	V
T <sub>A</sub>	Operating Ambient Temperature Range	-40	+85	°C
TJ	Operating Junction Temperature Range	-40	+125	°C

Stresses greater than the Absolute Maximum Ratings specified above may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions exceeding those indicated in this specification is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time.

<sup>2.</sup> Semiconductor devices are ESD sensitive and may be damaged by exposure to ESD events. Suitable ESD precautions should be taken when handling and transporting these devices.

# AP61203Z6-EVM



#### 2.3V TO 5.5V INPUT, 2A SYNCHRONOUS BUCK CONVERTER

### **QUICK START GUIDE**

The AP61203Z6-EVM has a simple layout and allows access to the appropriate signals through test points. To evaluate the performance of the AP61203Z6, follow the procedure below:

- For evaluation board configured at V<sub>OLIT</sub>=1.8V, connect a power supply to the input terminals VIN and GND. Set VIN to 5V.
- 2. Connect the positive terminal of the electronic load to VOUT and negative terminal to GND.
- 3. For Enable, place a jumper to "H" position to enable IC. Jump to "L" position to disable IC.
- 4. The evaluation board should now power up with a 1.8V output voltage.
- 5. Check for the proper output voltage of 1.8V (±1%) at the output terminals VOUT and GND. Measurement can also be done with a multimeter with the positive and negative leads between VOUT and GND.
- Set the load to 2A through the electronic load. Check for the stable operation of the SW signal on the oscilloscope. Measure the switching frequency.

### **MEASUREMENT/PERFORMANCE GUIDELINES:**

- 1) When measuring the output voltage ripple, maintain the shortest possible ground lengths on the oscilloscope probe. Long ground leads can erroneously inject high frequency noise into the measured ripple.
- 2) For efficiency measurements, connect an ammeter in series with the input supply to measure the input current. Connect an electronic load to the output for output current. Test the input capacitor voltage and output capacitor voltage with a multimeter as input voltage and output voltage.



### **Setting the Output Voltage of AP61203**

#### 1) Setting the output voltage

The AP61203 features external programmable output voltage by using a resistor divider network R1 and R2 as shown in the typical application circuit. The output voltage is calculated as below,

$$V_{OUT} = 0.6 \times \left(\frac{R_1 + R_2}{R_2}\right)$$

First, select a value for R2 according to the value recommended in the table 1. Then, R2 is determined. The output voltage is given by Table 1 for reference. For accurate output voltage, 1% tolerance is required.

Table 1. Resistor selection for output voltage setting

Vo	R1	R2	C3	C5
1.0V	200ΚΩ	301ΚΩ	10uF	33pF
1.2V	200ΚΩ	200ΚΩ	10uF	33pF
1.5V	200ΚΩ	133ΚΩ	10uF	33pF
1.8V	200ΚΩ	100ΚΩ	10uF	33pF
2.5V	200ΚΩ	63.2ΚΩ	10uF	33pF
3.3V	200ΚΩ	44.2ΚΩ	10uF	33pF

#### **EXTERNAL COMPONENT SELECTION:**

Inductor (L)

- (1) Low DCR for good efficiency
- (2) Inductance saturate current must be higher than 3.5A
- (3) 1.0µH inductor of Würth Elektronik(PN. 744 383 570 10) is recommended for all application circuits.

### **EVALUATION BOARD SCHEMATIC**

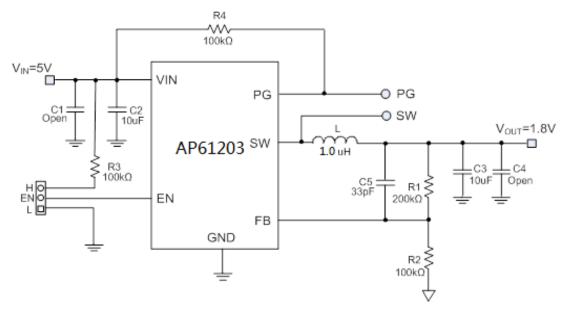


Figure 2. Typical Application Circuit

### **PCB TOP LAYOUT**

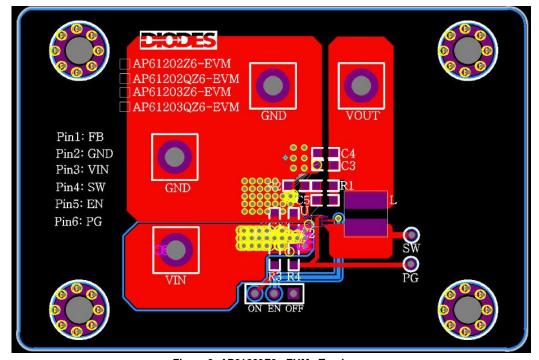


Figure 3. AP61203Z6 - EVM - Top Layer

## **PCB BOTTOM LAYOUT**

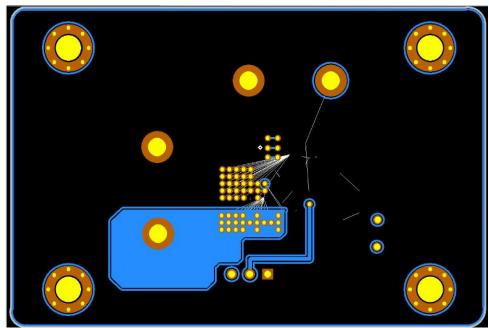


Figure 4. AP61203Z6 - EVM - Bottom Layer

## **EV Board View**



Figure 5. AP61203Z6 EV Board View

## BILL OF MATERIALS for AP61203Z6-EVM (Vout=1.8V)

Item	Value	Туре	Rating	Description	Description
C2	10uF	X5R/X7R, Ceramic/0805	10V	Input coupling CAP	TAIYO YUDEN EMK212ABJ106KD-T
C3	10uF	X5R/X7R, Ceramic/0805	10V	Output coupling CAP	TAIYO YUDEN EMK212ABJ106KD-T
L	1.0uH	SMD	>3.5A	Inductor	WURTH ELEC 74438357010
R1	200K	0805	1%	Voltage set RES*	
R2	100K	0805	1%	Voltage set NLS	
R3	100K	0805	1%	EN RES*	
U1		AP61203		SOT563	Diodes BCD

### **TYPICAL PERFORMANCE CHARACTERISTICS**

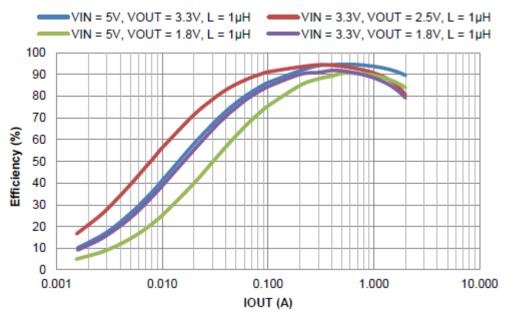


Figure 6. Efficiency vs. Output Current



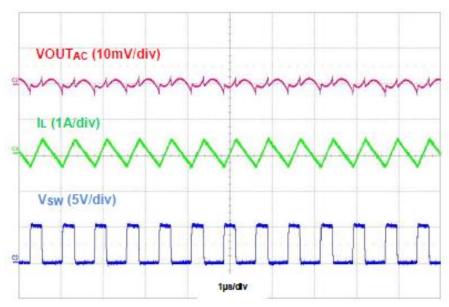


Figure 7. Output Voltage Ripple, IOUT = 50mA

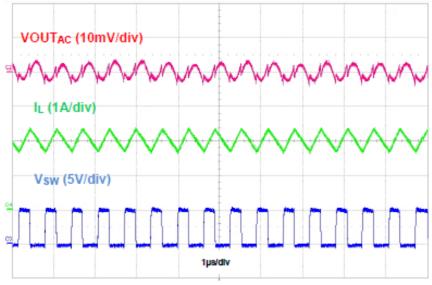


Figure 8. Output Voltage Ripple, IOUT =2A

## AP61203Z6-EVM



#### 2.3V TO 5.5V INPUT, 2A SYNCHRONOUS BUCK CONVERTER

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