Description

The DIODES™ AP7368 is a low dropout regulator featuring 500mA/1A that provides high output voltage accuracy, low $R_{DS(on)}$, high PSRR, low output noise, and low quiescent current. This regulator is based on a CMOS process.

The AP7368 includes a voltage reference, error amplifier, current-limit circuit, and enable inputs to turn on and off separately. With the integrated resistor network, fixed output voltage versions can be delivered.

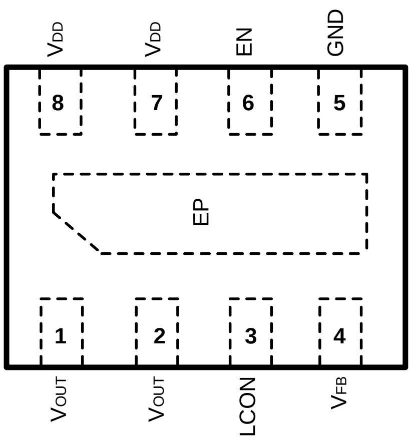
With the device's low power consumption and line and load transient response, the AP7368 is well suited for low power handheld communication equipment.

The AP7368 can select the output current limit 1.0A or 500mA by alternating the LCON pin between "H" or "L".

The AP7368 is packaged in the X1-DFN1612-8 (Type B) package, allowing for the smallest footprint and a dense PCB layout.

Pin Assignments

(TOP View)



X1-DFN1612-8 (Type B)

Features

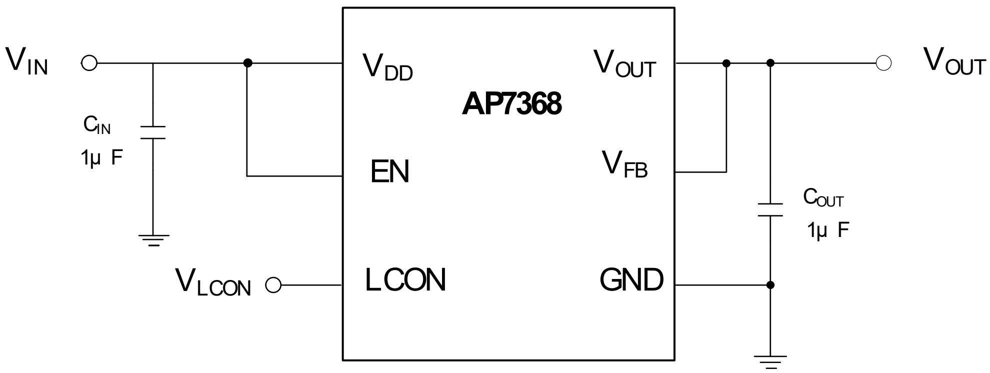
- Low V_{IN} and Wide V_{IN} Range: 1.7V to 5.5V
- Guarantee Output Current: 500mA/1A (set by LCON)
- V_{OUT} Accuracy ±1%
- Ripple Rejection 80dB at 1kHz
- Low Output Noise, 16µVrms from 10Hz to 100kHz
- Quiescent Current as Low as 110µA
- V_{OUT} Fixed 0.9V to 3.3V
- Inrush Current Limit: 300mA (LCON="L")
- Foldback Short Circuit Protection: 60mA (LCON="L")
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/**

Applications

- Smart Phones/Tablets
- RF Supplies
- Cameras
- Portable Videos
- Portable Media Players
- Wireless Adapters
- Wireless Communication

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

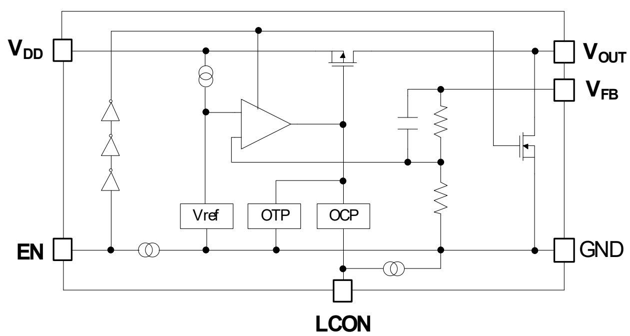
Typical Applications Circuit



Pin Descriptions

Pin Number	Pin Name	Function
1, 2	V_{OUT}	Output Voltage Pin
3	LCON	Output Current Limit Setting Pin ("H"=1A, "L"=500mA)
4	V_{FB}	Feedback Voltage Pin
5	GND	Ground
6	EN	Enable Pin This pin should be driven either high or low and must not be floating. Driving this pin high enables the regulator, while pulling it low puts the regulator into shutdown mode
7,8	V_{DD}	Power Input Pin
EP	Exposed Pad	In PCB layout, prefer to use large copper area to cover this pad for better thermal dissipation, then connect this area to GND or leave it open. However, do not use it as GND electrode function alone

Functional Block Diagram



Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Ratings	Unit
ESD HBM	Human Body Mode ESD Protection	> 2	KV
ESD MM	Machine Mode ESD Protection	> 200	V
V_{IN}	Input Voltage	6.0	V
V_{EN}	Input Voltage for EN Pin	6.0	V
V_{OUT}	Output Voltage	-0.3 to V_{IN} + 0.3	V
I_{OUT}	Each Channel Output Current	1000	mA
P_D	Power Dissipation	1700	mW
T_A	Operating Ambient Temperature	-40 to +85	°C
T_J	Operating Junction Temperature	-40 to +150	°C
T_{STG}	Storage Temperature	-55 to +150	°C

Note: 4. a). Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these conditions is not implied. Exposure to absolute-maximum-rated conditions for extended period may affect device reliability.
b). Ratings apply to ambient temperature at +25°C. The JEDEC High-K board design used to derive this data was a 3 inch x 3 inch multilayer board with 1oz. internal power and ground planes and 2oz. copper traces on the top and bottom of the board.

Recommended Operating Conditions (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
V_{IN}	Input Voltage	1.7	5.5	V
I_{OUT}	Each Channel Output Current	0	1000	mA
T_A	Operating Ambient Temperature	-40	+85	°C

Electrical Characteristics (@T_A = +25°C, V_{DD} = V_{OUT} +1.0V, C_{IN} = C_{OUT} = 1.0µF, I_{OUT} = 1.0mA, unless otherwise specified.)

Parameter	Condition		Min	Typ	Max	Units
Input Voltage	T_A = -40°C to +85°C		1.7	-	5.5	V
Output Voltage Accuracy	T_A = -40°C to +85°C		V_{OUT}(T)* 0.99	V_{OUT}(T)	V_{OUT}(T)* 1.01	V
Line Regulation ($dV_{OUT}/dV_{IN}/V_{OUT}$)	V_{IN} = ($V_{OUT-Nom}$ +1.0V) to 5.5V, I_{OUT} = 1.0mA		-	0.02	0.1	%/V
Load Regulation	V_{IN} = V_{OUT} +1V, I_{OUT} = 1mA to 500mA, LCON="L"		-	1	20	mV
	V_{IN} = V_{OUT} +1V, I_{OUT} = 1mA to 1A, LCON="H"		-	1	40	
Quiescent Current (Note 6)	I_{OUT} = 0mA		-	110	170	µA
$I_{STANDBY}$	V_{EN} = 0V (Disabled)		-	0.5	3	µA
Output Current Limit	V_{IN} = V_{OUT} +1V	LCON="L"	500	-	-	mA
		LCON="H"	1	-	-	A
Foldback Short Current (Note 7)	V_{OUT} Short to Ground, LCON="L"		-	60	-	mA
	V_{OUT} Short to Ground, LCON="H"		-	110	-	
PSRR (Note 8)	V_{IN} = (V_{OUT}+1V) V_{DC} + 0.2Vp-pAC, I_{OUT} = 30mA, $V_{OUT} \geq$ 1.8V	f = 1kHz	-	80	-	dB
	V_{IN} = (V_{OUT}+1V) V_{DC} + 0.2Vp-pAC, I_{OUT} = 30mA, $V_{OUT} \leq$ 1.8V		-	75	-	
Output Noise Voltage (Note 8) (Note 9)	BW = 10Hz to 100kHz, I_{OUT} = 100mA		-	16	-	µVrms
Dropout Voltage (Note 5)	I_{OUT} = 500mA	0.9V ≤ V_{OUT} ≤ 1.5V	-	(Note 10)	(Note 10)	V
		V_{OUT}=1.8V	-	0.095	0.135	
		V_{OUT}=2.5V	-	0.074	0.105	
		V_{OUT}=2.8V	-	0.07	0.1	
		V_{OUT}=2.85V	-	0.07	0.1	
		V_{OUT}=3.0V	-	0.066	0.095	
		V_{OUT}=3.3V	-	0.06	0.09	
	I_{OUT} = 1A	0.9V ≤ V_{OUT} ≤ 1.2V	-	(Note 10)	(Note 10)	
		V_{OUT}=1.5V	-	0.24	0.33	
		V_{OUT}=1.8V	-	0.2	0.28	
		V_{OUT}=2.5V	-	0.155	0.22	
		V_{OUT}=2.8V	-	0.145	0.205	
		V_{OUT}=2.85V	-	0.145	0.205	
		V_{OUT}=3.0V	-	0.14	0.2	
		V_{OUT}=3.3V	-	0.125	0.19	
Output Voltage Temperature Coefficient	I_{OUT} = 30mA, T_A = -40°C to +85°C		-	±30	-	ppm/°C
Thermal Shutdown Threshold (TSHDN)	-		-	+150	-	°C
Thermal Shutdown Hysteresis (THYS)	-		-	+20	-	°C
EN Input Low Voltage	-		0	-	0.4	V
EN Input High Voltage	-		1	-	5.5	V
EN Input Leakage	V_{EN} = 0, V_{IN} = 5.0V or V_{EN} = 5.0V, V_{IN} = 0V		-	-	0.6	µA
LCON Pull-down Current	-		-	0.3	-	µA
LCON Input Low Voltage	-		-	-	0.4	V
LCON Input High Voltage	-		1	-	-	V
Inrush Current Limit	-	LCON="L"	-	300	-	mA
		LCON="H"	-	500	-	
On Resistance of N-Channel for Auto-Discharge	V_{IN} = 4.0V, V_{EN} = 0V (Disabled)		-	30	-	Ω

Notes:
5. Dropout voltage is the voltage difference between the input and the output at which the output voltage drops 2% below its nominal value.
6. Quiescent current is defined here is the difference in current between the input and the output.
7. Short circuit current is measured with V_{OUT} pulled to GND.
8. This specification is guaranteed by design.
9. To make sure lowest environment noise minimizes the influence on noise measurement.
10. Input voltage should be equal or more than the minimum operating voltage

Electrical Characteristics (@T_A = +25°C, V_{DD}= V_{OUT} +1.0V, C_{IN} = C_{OUT} = 1.0µF, I_{OUT} = 1.0mA, unless otherwise specified.) (cont.)

Parameter	Condition	Min	Typ	Max	Units
Thermal Resistance Junction to Ambient (θ_{JA}) (Note 4)	X1-DFN1612-8 (Type B)	-	55.5	-	°C/W
Thermal Resistance Junction to Case (θ_{JC}) (Note 4)	X1-DFN1612-8 (Type B)	-	15.4	-	

Notes:
5. Dropout voltage is the voltage difference between the input and the output at which the output voltage drops 2% below its nominal value.
6. Quiescent current is defined here is the difference in current between the input and the output.
7. Short circuit current is measured with V_{OUT} pulled to GND.
8. This specification is guaranteed by design.
9. To make sure lowest environment noise minimizes the influence on noise measurement.
10. Input voltage should be equal or more than the minimum operating voltage

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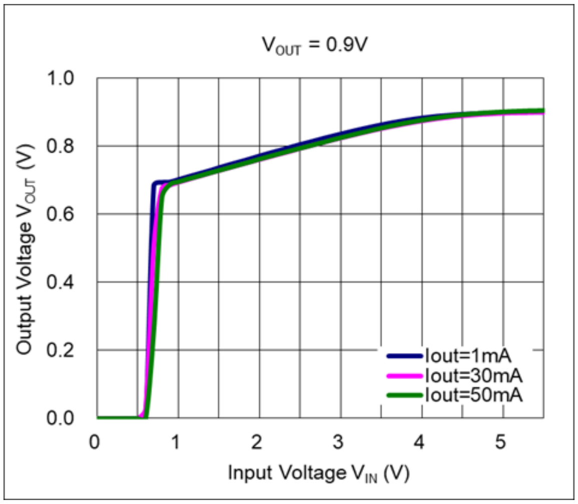
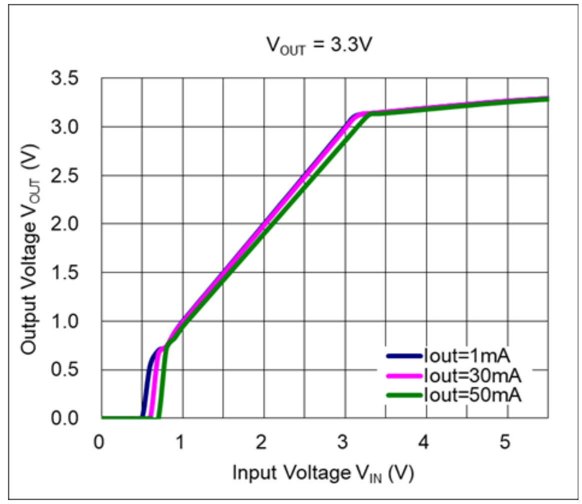
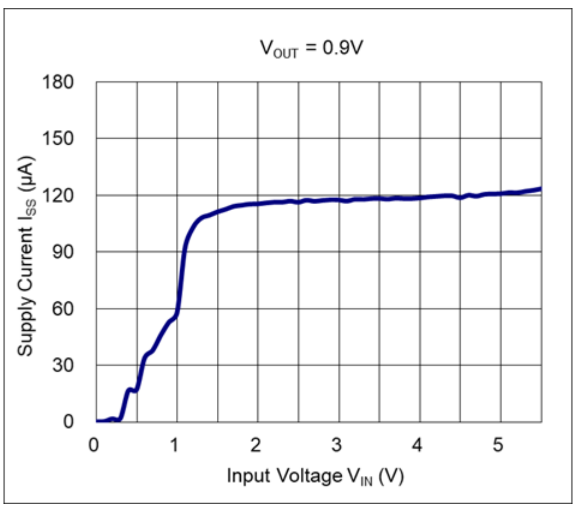
Typical Performance Characteristics

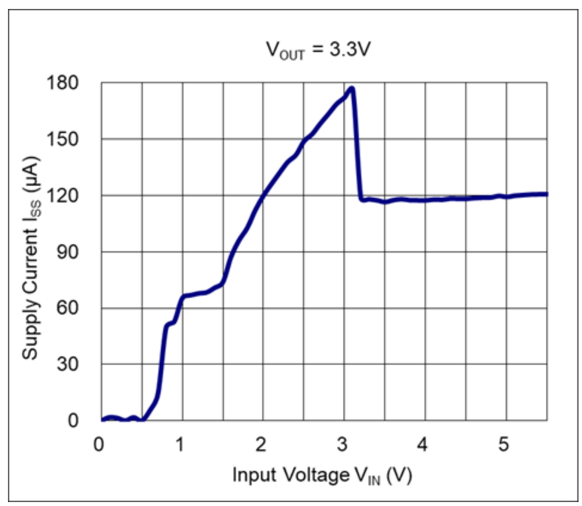
1) Output Voltage vs. Input Voltage (C_{IN} = Ceramic 1.0 μF, C_{OUT} = Ceramic 1.0 μF, Ta = 25°C)



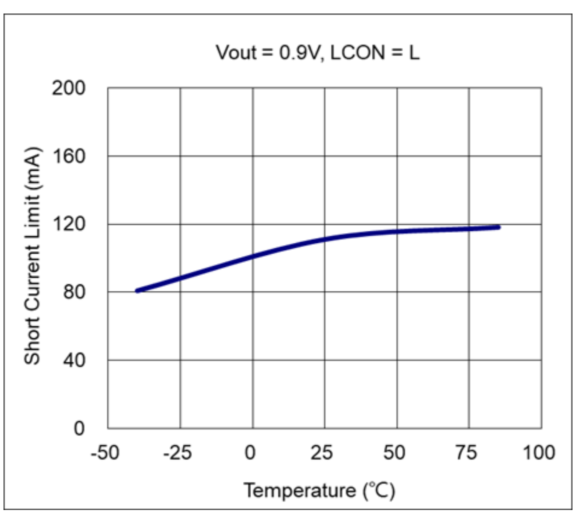


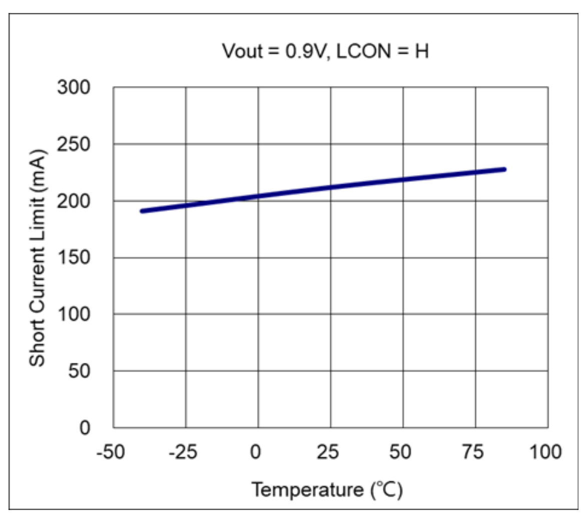
2) Supply Current vs. Input Voltage (C_{IN} = Ceramic 1.0 μF, C_{OUT} = Ceramic 1.0 μF, Ta = 25°C)





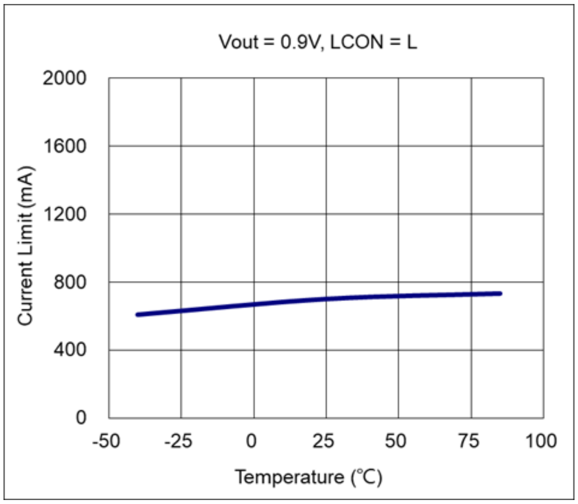
3) Short Current Limit vs. Temperature

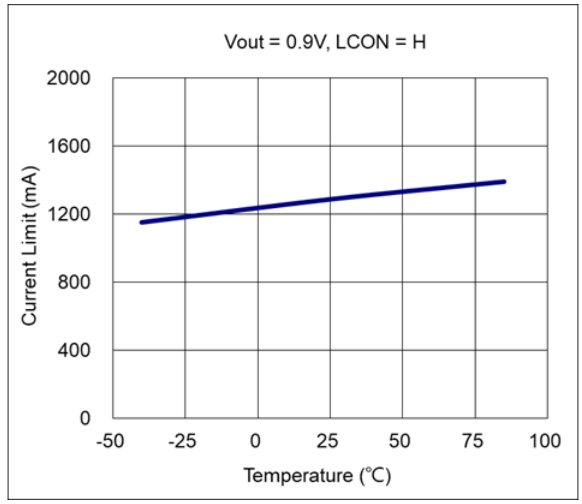




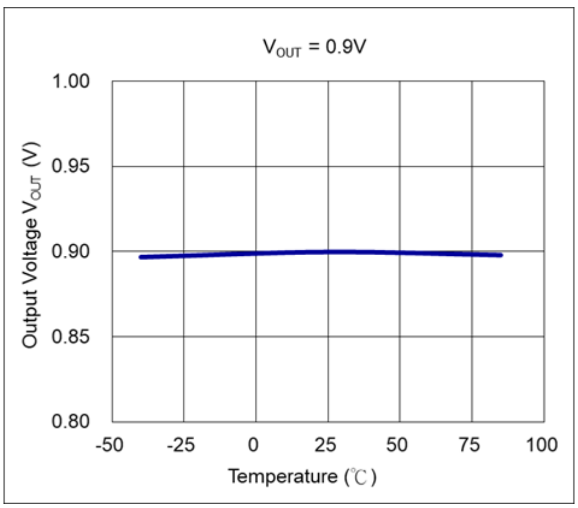
Typical Performance Characteristics (continued)

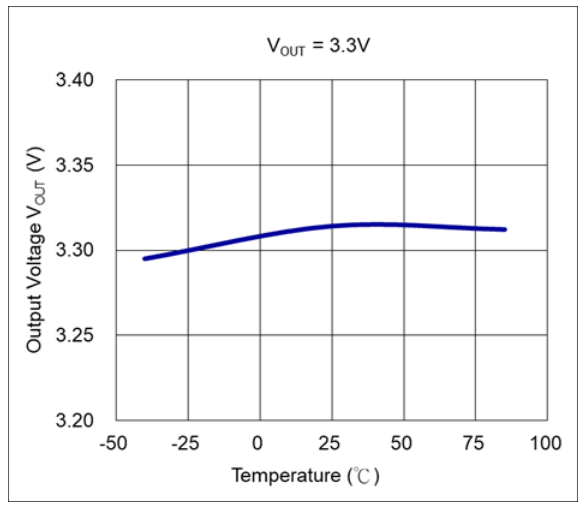
4) Peak Current Limit vs. Temperature (C$_{IN}$ = Ceramic 1.0 µF, C$_{OUT}$ = Ceramic 1.0 µF, V$_{IN}$=V$_{OUT}$+1V)



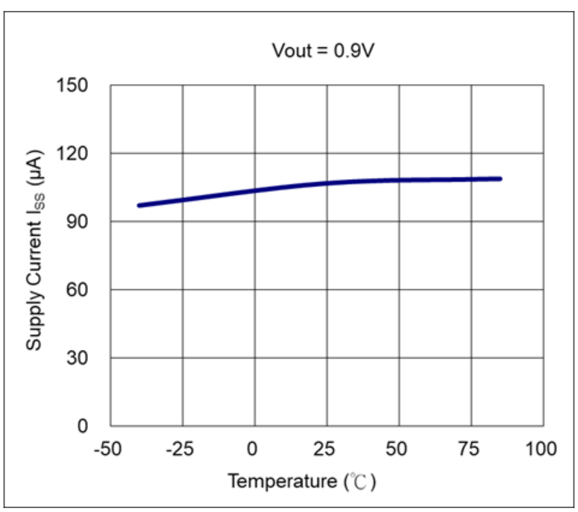


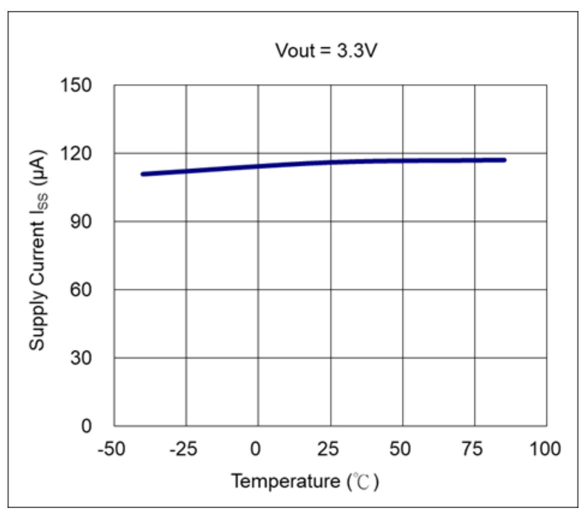
5) Output Voltage vs. Temperature (C$_{IN}$ = Ceramic 1.0 µF, C$_{OUT}$ = Ceramic 1.0 µF, V$_{IN}$=V$_{OUT}$+1V, I$_{OUT}$ = 1 mA)





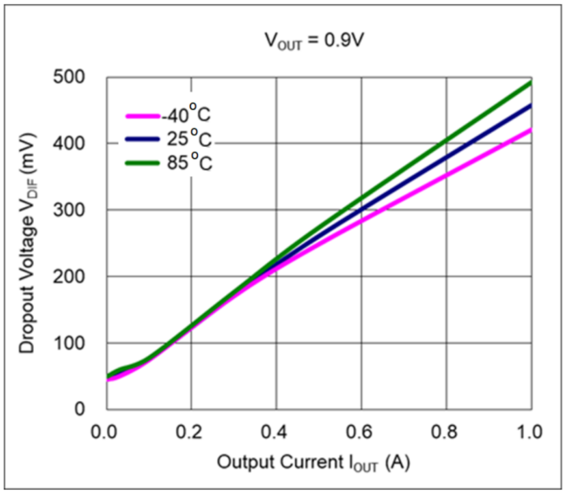
6) Supply Current vs. Temperature (C$_{IN}$ = Ceramic 1.0 µF, C$_{OUT}$ = Ceramic 1.0 µF, V$_{IN}$=V$_{OUT}$+1V, I$_{OUT}$ = 0 mA)

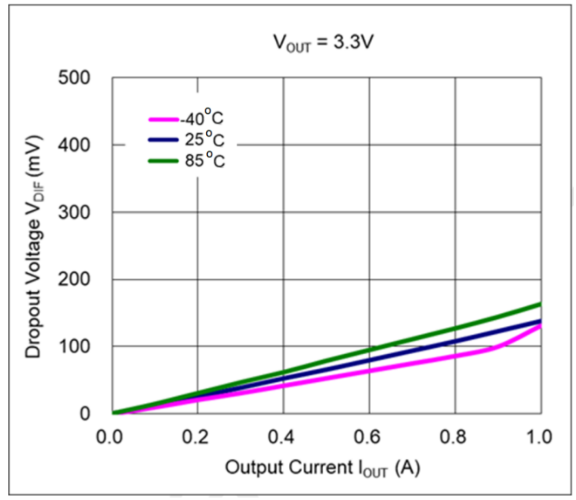




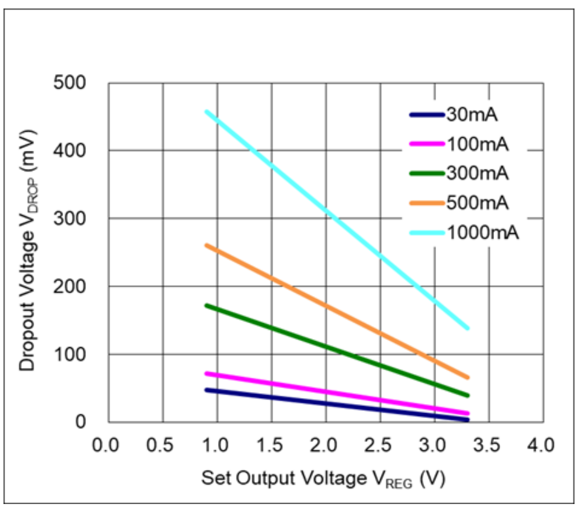
Typical Performance Characteristics (continued)

7) Dropout Voltage vs. Output Current (C_{IN} = Ceramic 1.0 µF, C_{OUT} = Ceramic 1.0 µF, V_{IN}=V_{OUT}+1V)

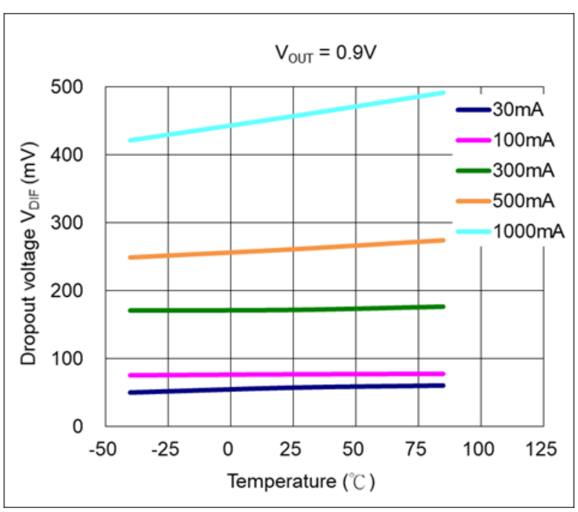


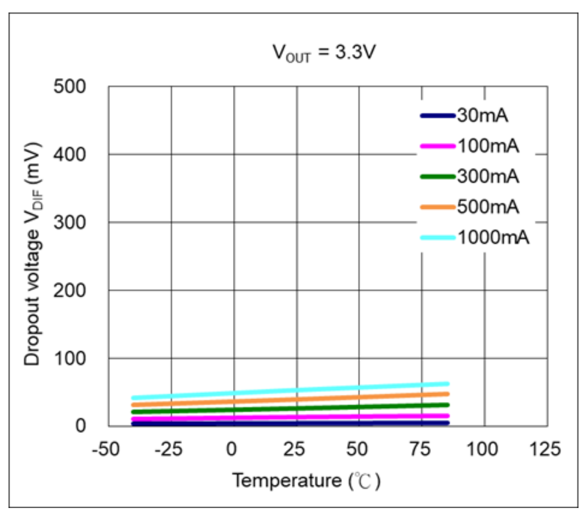


8) Dropout Voltage vs. Set Output Voltage (C_{IN} = Ceramic 1.0 µF, C_{OUT} = Ceramic 1.0 µF, Ta = 25°C)



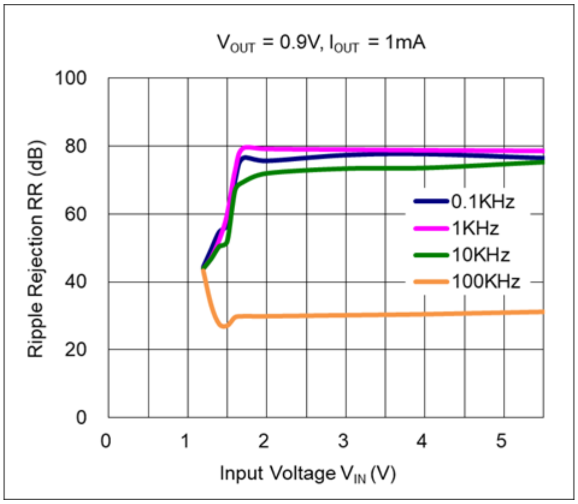
9) Dropout Voltage vs. Temperature (C_{IN} = Ceramic 1.0 µF, C_{OUT} = Ceramic 1.0 µF)

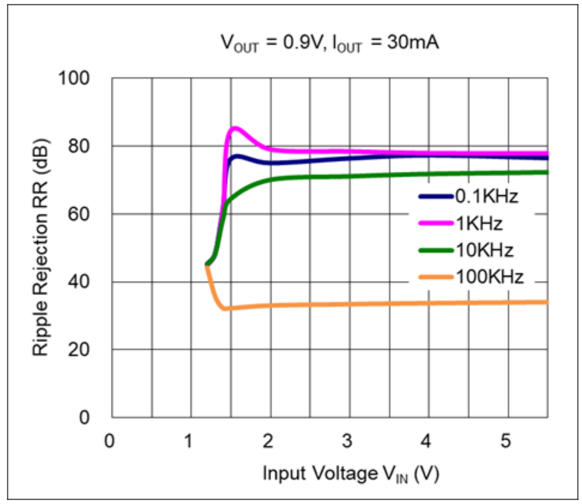


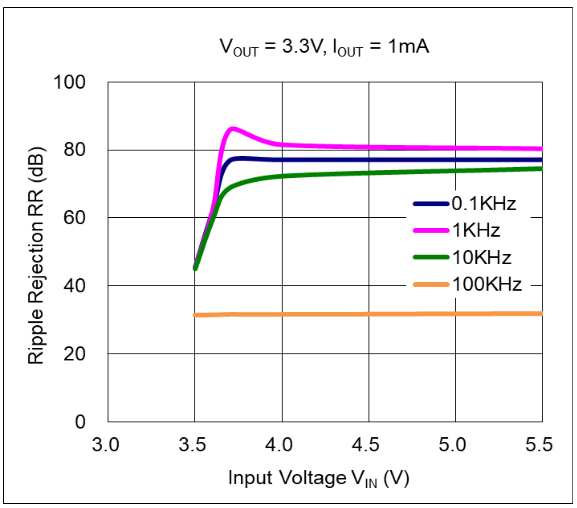


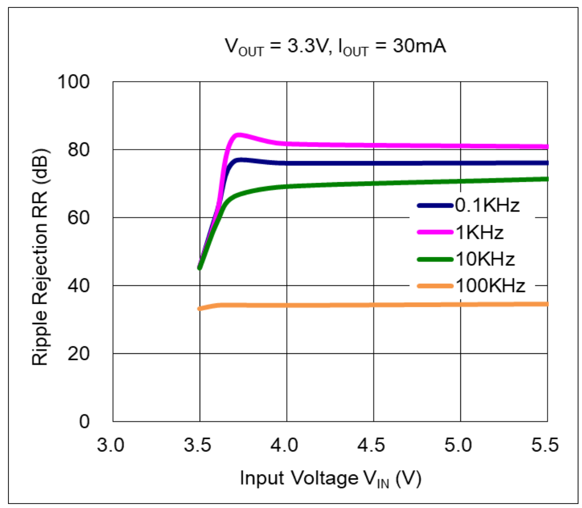
Typical Performance Characteristics (continued)

10) Ripple Rejection vs. Input Voltage (C_{IN} = none, C_{OUT} = Ceramic 1.0 μF, Ripple = 0.2 Vp-p, Ta = 25°C)

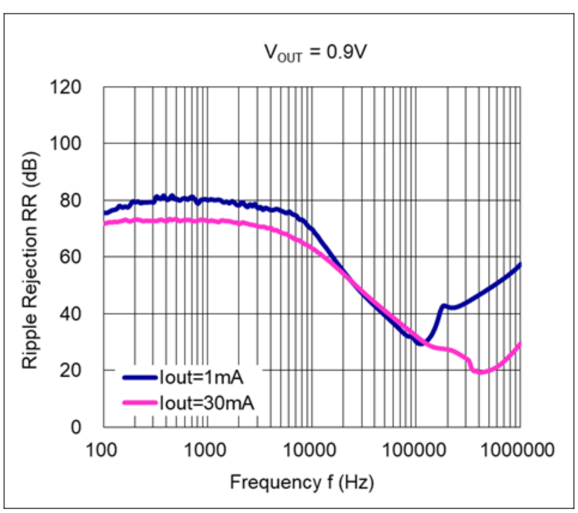


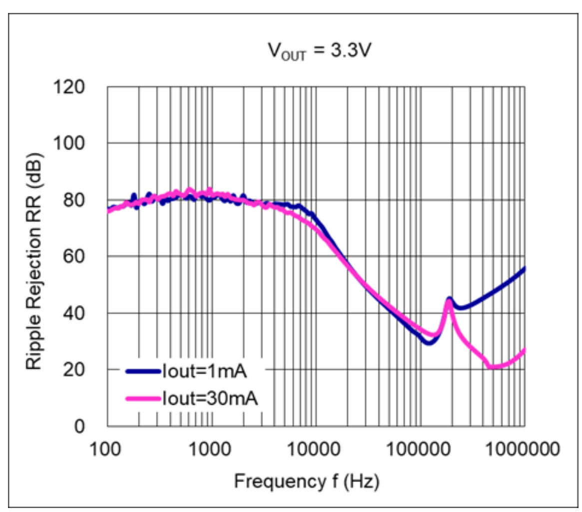






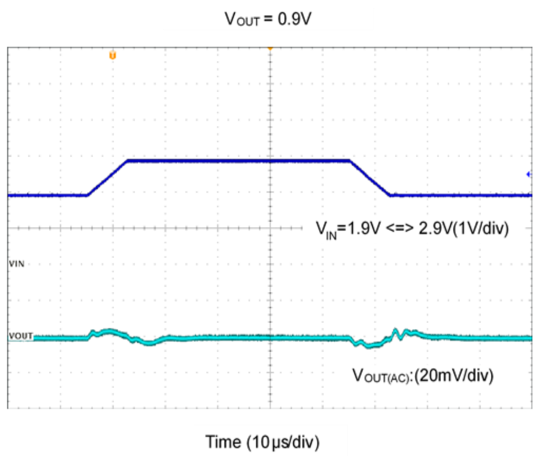
11) Ripple Rejection vs. Frequency (C_{IN} = none, C_{OUT} = Ceramic 1.0 μF, Ripple = 0.2 Vp-p, Ta = 25°C)

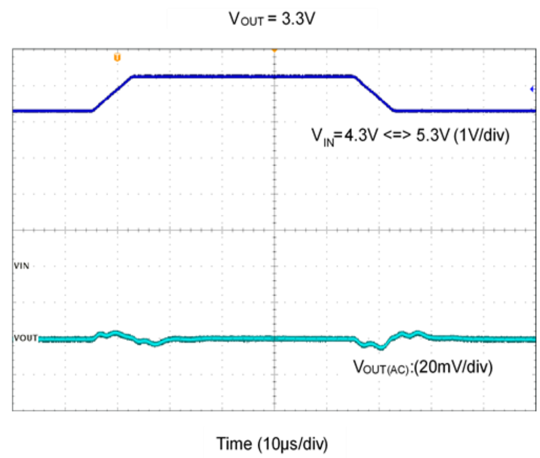




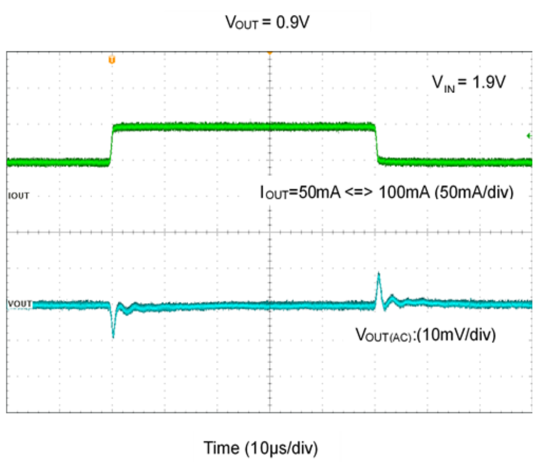
Typical Performance Characteristics (continued)

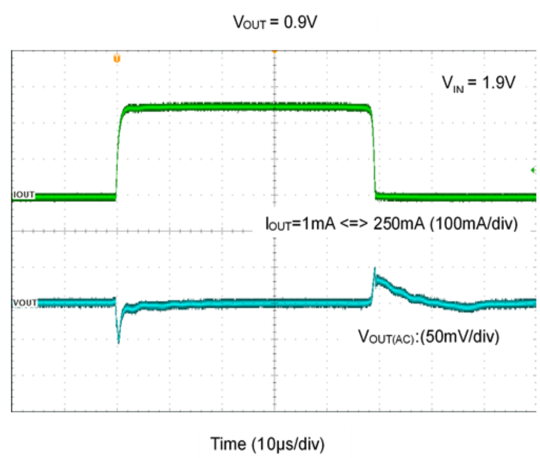
12) Line Transient Response C_{IN} = none, C_{OUT} = Ceramic 1.0 µF, I_{OUT} = 30 mA, tr = tf = 5 µs, Ta = 25°C)

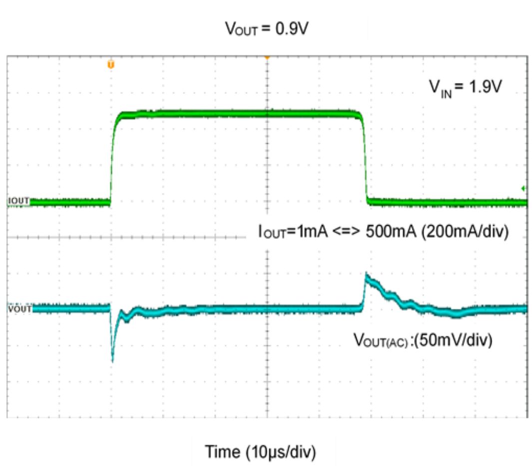


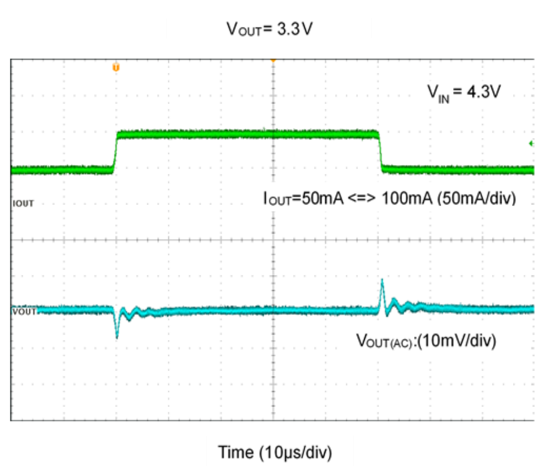


13) Load Transient Response (C_{IN} = Ceramic 1.0 µF, C_{OUT} = Ceramic 1.0 µF, tr = tf = 0.5 µs, Ta = 25°C)



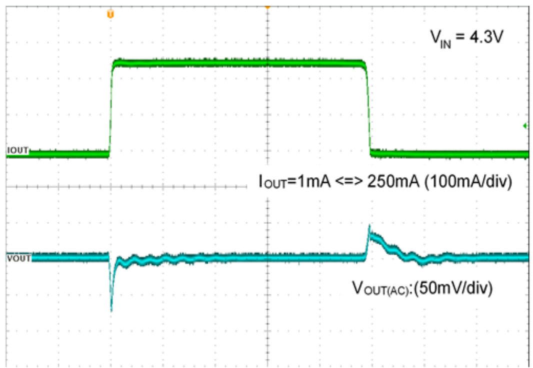






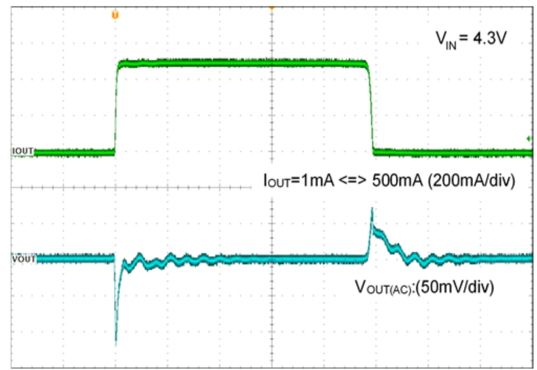
Typical Performance Characteristics

V_{OUT} = 3.3V



Time (10µs/div)

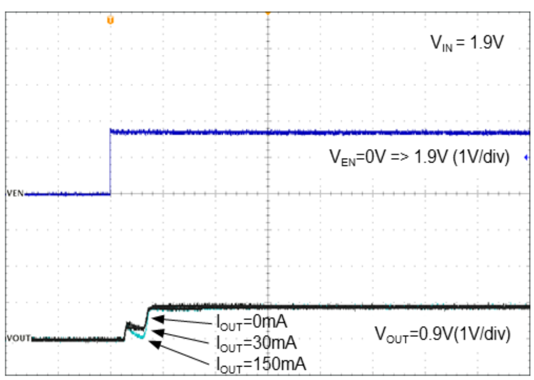
V_{OUT} = 3.3V



Time (10µs/div)

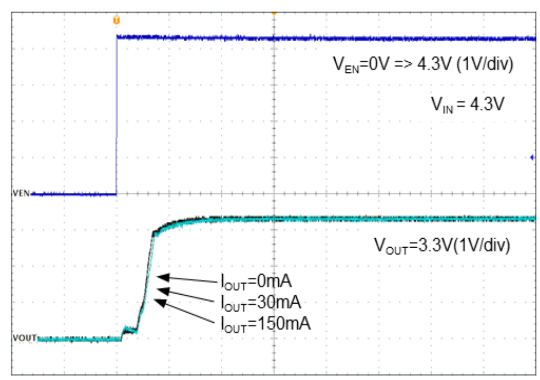
14) Turn-on Waveform by EN Pin Signal (C_{IN} = Ceramic 1.0 µF, C_{OUT} = Ceramic 1.0 µF, Ta = 25°C)

V_{OUT} = 0.9V



Time (40µs/div)

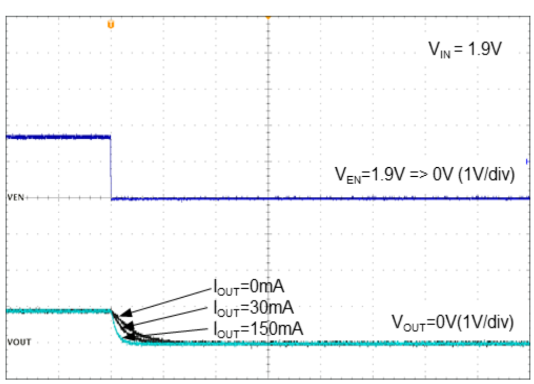
V_{OUT} = 3.3V



Time (40µs/div)

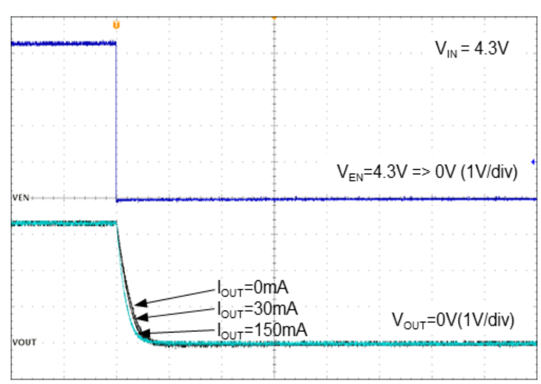
15) Turn-off Waveform by EN Pin Signal (C_{IN} = Ceramic 1.0 µF, C_{OUT} = Ceramic 1.0 µF, Ta = 25°C)

V_{OUT} = 0.9V



Time (40µs/div)

V_{OUT} = 3.3V

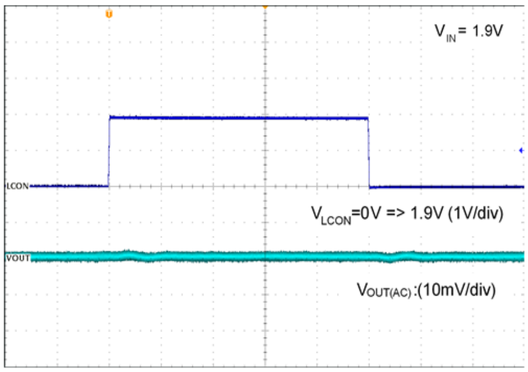


Time (40µs/div)

Typical Performance Characteristics

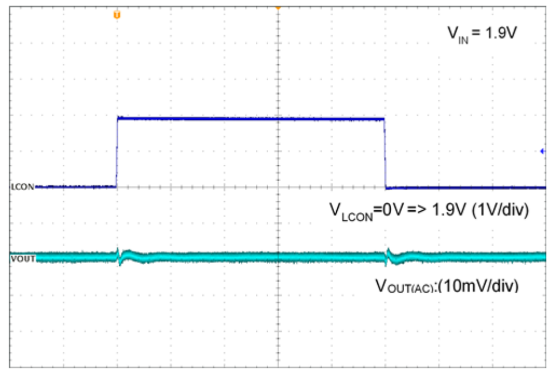
16) LCON Pin Transient Response (C_{IN} = Ceramic 1.0 µF, C_{OUT} = Ceramic 1.0 µF, Ta = 25°C)

V_{OUT} = 0.9V, I_{OUT} = 1mA



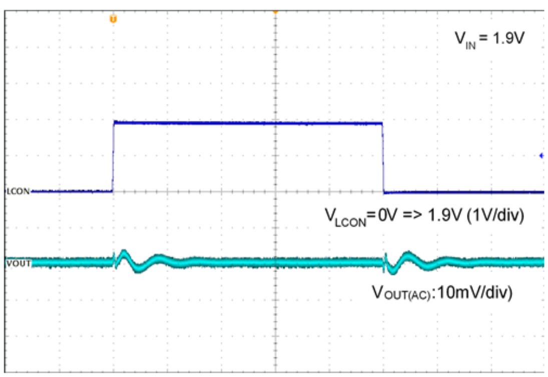
Time (10µs/div)

V_{OUT} = 0.9V, I_{OUT} = 150mA



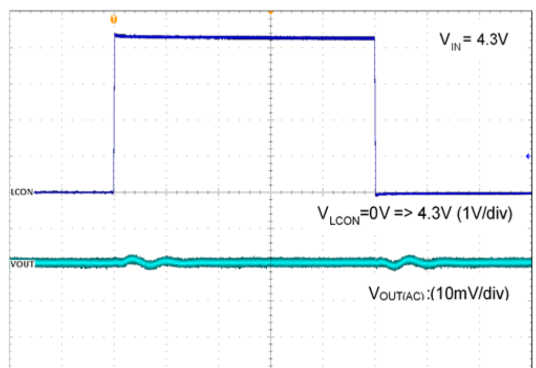
Time (10µs/div)

V_{OUT} = 0.9V, I_{OUT} = 500mA



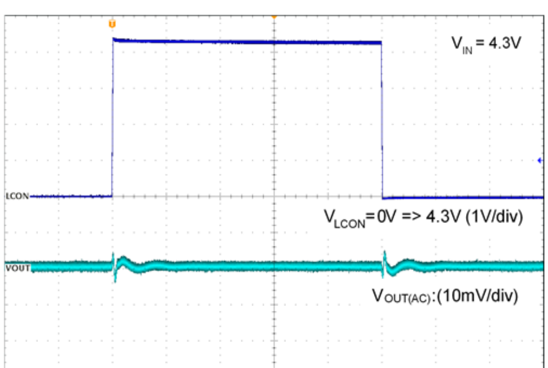
Time (10µs/div)

V_{OUT} = 3.3V, I_{OUT} = 1mA



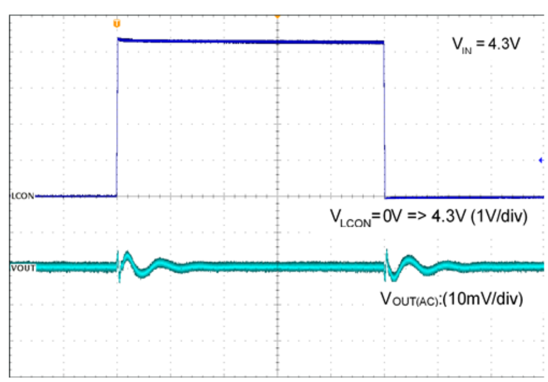
Time (10µs/div)

V_{OUT} = 3.3V, I_{OUT} = 150mA



Time (10µs/div)

V_{OUT} = 3.3V, I_{OUT} = 500mA



Time (10µs/div)

Application Information

Overview

The AP7368 is a low dropout regulator featuring 500mA/1A and provides high output voltage accuracy, low $R_{DS(on)}$, high PSRR, low output noise, and low quiescent current. The AP7368 is well suited for low power handheld communication equipment.

Output Capacitor

An output capacitor (C_{OUT}) is needed to improve transient response and maintain stability. The AP7368 is stable with very small ceramic output capacitors. The ESR (equivalent series resistance) and capacitance drives the selection. If the application has large load variations, it is recommended to utilize low-ESR bulk capacitors. It is recommended to place ceramic capacitors as close as possible to the load and the ground pin and care should be taken to reduce the impedance in the layout.

Input Capacitor

To prevent the input voltage from dropping during load steps, it is recommended to utilize an input capacitor (C_{IN}). A minimum 1µF ceramic capacitor is recommended between V_{IN} and GND pins to decouple input power supply glitch. This input capacitor must be located as close as possible to the device to assure input stability and reduce noise. For PCB layout, a wide copper trace is required for both V_{IN} and GND pins.

Enable Control

The AP7368 is turned on by setting the EN pin high, and is turned off by pulling it low. If this feature is not used, the EN pin should be tied to V_{IN} pin to keep the regulator output on at all times. To ensure proper operation, the signal source used to drive the EN pin must be able to swing above and below the specified turn-on/off voltage thresholds listed in the Electrical Characteristics section.

LCON Pin

The AP7368 can select the output current limit 1A or 500mA by alternating the LCON pin between “H” or “L”.

LCON = “L” ··500mA
LCON = “H” ··1A

Short Circuit Protection

When V_{OUT} pin is short-circuit to GND, short circuit protection will be triggered and clamp the output current to approximately 60mA. This feature protects the regulator from overcurrent and damage due to overheating.

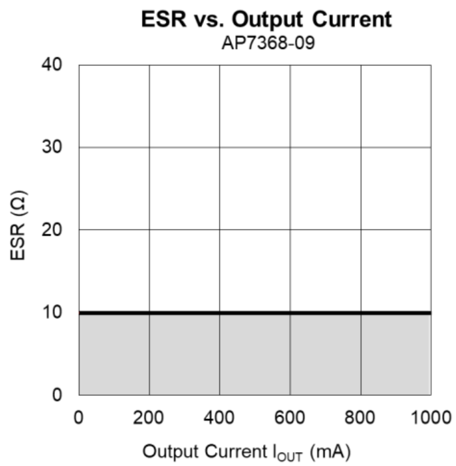
Layout Considerations

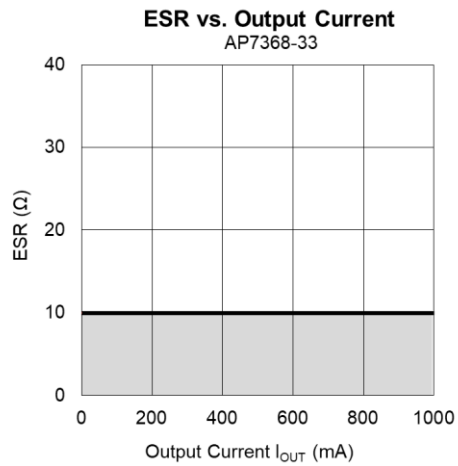
For good ground loop and stability, the input and output capacitors should be located close to the input, output, and ground pins of the device. The regulator ground pin should be connected to the external circuit ground to reduce voltage drop caused by trace impedance. Ground plane is generally used to reduce trace impedance. Wide trace should be used for large current paths from V_{IN} to V_{OUT}, and load circuit.

ESR vs. Output Current

Ceramic type output capacitor is recommended for this AP7368; however, the other output capacitors with low ESR also can be used. The relations between I_{OUT} (Output Current) and ESR of an output capacitor are shown below. The stable region is marked as the hatched area in the graph.

Measurement conditions: Frequency Band: 10Hz to 2MHz, Temperature: −40°C to +85°C.





Ordering Information (Note 11)

AP7368X - XXX XXX - 7

Output Discharge	Output Voltage	Package	Packing
D : Output Discharge	09 : 0.9V 10 : 1.0V 105 : 1.05V 11 : 1.1V 12 : 1.2V 15 : 1.5V 18 : 1.8V 25 : 2.5V 28 : 2.8V 285 : 2.85V 30 : 3.0V 33 : 3.3V	RS4 : X1-DFN1612-8	7 : Tape & Reel

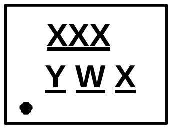
Part Number	Package Code	Package	7" Tape and Reel	
			Quantity	Part Number Suffix
AP7368D-XXRS4-7	RS4	X1-DFN1612-8 (Type B)	5,000/Tape & Reel	-7

Note: 11. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

(1) X1-DFN1612-8 (Type B)

(Top View)

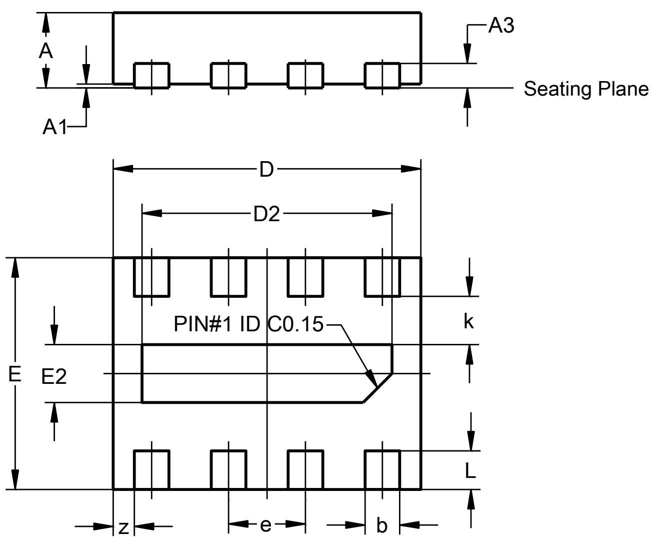


XXX : Identification Code
Y : Year : 0~9
W : Week : A~Z : 1~26 week;
a~z : 27~52 week; z represents
52 and 53 week
X : Internal code

Part Number	Package	Identification Code
AP7368D-09RS4-7	X1-DFN1612-8 (Type B)	H9A
AP7368D-10RS4-7	X1-DFN1612-8 (Type B)	H9B
AP7368D-105RS4-7	X1-DFN1612-8 (Type B)	H9R
AP7368D-11RS4-7	X1-DFN1612-8 (Type B)	H9N
AP7368D-12RS4-7	X1-DFN1612-8 (Type B)	H9C
AP7368D-15RS4-7	X1-DFN1612-8 (Type B)	H9D
AP7368D-18RS4-7	X1-DFN1612-8 (Type B)	H9E
AP7368D-25RS4-7	X1-DFN1612-8 (Type B)	H9F
AP7368D-28RS4-7	X1-DFN1612-8 (Type B)	H9G
AP7368D-285RS4-7	X1-DFN1612-8 (Type B)	H9P
AP7368D-30RS4-7	X1-DFN1612-8 (Type B)	H9H
AP7368D-33RS4-7	X1-DFN1612-8 (Type B)	H9J

Package Outline Dimensions

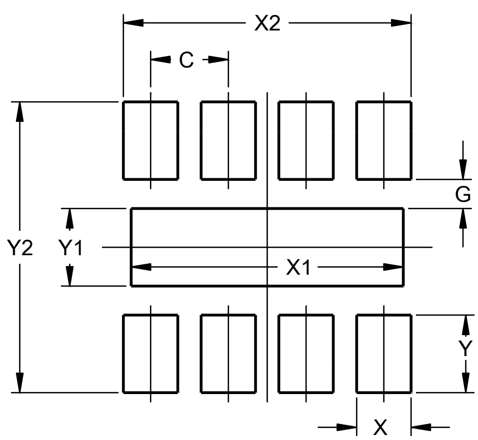
Please see http://www.diodes.com/package-outlines.html for the latest version.



X1-DFN1612-8 (Type B)			
Dim	**Min**	**Max**	**Typ**
A	0.36	0.43	0.39
A1	0.00	0.05	0.02
A3	--	--	0.127
b	0.13	0.23	0.18
D	1.55	1.65	1.60
D2	1.20	1.40	1.30
E	1.15	1.25	1.20
E2	0.20	0.40	0.30
e	--	--	0.40
k	--	--	0.25
L	0.15	0.25	0.20
z	--	--	0.11
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
C	0.400
G	0.150
X	0.280
X1	1.400
X2	1.480
Y	0.400
Y1	0.400
Y2	1.500

Mechanical Data

- Moisture Sensitivity: Level 1 Per J-STD-020
- Terminals: Finish - NiPdAu over Copper Leads, Solderable per JESD22-B102 Test Method 208 (e4)
- Weight: 0.003grams (Approximate)

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