

## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$ $T_A = +25^\circ C$
100V	6.0Ω @ $V_{GS} = 10V$	0.17A

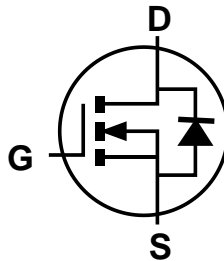
## Description and Applications

These N-Channel enhancement mode field effect transistors are produced using DIODES proprietary, high density, uses advanced trench technology. These products have been designed to minimize on-state resistance while provide rugged, reliable, and fast switching performance. These products are particularly suited for low voltage, low current applications such as:

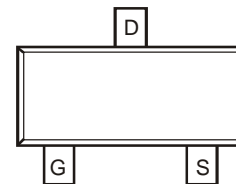
- Small Servo Motor Control
- Power MOSFET Gate Drivers
- Switching Applications



Top View



Equivalent Circuit



Top View

## Features and Benefits

- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- High Drain-Source Voltage Rating
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The BSS123Q is suitable for automotive applications requiring specific change control; these parts are AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

## Mechanical Data

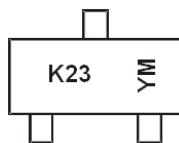
- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (e3)
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)

## Ordering Information (Note 4)

Part Number	Qualification	Case	Packaging
BSS123-7-F	Commercial	SOT23	3,000 / Tape & Reel
BSS123Q-13	Automotive	SOT23	10,000 / Tape & Reel
BSS123Q-7	Automotive	SOT23	3,000 / Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



K23 = Product Type Marking Code  
 YM = Date Code Marking  
 Y or  $\bar{Y}$  = Year (ex: C = 2015)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	T	U	V	W	X	Y	Z	A	B	C	D	E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	100	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	Continuous	I <sub>D</sub>	170
	Pulsed	I <sub>DM</sub>	680

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 5)	R <sub>θJA</sub>	417	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 6)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	100	-	-	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	0.1	μA	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V
		-	-	30	μA	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V @ T <sub>A</sub> = +150°C (Note 7)
		-	-	10	nA	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V
Gate-Source Leakage, Forward	I <sub>GSSF</sub>	-	-	50	nA	V <sub>GS</sub> = 20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 6)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.8	1.4	2.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 1mA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	-	6.0	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.17A
		-	-	10		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 0.17A
Forward Transfer Admittance	g <sub>FS</sub>	80	370	-	ms	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.17A, f = 1.0KHz
Diode Forward Voltage	V <sub>SD</sub>	-	0.84	1.3	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 0.34A
<b>DYNAMIC CHARACTERISTICS (Note 7)</b>						
Input Capacitance	C <sub>ISS</sub>	-	22	60	pF	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>OSS</sub>	-	3.5	15		
Reverse Transfer Capacitance	C <sub>RSS</sub>	-	2.0	6		
<b>SWITCHING CHARACTERISTICS (Note 7)</b>						
Turn-On Delay Time	t <sub>D(ON)</sub>	-	-	8	ns	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 30V, I <sub>D</sub> = 0.28A, R <sub>GEN</sub> = 50Ω
Turn-On Rise Time	t <sub>R</sub>	-	-	8		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	-	-	13		
Turn-Off Fall Time	t <sub>F</sub>	-	-	16		

- Notes:
- Part mounted on FR-4 board with recommended pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

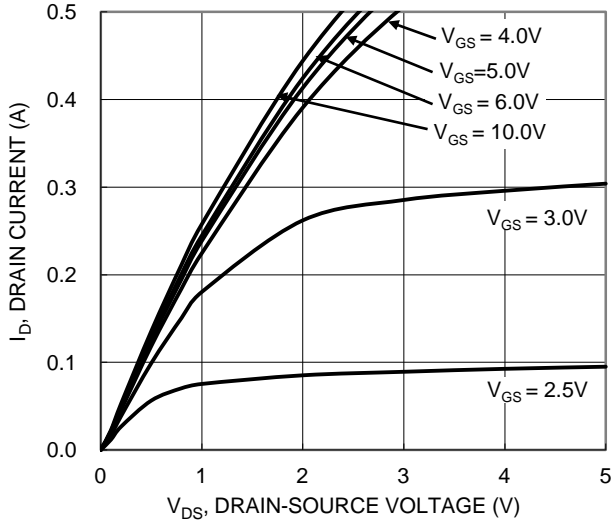


Figure 1. Typical Output Characteristic

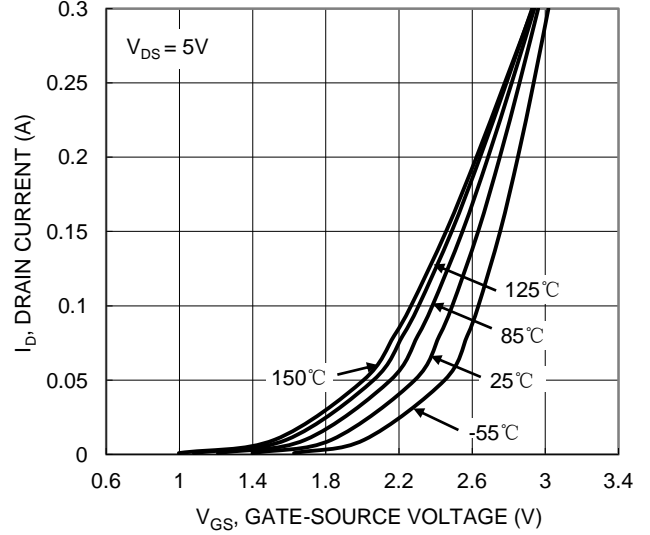


Figure 2. Typical Transfer Characteristic

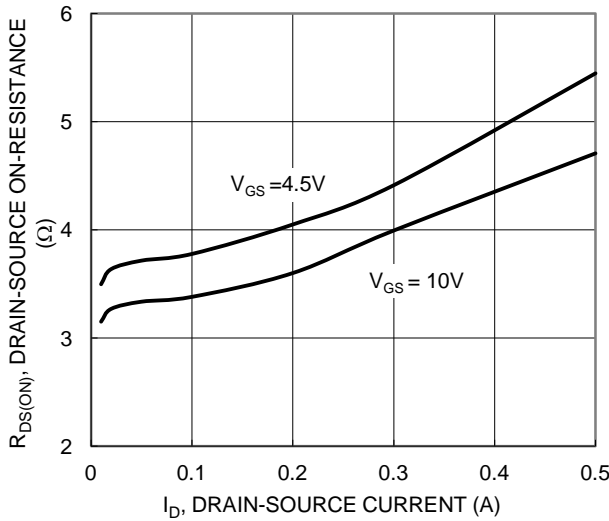


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

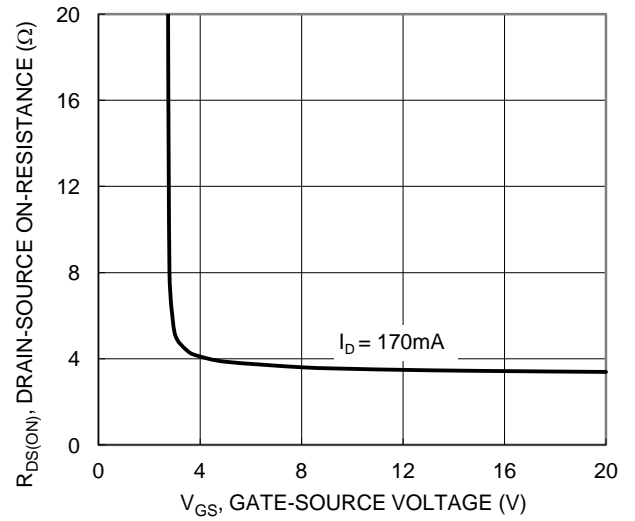


Figure 4. Typical Transfer Characteristic

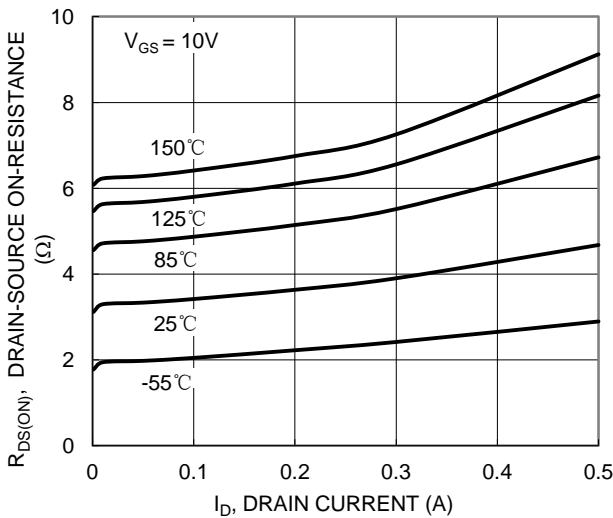


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

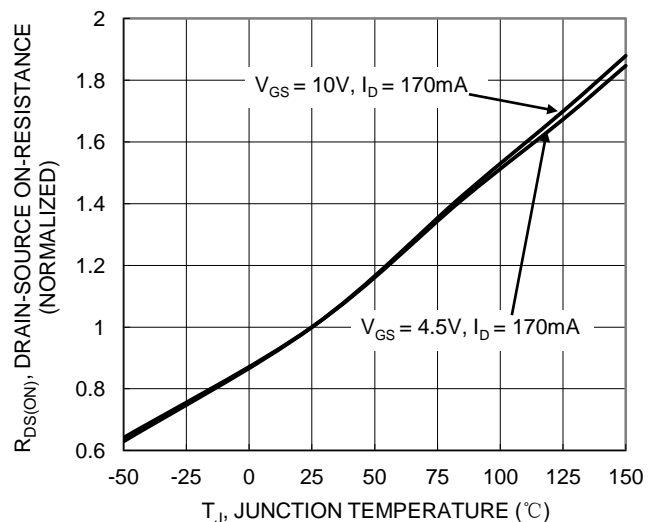


Figure 6. On-Resistance Variation with Junction Temperature

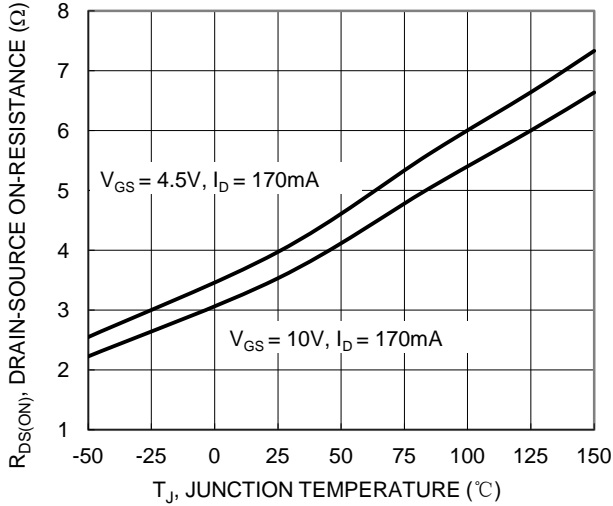


Figure 7. On-Resistance Variation with Junction Temperature

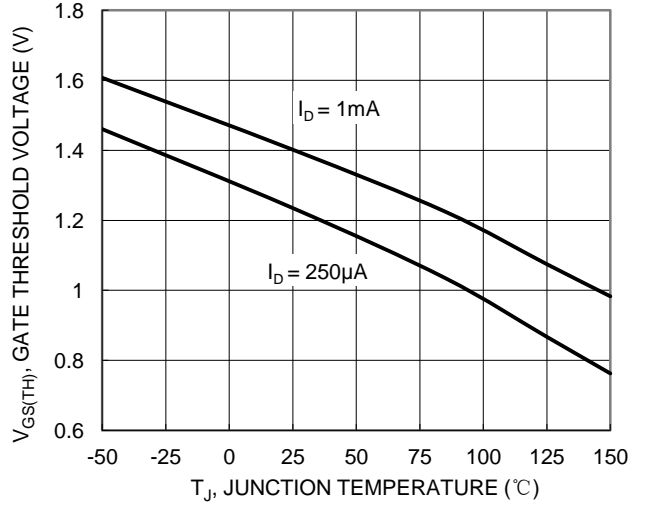


Figure 8. Gate Threshold Variation vs. Junction Temperature

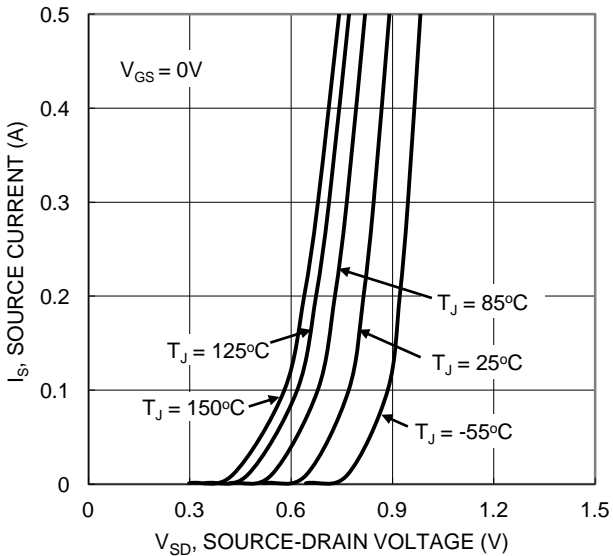


Figure 9. Diode Forward Voltage vs. Current

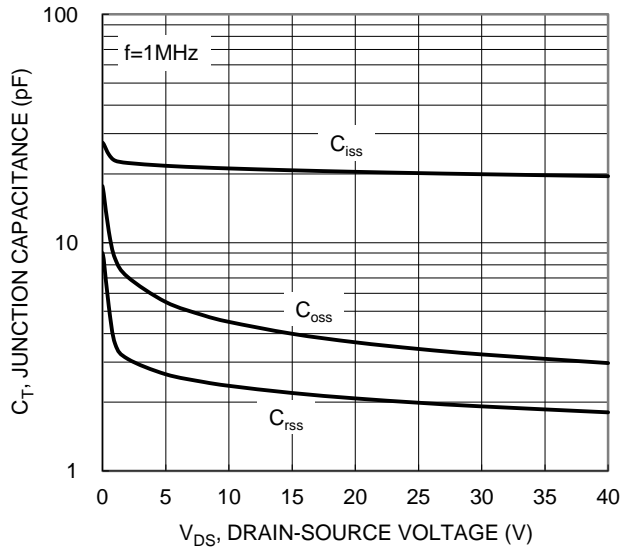


Figure 10. Typical Junction Capacitance

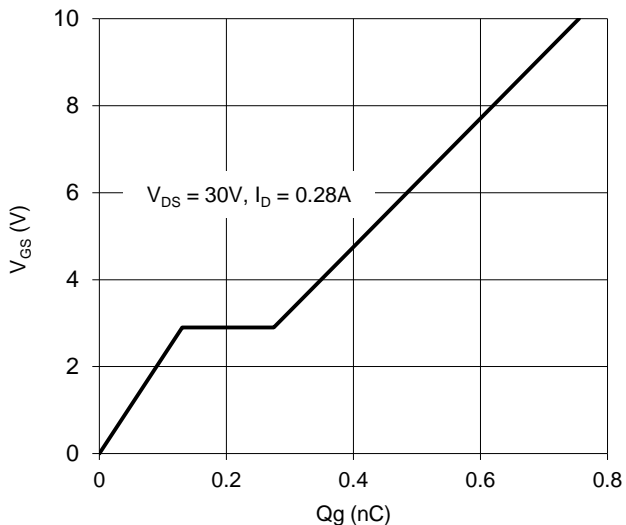


Figure 11. Gate Charge

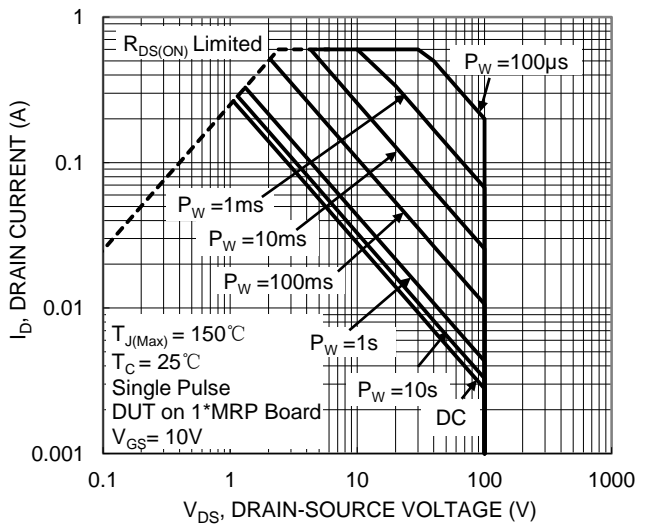


Figure 12. SOA, Safe Operation Area

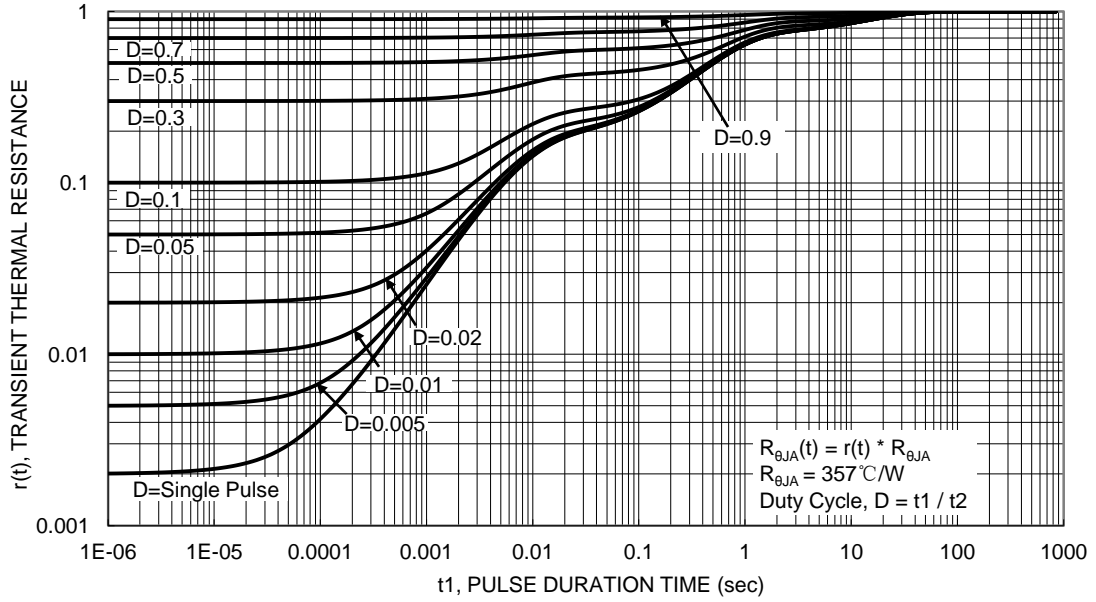
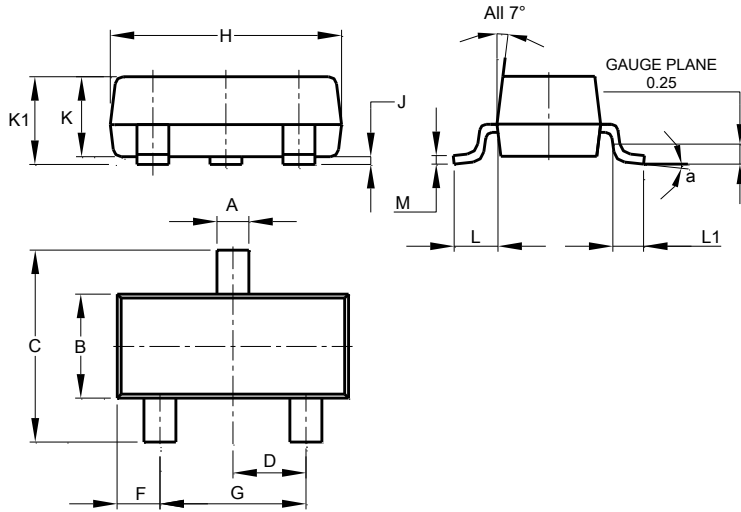


Figure 13. Transient Thermal Resistance

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT23**

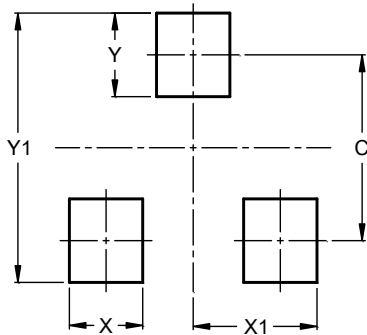


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT23**



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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