

#### 4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

#### **Product Summary**

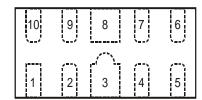
V <sub>RWM (Max)</sub>	I <sub>PP (Max)</sub>	C <sub>T (Typ)</sub>
60V	2A	10pF

#### **Description**

The D60V0L4B10LP is a high performance device suitable for protecting four high speed I/Os. These devices are assembled in U-DFN2510-10 package. They have high ESD surge capability and low capacitance.

### **Applications**

 Typically Used at Chip-On-Glass (COG) Panels, VBus Protection, LCD Televisions, Set Top Box



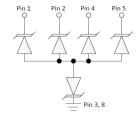
Pin Configuration (Top View)

## **Features**

- IEC 61000-4-2 (ESD):±8kV (Contact)
- IEC 61000-4-2 (ESD):±8kV (Air)
- 4 Channel of ESD Protection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Mechanical Data**

- Case: U-DFN2510-10
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe (Lead Free Plating)
   Solderable per MIL-STD-202, Method 208 4
- Weight: 0.038 grams (Approximate)



Schematic Diagram

# **Ordering Information** (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D60V0L4B10LP-7	Standard	QD4	7	8	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**

QD4 YM

QD4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: E = 2017) M = Month (ex: 9 = September)

Date Code Key

Year	20	13	20	014	20	15	20	16	20	17	20	18
Code	,	4		В	(	0		)		E	F	-
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Codo	1	2	2	1	E	6	7	0	0	$\sim$	NI	ח



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
ESD Contact Discharge	V <sub>ESD</sub>	8	kV	Standard IEC 61000-4-2
Peak Pulse Current	IPP	2	Α	Standard IEC 61000-4-5,8/20µs
Operating Temperature Range	T <sub>OP</sub>	-40 to +125	°C	_
Storage Temperature Range	T <sub>STG</sub>	-65 to +150	°C	_

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	$P_{D}$	350	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	$R_{ hetaJA}$	360	°C/W

## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

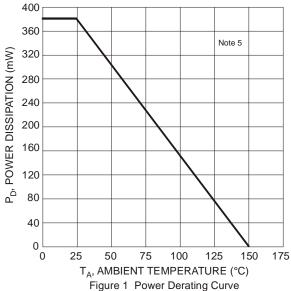
Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Standoff Voltage	V <sub>RWM</sub>	_	_	60	V	_
Channel Leakage Current (Note 6)	I <sub>RM</sub>	_	_	100	nA	V <sub>RWM</sub> = 60V
Clamping Voltage, Positive Transients	V <sub>CL</sub>	_	115	125	V	$I_{PP} = 2A$ , $tp = 8/20\mu s$
Breakdown Voltage	$V_{BR}$	65	75	85	V	I <sub>R</sub> = 1mA
Channel Input Capacitance	C <sub>T</sub>	_	10	12	pF	V <sub>R</sub> = 0V, f = 1MHz

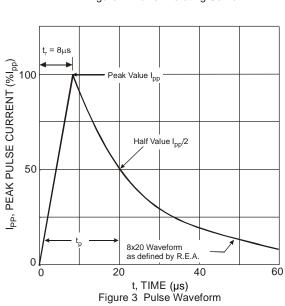
Notes:

<sup>5.</sup> Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.

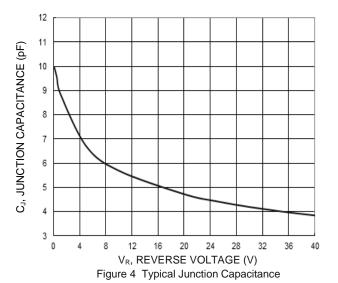
<sup>6.</sup> Short duration pulse test used to minimize self-heating effect.







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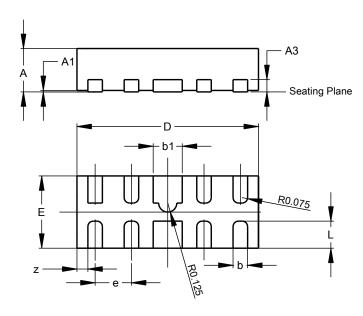




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN2510-10

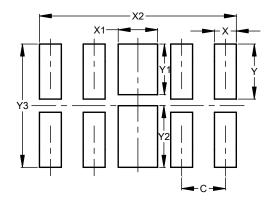


U-DFN2510-10							
Dim	Min	Max	Тур				
Α	0.545	0.605	0.575				
A1	0.00	0.05	0.03				
A3	-	-	0.13				
b	0.15	0.25	0.20				
b1	0.35	0.45	0.40				
D	2.450	2.575	2.500				
е	-	-	0.50				
Е	0.950	1.075	1.000				
L	0.325	0.425	0.375				
Z	-	-	0.150				
All Dimensions in mm							

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN2510-10



Dimensions	Value (in mm)
С	0.500
Х	0.250
X1	0.450
X2	2.250
Y	0.625
Y1	0.575
Y2	0.700
Y3	1 400



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P6KE39CA-TP P6KE8.2A SA110CA SA60CA SA64CA SMBJ12CATR SMBJ33CATR SMBJ8.0A SMLJ30CA-TP ESD112-B1-02EL
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VESD12A1A-HD1-GS08 CPDUR5V0R-HF CPDUR24V-HF CPDQC5V0U-HF CPDQC5V0USP-HF CPDQC5V0-HF D1213A-01LP4-7B
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