Land-free Groen

## Product Summary

| $\mathrm{V}_{\text {BR }}$ (Min) | Ipp (Max) | (Typ) |
| :---: | :---: | :---: |
| ${ }^{7.5 \mathrm{~V}}$ | 24A | 150pF |

## Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

## Applications

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral


## Features

- Low Profile Package ( 0.53 mm Max) and Ultra-Small PCB Footprint Area ( 1.08 mm * 0.68 mm Max) Suitable for Compact Portable Electronics
- Provides ESD Protection per IEC 61000-4-2 Standard: Air $\pm 30 \mathrm{kV}$, Contact $\pm 30 \mathrm{kV}$
- 1 Channel of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free \& Fully RoHS Compliant (Notes 1 \& 2)
- Halogen and Antimony Free. "Green" Device (Note 3)


## Mechanical Data

- Case: X1-DFN1006-2
- Case Material: Molded Plastic, "Green" Molding Compound.

UL Flammability Classification Rating 94V-0

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (44)
- Weight: 0.001 grams (Approximate)

> X1-DFN1006-2


Bottom View


Device Schematic

## Ordering Information (Note 4)

| Product | Compliance | Marking | Reel Size (inches) | Tape Width (mm) | Quantity Per Reel |
| :---: | :---: | :---: | :---: | :---: | :---: |
| D7VOH1U2LP-7B | Standard | P4 | 7 | 8 | $10,000 /$ Tape \& Reel |

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) \& 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain $<900 \mathrm{ppm}$ bromine, $<900 \mathrm{ppm}$ chlorine ( $<1500 \mathrm{ppm}$ total $\mathrm{Br}+\mathrm{Cl}$ ) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## Marking Information



P4 = Product Type Marking Code
Bar Denotes Pin 1 or Cathode Side

Maximum Ratings $\left(@ T_{A}=+25^{\circ} \mathrm{C}\right.$, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | Conditions |
| :--- | :---: | :---: | :---: | :---: |
| Peak Pulse Power Dissipation | $\mathrm{P}_{\mathrm{PP}}$ | 270 | W | $8 / 20 \mu \mathrm{~s}$, See Figure 3 |
| Peak Pulse Current | IPP $^{2}$ | 24 | A | $8 / 20 \mu \mathrm{~s}$, See Figure 3 |
| ESD Protection - Contact Discharge | V ESD_CONTACT | $\pm 30$ | kV | IEC 61000-4-2 Standard |
| ESD Protection - Air Discharge | V $_{\text {ESD_AIR }}$ | $\pm 30$ | kV | IEC 61000-4-2 Standard |

## Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Package Power Dissipation (Note 5) | $\mathrm{P}_{\mathrm{D}}$ | 250 | mW |
| Thermal Resistance, Junction to Ambient (Note 5) | $\mathrm{R}_{\theta \mathrm{JA}}$ | 500 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating and Storage Temperature Range | $\mathrm{T}_{\mathrm{J}}, \mathrm{T}_{\mathrm{STG}}$ | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics (@T $\mathrm{A}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Conditions |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse Working Voltage | $\mathrm{V}_{\mathrm{RWM}}$ | - | - | 7.0 | V | - |
| Reverse Current (Note 6) | $\mathrm{I}_{\mathrm{R}}$ | - | 0.1 | 1.0 | $\mu \mathrm{~A}$ | $\mathrm{~V}_{\mathrm{R}}=\mathrm{V}_{\mathrm{RWM}}=7.0 \mathrm{~V}$ |
| Reverse Breakdown Voltage | $\mathrm{V}_{\mathrm{BR}}$ | 7.5 | - | 9.5 | V | $\mathrm{I}_{\mathrm{R}}=1 \mathrm{~mA}$ |
| Reverse Clamping Voltage | $\mathrm{V}_{\mathrm{CL}}$ | - | - | 10.6 | V | $\mathrm{I}_{\mathrm{PP}}=5 \mathrm{~A}, \mathrm{t}_{\mathrm{p}}=8 / 20 \mu \mathrm{~s}$ |
|  |  | - | - | 14.4 |  | $\mathrm{I}_{\mathrm{PP}}=24 \mathrm{~A}, \mathrm{t}_{\mathrm{p}}=8 / 20 \mu \mathrm{~s}$ |
| Capacitance | $\mathrm{C}_{\mathrm{T}}$ | - | 150 | - | pF | $\mathrm{V}_{\mathrm{R}}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |

[^0]

Figure 3 Typical $8 \times 20 \mu$ S Pulse Waveform


Figure 2 Pulse Derating Curve


Figure 4 Typical Total Capacitance vs. Reverse Voltage

## Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

## X1-DFN1006-2



| X1-DFN1006-2 |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Dim | Min | Max | Typ |  |
| A | 0.47 | 0.53 | 0.50 |  |
| A1 | 0 | 0.05 | 0.03 |  |
| b | 0.45 | 0.55 | 0.50 |  |
| D | 0.95 | 1.075 | 1.00 |  |
| E | 0.55 | 0.675 | 0.60 |  |
| e | - | - | 0.40 |  |
| L | 0.20 | 0.30 | 0.25 |  |
| R | 0.05 | 0.15 | 0.10 |  |
| All Dimensions in mm |  |  |  |  |

## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.
X1-DFN1006-2


| Dimensions | Value (in mm) |
| :---: | :---: |
| $\mathbf{C}$ | 0.70 |
| $\mathbf{G}$ | 0.30 |
| $\mathbf{X}$ | 0.40 |
| $\mathbf{X 1}$ | 1.10 |
| $\mathbf{Y}$ | 0.70 |

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[^0]:    Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes' suggested pad layout, per http://www.diodes.com/package-outlines.html.
    6 . Short duration pulse test used to minimize self-heating effect.

