



1.5A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

Product Summary (@T_A = +25°C)

V _{RRM} (V)	I ₀ (A)	V _F (V)	Ι _R (μΑ)
1,000	1.5	1.0	5

Features and Benefits

- Glass Passivated Die Construction
- Miniature Package Saves Space on PC Boards
- Low Leakage Current
- Ideal for SMT Manufacturing
- Low Forward Voltage Drop
- Surge Overload Rating to 70A Peak
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description and Applications

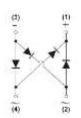
Suitable for AC to DC bridge full wave rectification for SMPS, LED lighting, adapter, battery charger, home appliances, office equipment, and telecommunication applications.

Mechanical Data

- Case: DBF
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Lead Free Plating (Matte Tin Finish). Solderable per MIL-STD-202, Method 208 (2)
- Polarity: As Marked on Body
- Weight: 0.214 grams (Approximate)



Top View



Internal Schematic

Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DBF1510U-13	Commercial	DBF	3,000/Tape & Reel

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

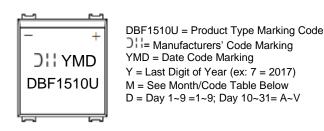
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	1000	V
RMS Reverse Voltage	V _{R(RMS)}	700	V
Average Rectified Output Current (Note 5) @ T _C = +110°C	lo	1.5	Α
Non-Repetitive Peak Forward Surge Current, 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	70	А
I ² t Rating for Fusing (1ms < t < 8.3ms)	l ² t	20.34	A ² S

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance, Junction to Ambient (Note 6) (Per Element)	R _{0JA}	50	°C/W
Typical Thermal Resistance, Junction to Case (Per Element)	$R_{\theta JC}$	10	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

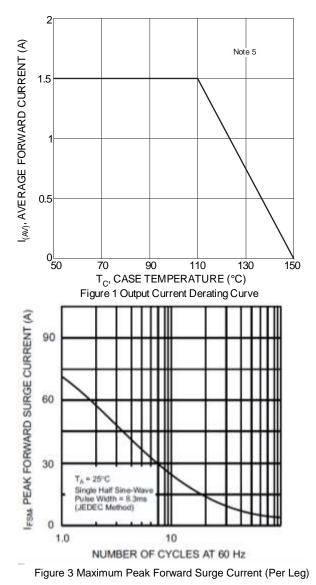
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

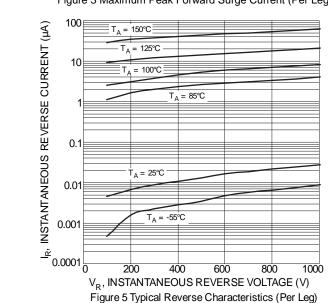
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	V _{(BR)R}	1,000	_	_	V	I _R = 5μA
Forward Voltage (Per Element)	VF		0.87 0.94	0.95 1.0	V	I _F = 0.75A, T _A = +25°C I _F = 1.5A, T _A = +25°C
Leakage Current (Note 7) (Per Element)	I _R		0.03 11	5 500	μA	V _R = 1,000V, T _A = +25°C V _R = 1,000V, T _A = +125°C
Total Capacitance (Per Element)	Ст		25	—	pF	$V_{R} = 4V, f = 1.0MHz$

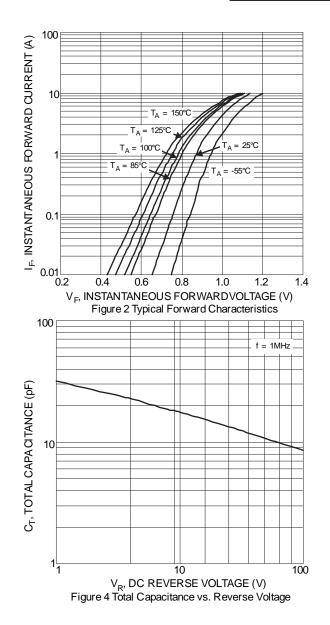
Notes:

Device mounted on glass epoxy PC board with 1.3mm² solder pad.
 Device mounted on glass epoxy substrate with 1oz/ft², 15mmx15mm copper pad per pin.
 Short duration pulse test used to minimize self-heating effect.











Тур

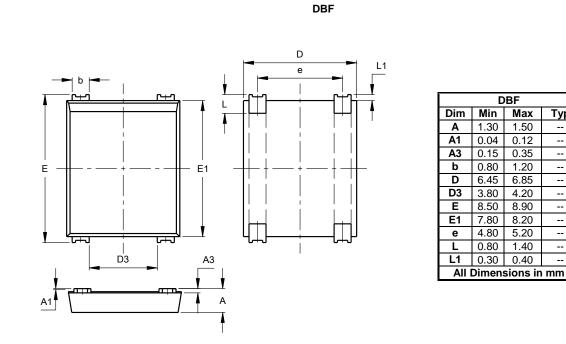
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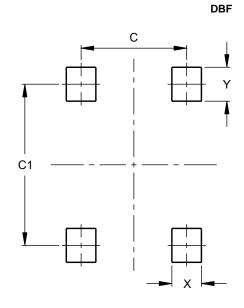
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suaa	ested	Pad	Layout
ougg	COLCU	I UU	Luyout

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Dimensions	Value				
Dimensions	(in mm)				
С	5.00				
C1	7.60				
Х	1.40				
Y	1.60				



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