

#### PNP PRE-BIASED SMALL SIGNAL DUAL SURFACE MOUNT TRANSISTOR

Case Material: Molded Plastic. UL Flammability Classification

Moisture Sensitivity: Level 1 per J-STD-020D

Marking Information: See Table and Page 5.

Terminals: Solderable per MIL-STD-202, Method 208 Also Available in Lead Free Plating (Matte Tin Finish annealed over Copper leadframe). Please see Ordering Information,

Terminal Connections: See Diagram

Ordering Information See Page 5 Weight: 0.015 grams (approximate)

**Mechanical Data** 

Case: SOT-26

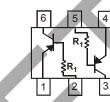
Rating 94V-0

Note 5, on Page 5

#### Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDC)
- Built-In Biasing Resistors
- Available in Lead Free/RoHS Compliant Version (Note 3)

Part Number	R1	R2	Marking
DDA124EK	22ΚΩ	<b>22K</b> Ω	P17
DDA144EK	47ΚΩ	47ΚΩ	P20
DDA114YK	10KΩ	47ΚΩ	P14
DDA123JK	<b>2.2K</b> Ω	47ΚΩ	P06
DDA114EK	10KΩ	10KΩ	P13
DDA143TK	4.7KΩ	-	P07
DDA114TK	10KΩ	-	P12



Top View

## R1, R2 Device Schematic R1 only Device Schematic

#### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit V	
Supply Voltage, (1) to (6) and (4) to (3)		Vcc	50		
Input Voltage, (2) to (1) and (5) to (4)	DDA124EK DDA144EK DDA114YK DDA123JK DDA123JK DDA114EK DDA143TK DDA114TK	V <sub>IN</sub>	+10 to -40 +10 to -40 +6 to -40 +5 to -12 +10 to -40 +5V max +5V max	V	
Output Current	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK DDA143TK DDA114TK	lo	-30 -30 -70 -100 -50 -100 -100	mA	
Output Current	All	I <sub>C(MAX)</sub>	-100	mA	

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### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Total)	PD	300	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{ heta JA}$	416.7	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Notes:

Mounted on FR4 PC Board with recommended pad layout at http://www.diodes.com/datasheets/ap02001.pdf.
200mW per element must not be exceeded.

No purposefully added lead.



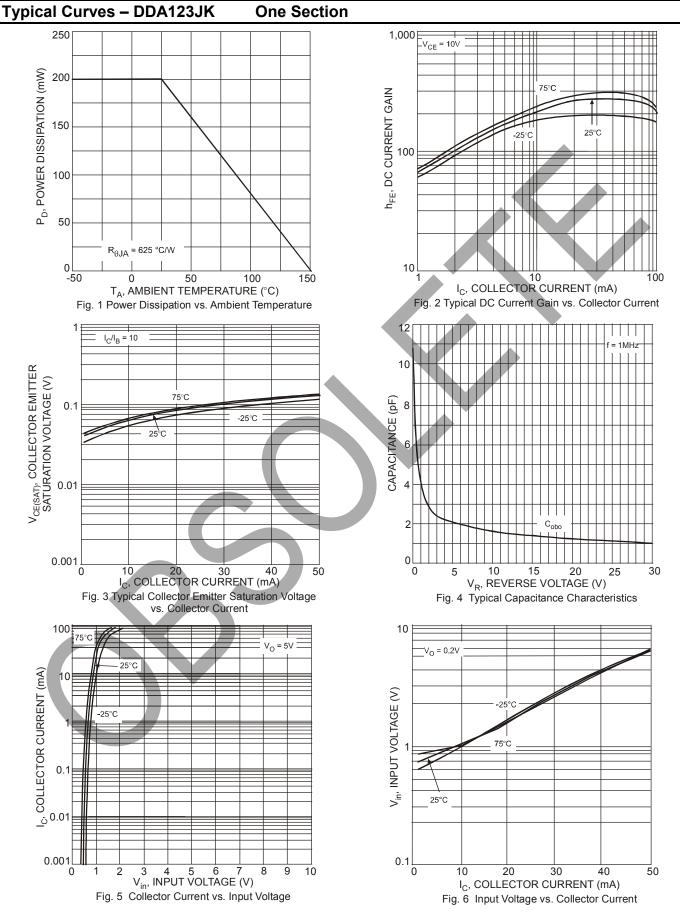
## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic (DDA143TK & DDA114TK only)	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-50	_	_	V	I <sub>C</sub> = -50μA
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	-50	_	_	V	I <sub>C</sub> = -1mA
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-5			V	I <sub>E</sub> = -50μA
Collector Cutoff Current	I <sub>CBO</sub>	—	_	-0.5	μA	V <sub>CB</sub> = -50V
Emitter Cutoff Current	I <sub>EBO</sub>		_	-0.5	μA	V <sub>EB</sub> = -4V
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	_	-0.3	V	I <sub>C</sub> /I <sub>B</sub> = -2.5mA / -0.25mA DDA143TK I <sub>C</sub> /I <sub>B</sub> = -1mA / -0.1mA DDA114TK
DC Current Transfer Ratio	h <sub>FE</sub>	100	250	600		I <sub>C</sub> = -1mA, V <sub>CE</sub> = -5V
Input Resistor (R1) Tolerance	$\Delta R_1$	-30	_	+30	%	_
Gain-Bandwidth Product*	f⊤	—	250	_	MHz	V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz

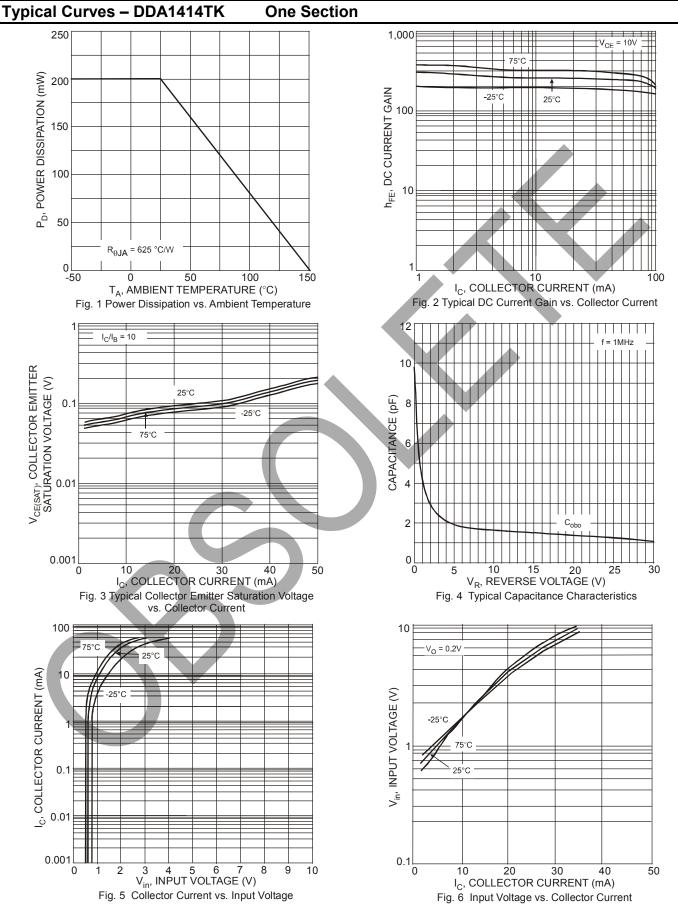
Characteris	tic	Symbol	Min	Тур	Max	Unit	Test Condition
	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	VI(OFF)	-0.5 -0.5 -0.3 -0.5 -0.5	-1.1 -1.1 — -1.1		V	V <sub>CC</sub> = -5V, I <sub>O</sub> = -100μA
Input Voltage	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	V <sub>I(ON)</sub>		-1.9 -1.9  -1.9	-3.0 -3.0 -1.4 -1.1 -3.0	V	$\begin{array}{l} V_{\rm O} = -0.3,  I_{\rm O} = -5mA \\ V_{\rm O} = -0.3,  I_{\rm O} = -2mA \\ V_{\rm O} = -0.3,  I_{\rm O} = -1mA \\ V_{\rm O} = -0.3,  I_{\rm O} = -5mA \\ V_{\rm O} = -0.3,  I_{\rm O} = -10mA \end{array}$
Output Voltage	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	V <sub>O(ON)</sub>		-0.1	-0.3	V	I <sub>O</sub> /I <sub>I</sub> = -10mA / -0.5mA I <sub>O</sub> /I <sub>I</sub> = -10mA / -0.5mA I <sub>O</sub> /I <sub>I</sub> = -5mA / -0.25mA I <sub>O</sub> /I <sub>I</sub> = -5mA / -0.25mA I <sub>O</sub> /I <sub>I</sub> = -10mA / -0.5mA
Input Current	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	I		_	-0.36 -0.18 -0.88 -3.6 -0.88	mA	V <sub>1</sub> = -5V
Output Current		I <sub>O(OFF)</sub>			-0.5	μA	$V_{CC} = 50V, V_{I} = 0V$
DC Current Gain	DDA124EK DDA144EK DDA114YK DDA123JK DDA114EK	Gı	56 68 80 30	_	_	_	$V_{O} = -5V, I_{O} = -5mA$ $V_{O} = -5V, I_{O} = -5mA$ $V_{O} = -5V, I_{O} = -10mA$ $V_{O} = -5V, I_{O} = -10mA$ $V_{O} = -5V, I_{O} = -5mA$
Input Resistor (R1) Tolerance	•	$\Delta R_1$	-30		+30	%	
Resistance Ratio Tolerance		R <sub>2</sub> /R <sub>1</sub>	-20		+20	%	
Gain-Bandwidth Product*		f <sub>T</sub>		250		MHz	V <sub>CE</sub> = -10V, I <sub>E</sub> = -5mA, f = 100MHz

\* Transistor - For Reference Only









DDA(XXXX)K Document number: DS30349 Rev. 6 - 4 4 of 6 www.diodes.com



#### Ordering Information (Notes 4 & 5)

Part Number	Case	Packaging
DDA124EK-7	SOT-26	3000/Tape & Reel
DDA144EK-7	SOT-26	3000/Tape & Reel
DDA114YK-7	SOT-26	3000/Tape & Reel
DDA123JK-7	SOT-26	3000/Tape & Reel
DDA114EK-7	SOT-26	3000/Tape & Reel
DDA143TK-7	SOT-26	3000/Tape & Reel
DDA114TK-7	SOT-26	3000/Tape & Reel

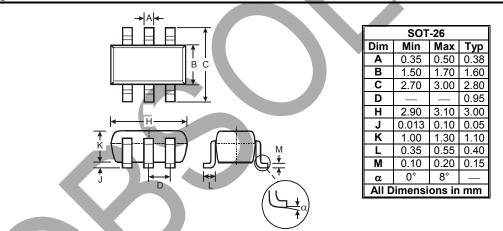
Notes:

For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.
For Lead Free/RoHS Compliant version part numbers, please add "-F" suffix to the part numbers above. Example: DDA114TK-7-F.

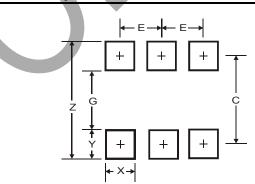
### **Marking Information**

Date Code Key				Рхх Ү ИА <sup>х)</sup>		YM = Da Y = Yea	ate Code N r (ex: T = 2		de (See F	⊃age 1)		
Year	2006	2007	20	08	2009	2010	2011	2012	2013	3	2014	2015
Code	Т	U	١	/	W	Х	Y	Z	Ă		В	С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul		Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

## Package Outline Dimensions



## Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
С	2.40
E	0.95



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