



NPN PRE-BIASED SMALL SIGNAL DUAL SURFACE MOUNT TRANSISTOR

Features

- **Epitaxial Planar Die Construction**
- Complementary PNP Types Available (DDA)
- **Built-In Biasing Resistors**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- **PPAP Capable (Note 4)**

Part Number	R1 (NOM)	R2 (NOM)
DDC124EU	22kΩ	22kΩ
DDC144EU	47kΩ	47kΩ
DDC114YU	10kΩ	47kΩ
DDC123JU	2.2kΩ	47kΩ
DDC114EU	10kΩ	10kΩ
DDC143ZU	4.7kΩ	47kΩ
DDC115EU	100kΩ	100kΩ

SOT363

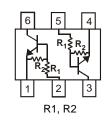


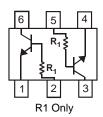
Top View

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (Approximate)

Part Number	R1 Only
DDC113TU	1kΩ
DDC143TU	4.7kΩ
DDC114TU	10kΩ





Device Schematic

Ordering Information (Notes 4, 5 & 6)

Product	Status	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DDC124EU-7-F	Active	AEC-Q101	N17	7	8	3,000
DDC124EUQ-7-F	NRND (Use ADC124EUQ)	Automotive	N17	7	8	3,000
DDC144EU-7-F	Active	AEC-Q101	N20	7	8	3,000
DDC114YU-7-F	Active	AEC-Q101	N14	7	8	3,000
DDC114YUQ-7-F	NRND (Use ADC114YUQ)	Automotive	N14	7	8	3,000
DDC114YUQ-13-F	NRND (Use ADC114YUQ)	Automotive	N14	13	8	10,000
DDC123JU-7-F	Active	AEC-Q101	N06	7	8	3,000
DDC114EU-7-F	Active	AEC-Q101	N13	7	8	3,000
DDC114EUQ-7-F	NRND (Use ADC114EUQ)	Automotive	N13	7	8	3,000
DDC114EUQ-13-F	NRND (Use ADC114EUQ)	Automotive	N13	13	8	10,000
DDC113TU-7-F	Active	AEC-Q101	N01	7	8	3,000
DDC143TU-7-F	Active	AEC-Q101	N07	7	8	3,000
DDC114TU-7-F	Active	AEC-Q101	N12	7	8	3,000
DDC114TUQ-7-F	Active	Automotive	N12	7	8	3,000
DDC143ZU-7-F	Active	AEC-Q101	N03	7	8	3,000
DDC115EU-7-F	Active	AEC-Q101	N02	7	8	3,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/guality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/. 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

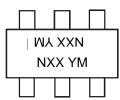
6. NRND = Not Recommended for New Design.

Notes:



Marking Information

SOT363



NXX = Product Type Marking Code (See Ordering Information) YM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key												
Year	2018	2019	2020	2021	202	22 20)23	2024	2025	2026	2027	2028
Code	F	G	Н		J		K	L	М	Ν	0	Р
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Charao	teristic	Symbol	Value	Unit
Supply Voltage, <pin: (1)="" (6)="" a<="" td="" to=""><td>nd (3) to (4)></td><td>V_{CC}</td><td>50</td><td>V</td></pin:>	nd (3) to (4)>	V _{CC}	50	V
Input Voltage, <pin: (1)="" (2)="" (4)="" (5)="" and="" to=""></pin:>	DDC124EU DDC144EU DDC114YU DDC123JU DDC114EU DDC113TU DDC143TU DDC143TU DDC144TU DDC144ZU DDC115EU	Vin	-10 to +40 -10 to +40 -6 to +40 -5 to +12 -10 to +40 -5V max -5V max -5V max -5V max -5 to +30 -10 to +40	V
Output Current	DDC124EU DDC144EU DDC114YU DDC123JU DDC114EU DDC113TU DDC143TU DDC144TU DDC144ZU DDC145EU	Io	30 30 70 100 50 100 100 100 100 20	mA
Output Current		I _{C(MAX)}	100	mA

Thermal Characteristics (@T _A = +25°C, unless otherwise specified.)						
Characteristic	Symbol	Value	Unit			
Power Dissipation (Notes 7 & 8)	PD	200	mW			
Thermal Resistance, Junction to Ambient Air (Note 7)	R _{0JA}	625	°C/W			
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C			

Notes: 7. Mounted on FR-4 PC Board with minimum recommended pad layout. 8. 150mW per element must not be exceeded.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

For R1 only Devices: DDC113TU & DDC143TU & DDC114TU

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	50			V	$I_{\rm C} = 50\mu A$
Collector-Emitter Breakdown Voltage	BV _{CEO}	50			V	$I_{C} = 1mA$
Emitter-Base Breakdown Voltage	BVEBO	5			V	$I_E = 50\mu A$
Collector Cutoff Current	I _{CBO}			0.5	μA	$V_{CB} = 50V$
Emitter Cutoff Current	I _{EBO}			0.5	μA	$V_{EB} = 4V$
Collector-Emitter Saturation Voltage	V _{CE(SAT)}			0.3	V	$\label{eq:lc/l_B} \begin{split} & I_C/I_B = 2.5 mA \ / \ 0.25 mA & DDC143TU \\ & I_C/I_B = 1 mA \ / \ 0.1 mA & DDC114TU \\ & I_C/I_B = 10 mA \ / \ 1 mA & DDC113TU \end{split}$
DC Current Transfer Ratio	h _{FE}	100	250	600		$I_C = 1mA$, $V_{CE} = 5V$
Input Resistor (R ₁) Tolerance	ΔR_1	-30	_	+30	%	—
Gain-Bandwidth Product (Note 9)	fT	_	250		MHz	$V_{CE} = 10V, I_E = -5mA, f = 100MHz$

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

For R1, R2 Devices: DDC124EU& DDC144EU& DDC114YU& DDC123JU& DDC114EU& DDC143ZU& DDC115EU

Characterist	ic	Symbol	Min	Тур	Max	Unit	Test Condition
	DDC124EU DDC144EU DDC114YU DDC123JU DDC123JU DDC114EU DDC143ZU DDC115EU	V _{L(OFF)}	0.5 0.5 0.5 0.5 0.5 0.5 0.5	1.1 1.1 — 1.1 —	_		V _{CC} = 5V, I _O = 100µA
Input Voltage	DDC124EU DDC144EU DDC114YU DDC123JU DDC123JU DDC14EU DDC143ZU DDC115EU	V _{L(ON)}		1.9 1.9 — 1.9 —	3.0 3.0 1.4 1.1 3.0 1.3 3	V	$ \begin{array}{l} V_{O}=0.3V, \ I_{O}=5mA \\ V_{O}=0.3V, \ I_{O}=2mA \\ V_{O}=0.3V, \ I_{O}=1mA \\ V_{O}=0.3V, \ I_{O}=5mA \\ V_{O}=0.3V, \ I_{O}=10mA \\ V_{O}=0.3V, \ I_{O}=5mA \\ V_{O}=0.3V, \ I_{O}=5mA \\ V_{O}=0.3V, \ I_{O}=1mA \end{array} $
Output Voltage	DDC124EU DDC144EU DDC114YU DDC123JU DDC123JU DDC114EU DDC143ZU DDC115EU	V _{O(ON)}		0.1	0.3	V	$\begin{split} & _O/I_L = 10mA / 0.5mA \\ & _O/I_L = 10mA / 0.5mA \\ & _O/I_L = 5mA / 0.25mA \\ & _O/I_L = 5mA / 0.25mA \\ & _O/I_L = 10mA / 0.5mA \\ & _O/I_L = 5mA / 0.25mA \\ & _O/I_L = 10mA / 0.5mA \end{split}$
Input Current	DDC124EU DDC144EU DDC114YU DDC123JU DDC123JU DDC114EU DDC143ZU DDC115EU	ار	_	_	0.36 0.18 0.88 3.6 0.88 1.8 0.15		VI = 5V
Output Current		I _{O(OFF)}	_	_	0.5	μA	$V_{CC} = 50V, V_1 = 0V$
DC Current Gain	DDC124EU DDC144EU DDC114YU DDC114YUQ DDC114YUQ DDC123JU DDC114EU DDC143ZU DDC115EU	GL	56 68 80 80 30 80 82	_			$ \begin{array}{l} V_{O}=5V, \ I_{O}=5mA \\ V_{O}=5V, \ I_{O}=5mA \\ V_{O}=5V, \ I_{O}=10mA \\ V_{O}=5V, \ I_{O}=5mA \\ V_{O}=5V, \ I_{O}=10mA \\ V_{O}=5V, \ I_{O}=5mA \\ V_{O}=5V, \ I_{O}=10mA \\ V_{O}=5V, \ I_{O}=10mA \\ V_{O}=5V, \ I_{O}=5mA \end{array} $
Input Resistor (R1) Tolerance		ΔR_1	-30		+30	%	
Resistance Ratio Tolerance		$\Delta(R_2/R_1)$	-20		+20	%	
Gain-Bandwidth Product (Note 9)		f _T	—	250	—	MHz	$V_{CE} = 10V, I_E = 5mA, f = 100MHz$

Note: 9. Transistor - For Reference Only.



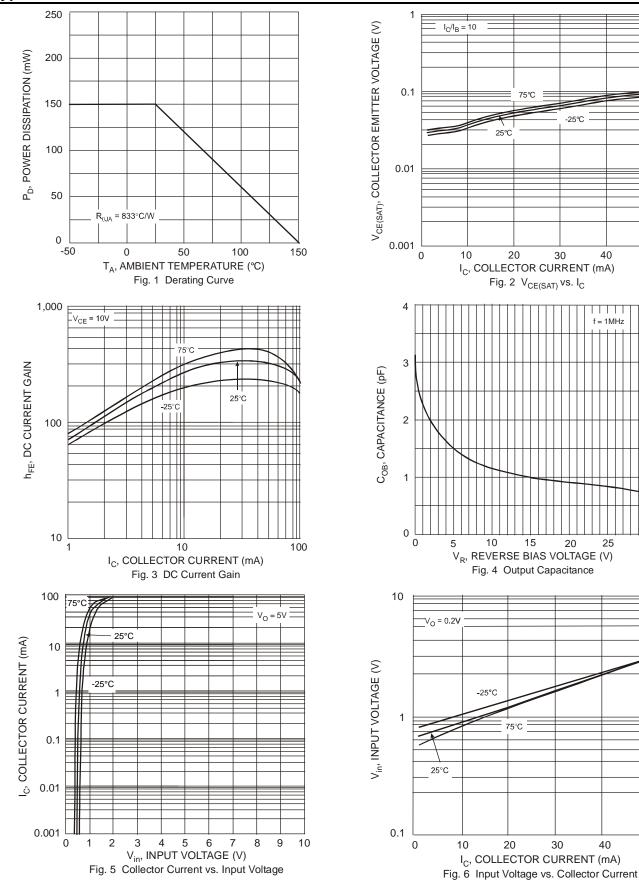
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f = 1MHz

50

30

Typical Curves – DDC123JU (@T_A = +25°C, unless otherwise specified.)

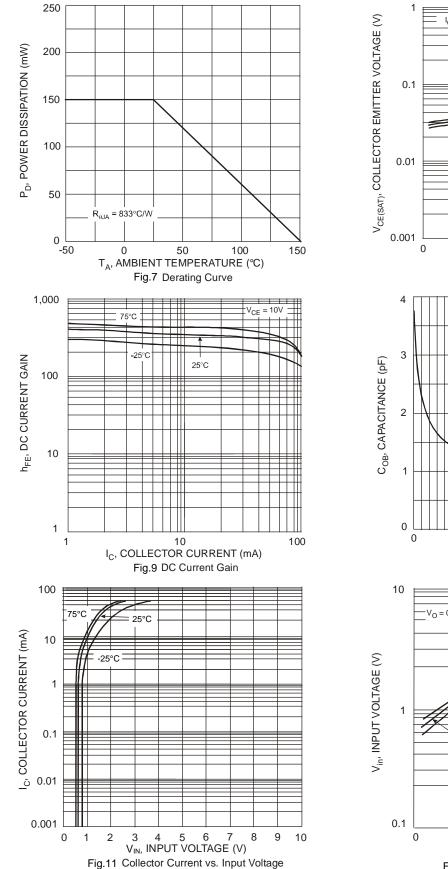


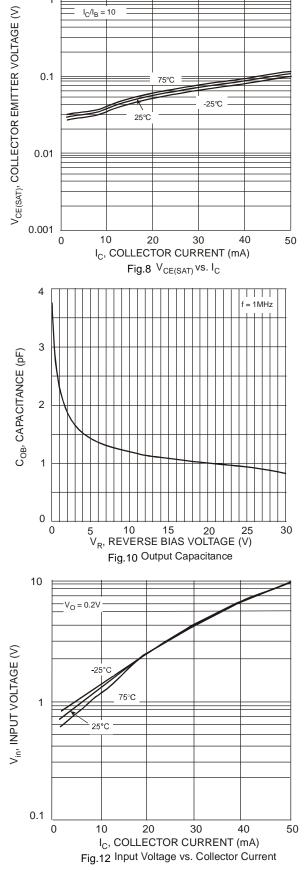
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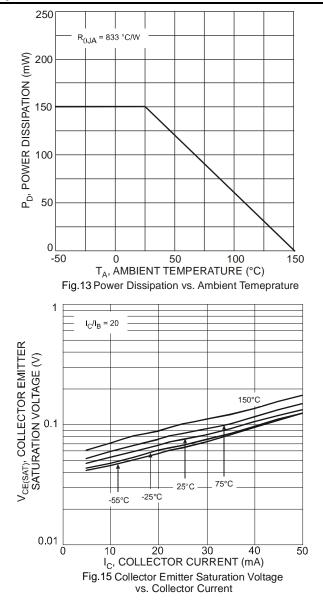
Typical Curves – DDC114YU (@T_A = +25°C, unless otherwise specified.)

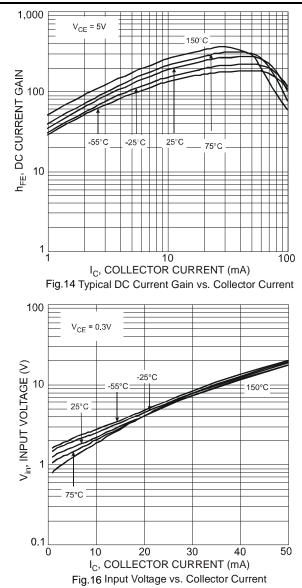






Typical Curves – DDC124EU (@T_A = +25°C, unless otherwise specified.)

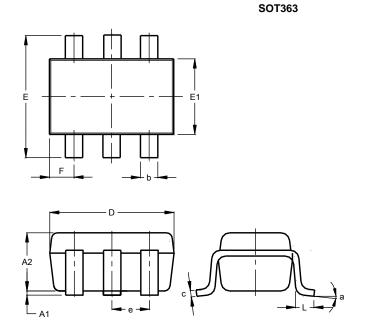






Package Outline Dimensions

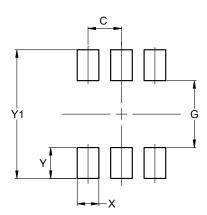
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT363								
Dim	Min Max Typ								
A1	0.00	0.10	0.05						
A2	0.90	1.00	0.95						
b	0.10	0.30	0.25						
С	0.10	0.22	0.11						
D	1.80	2.20	2.15						
ш	2.00	2.20	2.10						
E1	1.15	1.35	1.30						
e	C).650 E	SC						
F	0.40	0.45	0.425						
L	0.25	0.40	0.30						
а	0°	8°	-						
All	Dimen	sions	in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT363

Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500



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