



## DGTD65T60S2PT

### 650V FIELD STOP IGBT IN TO-247

#### Description

The DGTD65T60S2PT is produced using advanced Field Stop Trench IGBT 2<sup>nd</sup> Generation Technology, which not only gives high-switching efficiency, but is also extremely rugged and excellent quality for applications where low conduction losses are essential.

## Features

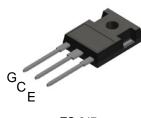
- High Speed Switching & Low Power Loss
- $V_{CE(sat)} = 1.85V @ I_C = 60A$
- High Input Impedance
- t<sub>rr</sub> = 110ns (typ) @ di<sub>F</sub>/dt = 500A/µs
- $E_{off} = 0.53 \text{mJ} @ T_{C} = 25^{\circ}\text{C}$
- Maximum Junction Temperature 175°C
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Applications**

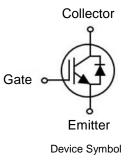
- UPS
- Welder
- Solar Inverter
- IH Cooker

#### Mechanical Data

- Case: TO-247 (Type MC)
- Case Material: Molded Plastic. "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Terminals: Finish Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 5.6 grams (Approximate)



TO-247



## Ordering Information (Note 4)

Product	Marking	Quantity
DGTD65T60S2PT	DGTD65T60S2	450 per Box in Tubes (Note 5)

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied. 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

5. 30 Devices per Tube.

## **Marking Information**



);; = Manufacturer's Marking DGTD65T60S2 = Product Type Marking Code YY = Year (ex: 18 = 2018) LLLLL = Lot Code WW = Week (01 to 53)



## Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Collector-Emitter Voltage		V <sub>CE</sub>	650	V	
DC Calle star Concert limited by T	$T_C = 25^{\circ}C$	lc	100	А	
DC Collector Current, limited by T <sub>vjmax</sub>	$T_C = 100^{\circ}C$		60	А	
Pulsed Collector Current, tp limited by Tvjmax	I <sub>Cpuls</sub>	180	А		
Turn Off Safe Operating Area $V_{CE} \le 650V$ , $T_{vi} = 175^{\circ}C$		-	180	А	
Diada Forward Current limited by T	$T_C = 25^{\circ}C$	IF	60	А	
Diode Forward Current limited by T <sub>vjmax</sub>	$T_C = 100^{\circ}C$		30	А	
Diode Pulsed Current, tp limited by Tvimax		I <sub>Fpuls</sub>	200	A	
Gate-Emitter Voltage		V <sub>GE</sub>	±20	V	
Short Circuit Withstand Time $V_{CC} \le 400V$ , $R_G=7\Omega$ , $V_{GE} = 15V$ , $T_{vj} = 150^{\circ}C$ Allowed Number of Short Circuits < 1000 Time Between Short Circuits < 1.0s		tsc	5	μs	
					5

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Power Dissipation Linear Derating Factor (Note 6) $T_{C} =$	25°C	428	W	
Fower dissipation Einear derating raciol (Note o) $T_{\rm C} =$	100°C PD	214		
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>0JA</sub>	40	°C/W	
Thermal Resistance, Junction to Case for IBGT (Note 6)	R <sub>θJC</sub>	0.35		
Thermal Resistance, Junction to Case for Diode (Note 6)	R <sub>eJC</sub>	1.20		
Operating Temperature	T <sub>vi</sub>	-40 to +175	°C	
Storage Temperature Range	T <sub>STG</sub>	-55 to +150		

Note: 6. When mounted on a standard JEDEC 2-layer FR-4 board.

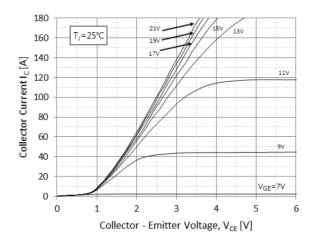


## Electrical Characteristics (@T<sub>vj</sub> = +25°C, unless otherwise specified.)

Parameter		Symbol	Min	Тур	Max	Unit	Condition	
STATIC CHARACTERISTICS								
Collector-Emitter Breakdown Voltage		BV <sub>CES</sub>	650	-	-	V	$I_C = 2mA, V_{GE} = 0V$	
Collector-Emitter Saturation Voltage	T <sub>vj</sub> = 25°C		-	1.85	2.40	V	I <sub>C</sub> = 60A, V <sub>GE</sub> = 15V	
Collector-Emitter Saturation Voltage	T <sub>vj</sub> = 175°C	V <sub>CE(sat)</sub>	-	2.60	-			
Diode Forward Voltage	$T_{vj} = 25^{\circ}C$	N/	-	1.45	2.00	- V	$V_{GE} = 0V, I_F = 25A$	
Didde Folward Voltage	T <sub>vj</sub> = 175°C	VF	-	1.35	-			
Gate-Emitter Threshold Voltage		V <sub>GE(th)</sub>	4.0	5.0	6.0	V	$V_{CE} = V_{GE}$ , $I_C = 0.5mA$	
Zero Gate Voltage Collector Current		ICES	-	-	40	μA	$V_{CE} = 650V, V_{GE} = 0V$	
Gate-Emitter Leakage Current		I <sub>GES</sub>	-	-	±100	nA	$V_{GE} = 20V, V_{CE} = 0V$	
DYNAMIC CHARACTERISTICS								
Total Gate Charge		Qg	-	95	-		$V_{CE} = 520V, I_{C} = 60A,$	
Gate-Emitter Charge		Q <sub>ge</sub>	-	19	_	nC	$V_{CE} = 520V, T_{C} = 60A,$ $V_{GE} = 15V$	
Gate-Collector Charge		Q <sub>gc</sub>	-	47	-		VGE = 15 V	
Input Capacitance		Cies	-	2,327	-			
Reverse Transfer Capacitance		Cres	-	55	-	pF	$V_{CE} = 25V, V_{GE} = 0V,$ f = 1MHz	
Output Capacitance		Coes	-	270	-		1 - 110112	
Internal Emitter Inductance Measured From Case	5mm (0.197")	LE	-	13	-	nH	-	
SWITCHING CHARACTERISTICS				•		•		
Turn-on Delay Time		t <sub>d(on)</sub>	-	42	-			
Rise time		tr	-	54	-	<b>n</b> 0		
Turn-off Delay Time		t <sub>d(off)</sub>	-	142	-	ns	$V_{GE} = 15V, V_{CC} = 400V,$	
Fall Time		t <sub>f</sub>	-	40	-		$I_{C} = 60A, R_{G} = 7\Omega,$ Inductive Load.	
Turn-on Switching Energy		Eon	-	0.92	-		$T_{vi} = 25^{\circ}C$	
Turn-off Switching Energy		Eoff	-	0.53	-	mJ	$V_{ij} = 25 \text{ C}$	
Total Switching Energy		E <sub>ts</sub>	-	1.45	-	1		
Reverse Recovery Time		t <sub>rr</sub>	-	110	-	ns	I <sub>F</sub> = 25A,	
Reverse Recovery Current		Irr	-	18	-	А	di <sub>F</sub> /dt = 500A/µs,	
Reverse Recovery Charge		Q <sub>rr</sub>	-	1.10	-	μC		
Turn-on Delay Time		t <sub>d(on)</sub>	-	45	-			
Rise time		tr	-	58	_		$V_{GE} = 15V, V_{CC} = 400V,$ $I_C = 60A, R_G = 7\Omega,$ Inductive Load.	
Turn-off Delay Time		t <sub>d(off)</sub>	-	152	-	ns		
Fall Time		t <sub>f</sub>	-	35	-			
Turn-on Switching Energy		Eon	-	1.43	_			
Turn-off Switching Energy		Eoff	-	0.53	_	– T <sub>vj</sub> = 175°C mJ		
Total Switching Energy		E <sub>ts</sub>	-	1.96	-			
Reverse Recovery Time		t <sub>rr</sub>	-	205	_	ns	I <sub>F</sub> = 25A,	
Reverse Recovery Current		Irr	-	25	-	А	di <sub>F</sub> /dt = 500A/µs,	
Reverse Recovery Charge		Q <sub>rr</sub>	-	2.67	-	μC	T <sub>vj</sub> = 175°C	



## Typical Performance Characteristics (@TA = +25°C, unless otherwise specified.)





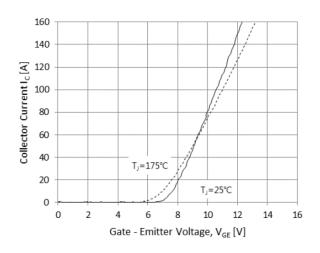
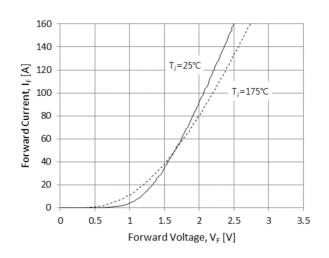


Fig.3 Typical Transfer Characteristics



**Fig.5 Diode Forward Characteristics** 

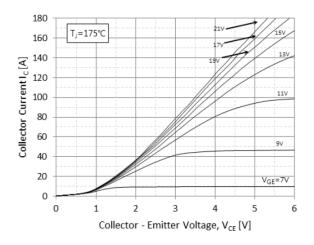


Fig.2 Typical Output Characteristics(TJ=175℃)

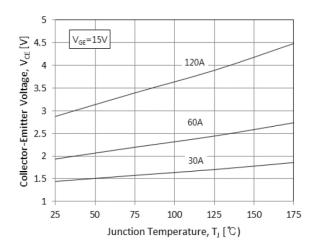
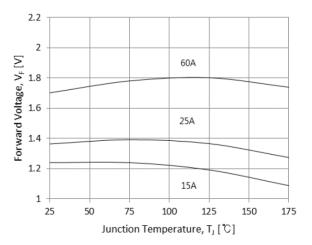


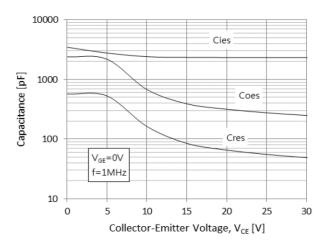
Fig.4 Typical Collector-Emitter Saturation Voltage -Junction Temperature



#### Fig.6 Diode Forward-Junction Temperature



## Typical Performance Characteristics (continued)



#### Fig.7 Typical Capacitance

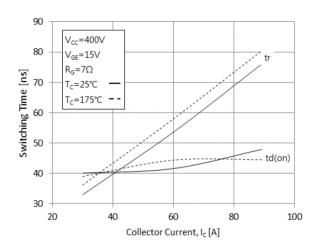


Fig.9 Typical Turn on-Collector Current

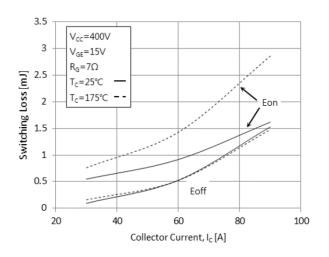
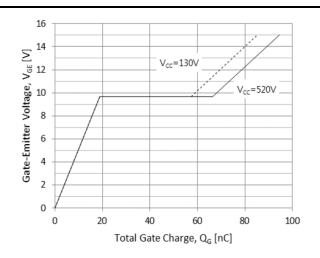


Fig.11 Switching Loss-Collector Current



#### Fig.8 Typical Gate Charge

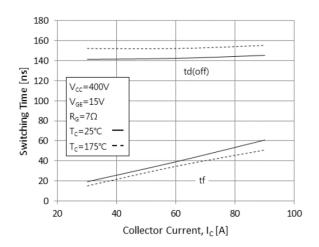


Fig.10 Typical Turn off-Collector Current

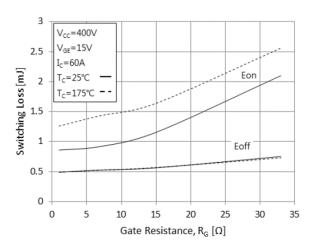
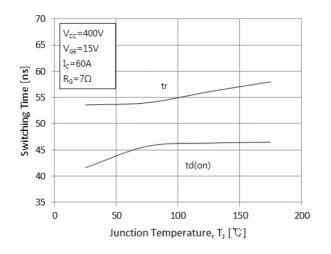


Fig.12 Switching Loss-Gate Resistance



## Typical Performance Characteristics (cont.)



### Fig.13 Turn on Characteristics-Junction Temperature

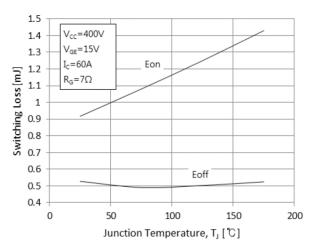


Fig.15 Switching Loss-Junction Temperature

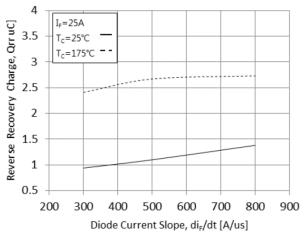


Fig.17 Reverse Recovery Charge - Diode Current Slope

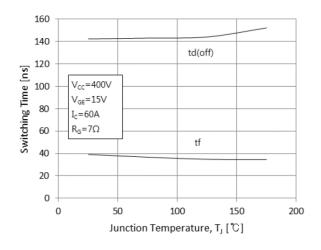


Fig.14 Turn off Characteristics-Junction Temperature

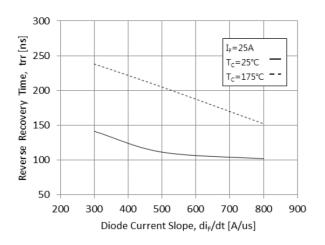


Fig.16 Reverse Recovery Time - Diode Current Slope

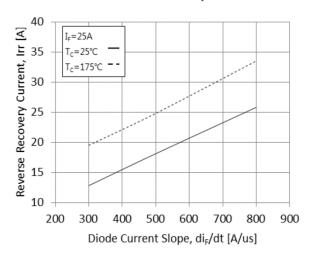


Fig.18 Reverse Recovery Current - Diode Current Slope



## Typical Performance Characteristics (cont.)

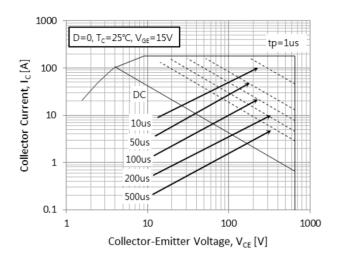
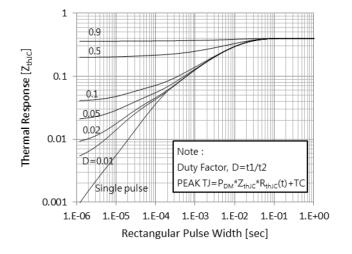
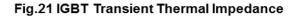


Fig.19 Forward Bias Safe Operating Area





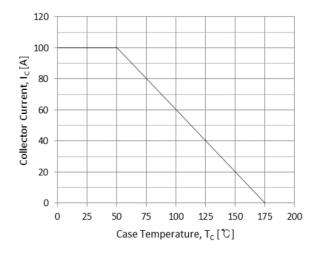


Fig.20 Case Temperature-Collector Current

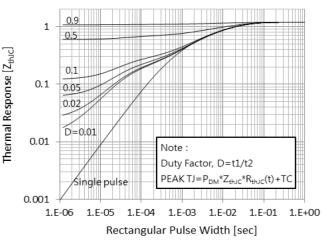
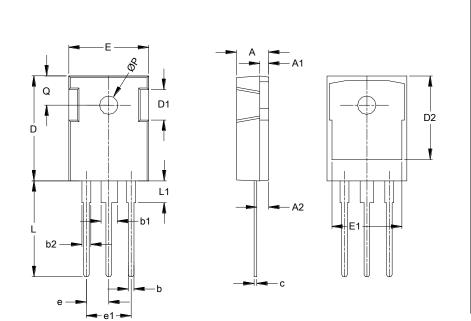


Fig.22 FRD Transient Thermal Impedance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.



## TO247 (Type MC)

TO-247 (Type MC)					
Dim	Min Max		Тур		
Α	4.700	5.310	-		
A1	1.500	2.490	-		
A2	2.200	2.600	-		
b	0.990	1.400	-		
b1	2.590	3.430	-		
b2	1.650	2.390	-		
с	0.380	0.890	-		
D	20.30	21.46	-		
D1	4.320	5.490	-		
D2	13.08	-	-		
Е	15.45	16.26	-		
E1	13.06	13.06 14.02			
е	5.450				
e1	10.90				
L	19.81	20.57	-		
L1	-	4.500	-		
Q	5.380	6.200	-		
øP	3.500	3.700	-		
All Dimensions in mm					

Note : For high-voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.



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