



#### COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

#### **Product Summary**

Device	BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
04	20	0.4Ω @ V <sub>GS</sub> = 10V	0.8A
Q1 30	30	0.7Ω @ V <sub>GS</sub> = 4.5V	0.62A
00	00	0.9Ω @ V <sub>GS</sub> = -10V	-0.55A
Q2	-30	1.7Ω @ V <sub>GS</sub> = -4.5V	-0.4A

#### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- **Power Management Functions**
- DC-DC Converters

#### **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Mechanical Data**

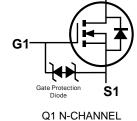
- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.027 grams (Approximate)

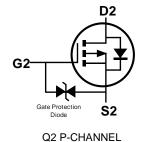


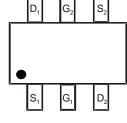


Top View









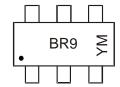
Top View Pin out

### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMC3401LDW-7	SOT363	3000/Tape & Reel
DMC3401LDW-13	SOT363	10000/Tape & Reel

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  - 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
  - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  - 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



BR9 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  or  $\underline{Y}$ = Year (ex: G = 2019) M = Month (ex: 9 = September)

#### Date Code Key

Year	2018	2	019	2020		2021	2022		2023	2024		2025
Code	F		G	Н		I	J		K	L		М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code					_	_	_	_	_	_		_



# **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic				Value_Q1	Value_Q2	Unit
Drain-Source Voltage			$V_{DSS}$	30	-30	V
Gate-Source Voltage				±20	±20	V
Continuous Drain Current (Note 6) Q1: V <sub>GS</sub> = 10V Q2: V <sub>GS</sub> = -10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C		0.8 0.6	-0.55 -0.44	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	0.4	-0.38	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 19	I <sub>DM</sub>	4	-2.4	Α		

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		$P_{D}$	0.29	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	433	°C/W
Total Power Dissipation (Note 6)		$P_{D}$	0.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	301	°C/W
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +150	°C

## Electrical Characteristics – N Channel – Q1 (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	-	-	٧	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	1.0	μΑ	$V_{DS} = 30V$ , $V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.8	1.2	1.6	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	D	-	0.2	0.4	Ω	$V_{GS} = 10V, I_D = 0.59A$
Static Dialit-Source Off-Resistance	R <sub>DS(ON)</sub>	-	0.3	0.7	Ω	$V_{GS} = 4.5V, I_D = 0.2A$
Diode Forward Voltage	$V_{SD}$	-	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 0.1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>	-	50	1	рF	451/11/
Output Capacitance	Coss	-	12	-	рF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, -f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	-	10	-	рF	1 – 1.0101112
Gate Resistance	$R_{g}$	-	58	-	Ω	$V_{DS} = V_{GS} = 0V$ , $f = 1.0MHz$
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	-	0.5	-	nC	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	-	1.2	-	nC	\/ 10\/   350mA
Gate-Source Charge	Qgs	-	0.2	-	nC	$V_{DS} = 10V, I_D = 250mA$
Gate-Drain Charge	Q <sub>gd</sub>	-	0.1	-	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	-	3.5	-	ns	
Turn-On Rise Time	t <sub>R</sub>	-	3.3	-	ns	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30V,
Turn-Off Delay Time	t <sub>D(OFF)</sub>	-	16.8	-	ns	$I_D = 100 \text{mA}, R_G = 25 \Omega$
Turn-Off Fall Time	t <sub>F</sub>	-	13.8	-	ns	

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

<sup>7.</sup> Short duration pulse test used to minimize self-heating effect.

<sup>8.</sup> Guaranteed by design. Not subject to product testing.



# **Electrical Characteristics – P Channel – Q2** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

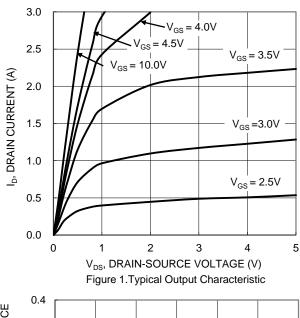
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	-	-	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	-1	μΑ	$V_{DS} = -24V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-1	-2.2	-2.6	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	D	-	0.5	0.9	Ω	$V_{GS} = -10V, I_D = -0.42A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	0.78	1.7	12	$V_{GS} = -4.5V$ , $I_{D} = -0.2A$
Diode Forward Voltage	$V_{SD}$	-	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -0.23A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>	-	19	-	pF	151/11/ 01/
Output Capacitance	Coss	-	16	-	рF	$V_{DS} = -15V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	Crss	-	3	-	рF	1 = 1.000112
Gate Resistance	Rg	-	4.4	-	kΩ	$V_{DS} = V_{GS} = 0V$ , $f = 1.0MHz$
Total Gate Charge (V <sub>GS</sub> = -4.5V)	$Q_{g}$	-	0.36	-	nC	
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg	-	0.8	-	nC	V <sub>DS</sub> = -10V. I <sub>D</sub> = -0.24A
Gate-Source Charge	$Q_{gs}$	-	0.1	-	nC	$V_{DS} = -10V, I_{D} = -0.24A$
Gate-Drain Charge	$Q_{gd}$	-	0.1	-	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	-	3.3	-	ns	
Turn-On Rise Time	t <sub>R</sub>	-	2.3	-	ns	$V_{GS} = -10V, V_{DD} = -15V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	-	406	-	ns	$I_D = -0.5A, R_G = 1\Omega$
Turn-Off Fall Time	t <sub>F</sub>	-	237	-	ns	

Notes:

<sup>7.</sup> Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.



### **Typical Characteristics - N-CHANNEL**



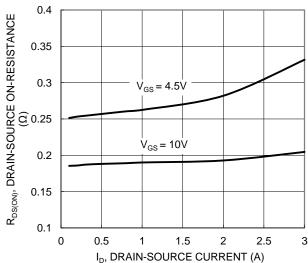


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

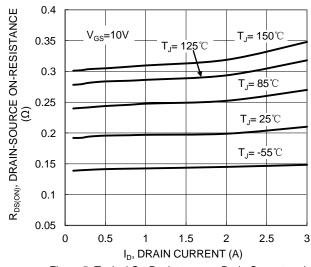
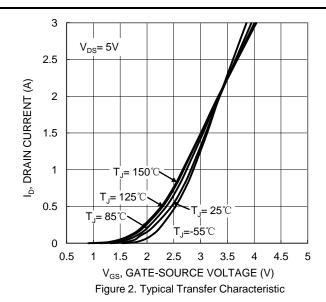
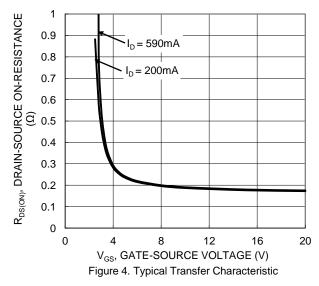


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





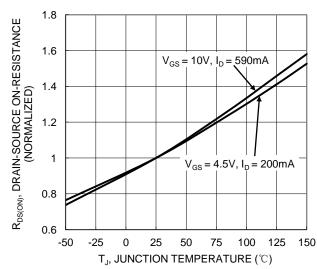


Figure 6. On-Resistance Variation with Temperature



#### **Typical Characteristics - N-CHANNEL (Cont.)**

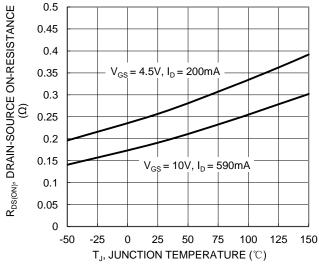
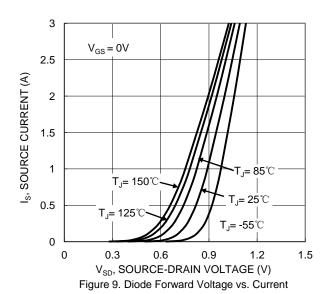
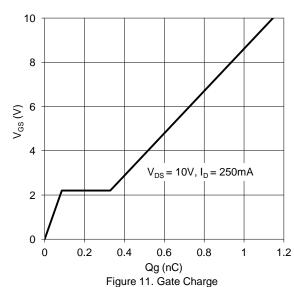
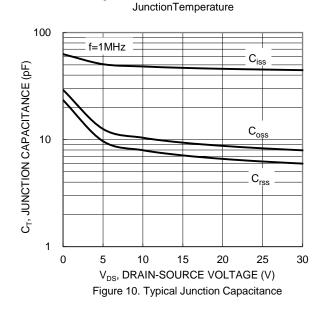


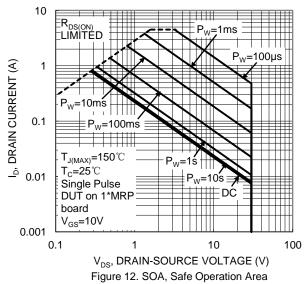
Figure 7. On-Resistance Variation with Temperature





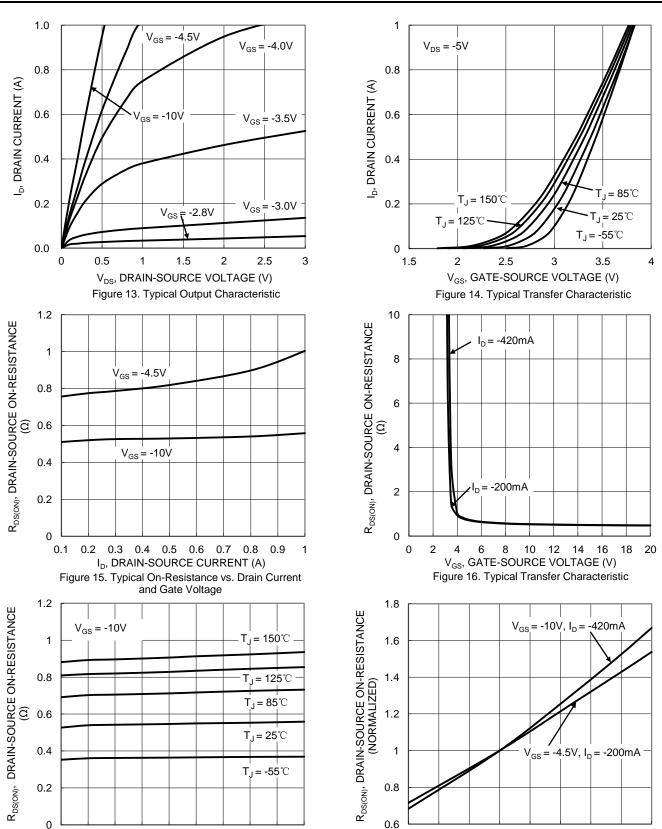
1.5 V<sub>GS(TH)</sub>, GATE THRESHOLD VOLTAGE (V) 1.4 1.3 1.2  $I_{D} = 250 \mu A$ 1.1 1 0.9 0.8 0.7 0.6 -50 -25 25 50 75 100 125 T<sub>J</sub>, JUNCTION TEMPERATURE (°C) Figure 8. Gate Threshold Variation vs.







### **Typical Characteristics - P-CHANNEL**



50

T<sub>J</sub>, JUNCTION TEMPERATURE (°C)

75

100

0

-50

25

0.1 0.2

0.3 0.4 0.5 0.6 0.7 0.8 0.9

I<sub>D</sub>, DRAIN CURRENT (A)

Figure 17. Typical On-Resistance vs. Drain Current

and Junction Temperature

125



#### Typical Characteristics - P-CHANNEL (Cont.)

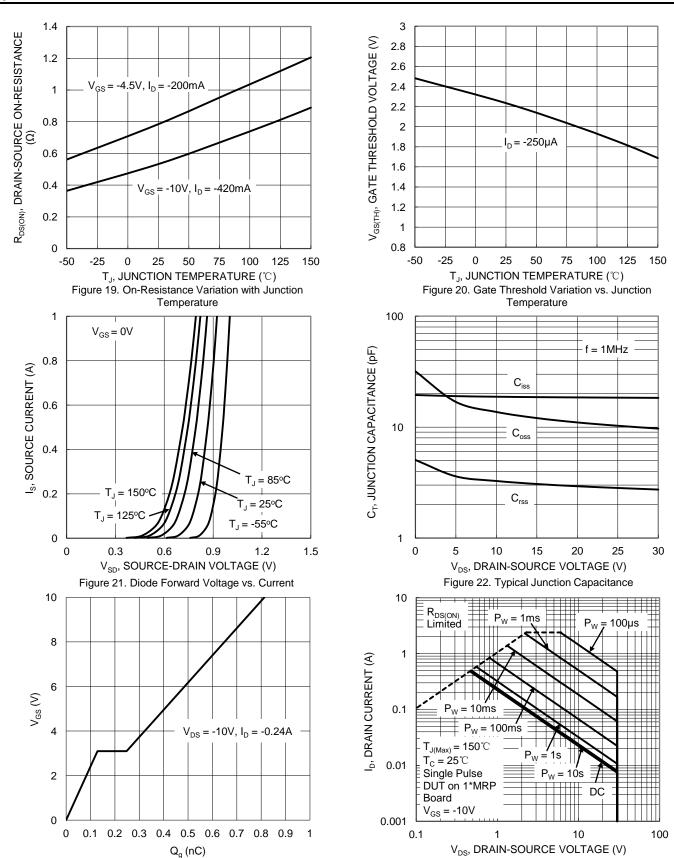


Figure 23. Gate Charge

Figure 24. SOA, Safe Operation Area



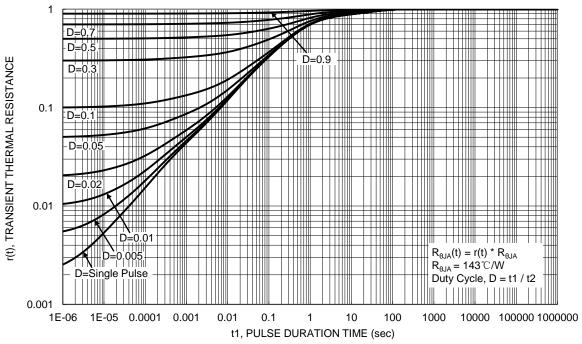
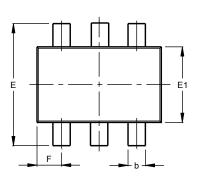


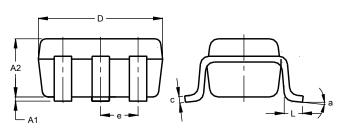
Figure 25. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.





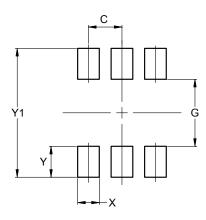
SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90 1.00 0.95						
b	0.10	0.30	0.25				
C	0.10	0.22	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C	.650 B	SC				
F	0.40	0.45	0.425				
Ĺ	0.25	0.40	0.30				
а	0°	8°					
All I	Dimen	sions	in mm				

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### **SOT363**

**SOT363** 



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500



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