





#### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
201/	$29m\Omega$ @ $V_{GS} = 10V$	5.47A
20V	$35m\Omega$ @ $V_{GS} = 4.5V$	5.2A

## **Description**

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP.

### **Applications**

- General Purpose Interfacing Switch
- Power Management Functions

## **Features**

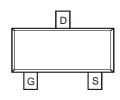
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

### **Mechanical Data**

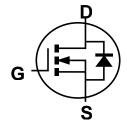
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 ©3
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)







Top View



**Equivalent Circuit** 

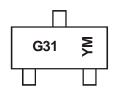
# **Ordering Information** (Note 5)

Part Number	Qualification	Case	Packaging
DMG3420UQ-7	Automotive	SOT23	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



G31 = Product Type Marking Code YM = Date Code Marking

YM = Date Code Marking Y = Year (ex: G = 2019)

M = Month (ex: 9 = September)

Date Code Key

Year	2009	~	2019	2020	202	21 20	022	2023	2024	2025	2026	2027
Code	W	?	G	Н	- 1		J	K	L	M	N	0
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characte		Symbol	Value	Unit
Drain-Source Voltage		V <sub>DSS</sub>	20	V
Gate-Source Voltage		V <sub>GSS</sub>	±12	V
Continuous Drain Current (Note 6)	$T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	I <sub>D</sub>	5.47 3.43	А
Pulsed Drain Current (Note 7)		I <sub>DM</sub>	20	Α

### **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P <sub>D</sub>	0.74	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 6)	$R_{\theta JA}$	167	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

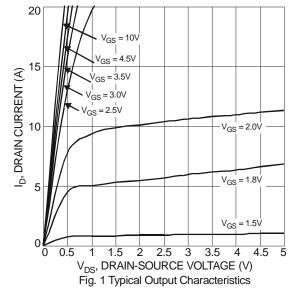
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

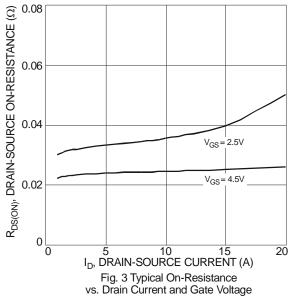
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
DFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	_	1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)			•	•		
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5	0.95	1.2	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
		-	21	29		$V_{GS} = 10V, I_D = 6A$
Static Desir Course On Besistance		-	25	35		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5A
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	34	48	mΩ	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 4A
			65	91		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 2A
Forward Transfer Admittance	Y <sub>fs</sub>	-	9	_	s	V <sub>DS</sub> = 5V, I <sub>D</sub> = 3.8A
Diode Forward Voltage	V <sub>SD</sub>	-	0.75	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A
DYNAMIC CHARACTERISTICS (Note 9)					•	•
Input Capacitance	C <sub>iss</sub>		434.7	_	pF	
Output Capacitance	Coss		69.1	_	pF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	_	61.2	_	pF	1 - 1.000112
Gate Resistance	Rg	_	1.53	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$
Total Gate Charge	Qg	_	5.4	_	nC	
Gate-Source Charge	Q <sub>gs</sub>	_	0.9	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	$Q_{gd}$	_	1.5	_	nC	$-I_D = 6A$
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.5	_	ns	
Turn-On Rise Time	t <sub>R</sub>	_	8.3	_	ns	$V_{DD} = 10V, V_{GS} = 5V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	21.6	_	ns	$R_L = 1.7\Omega$ , $R_g = 6\Omega$
Turn-Off Fall Time	t <sub>F</sub>		5.3		ns	

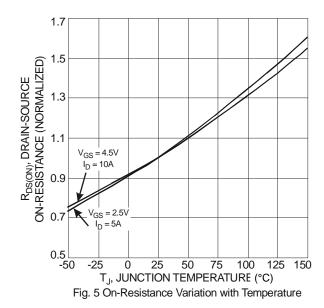
Notes:

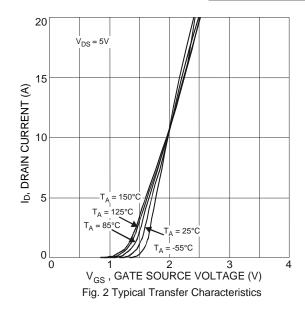
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 7. Repetitive rating, pulse width limited by junction temperature.
- Short duration pulse test used to minimize self-heating effect.
  Guaranteed by design. Not subject to production testing.

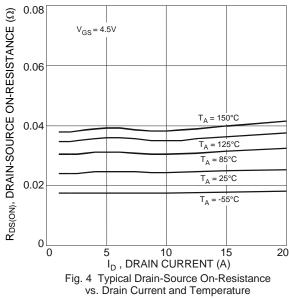


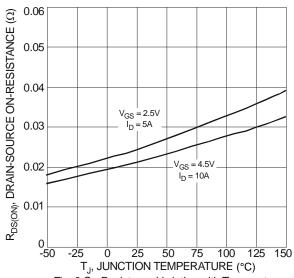




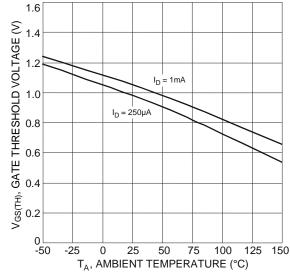




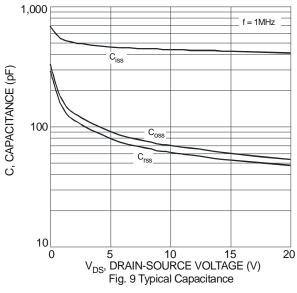


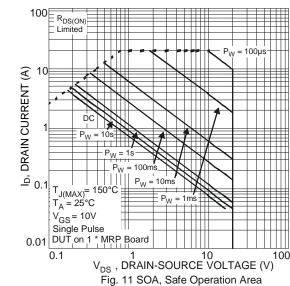


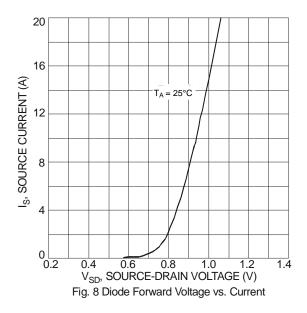


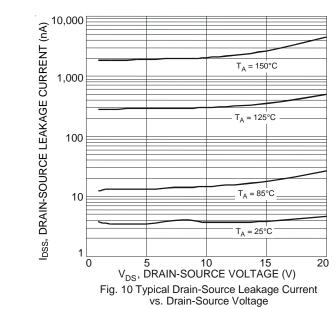














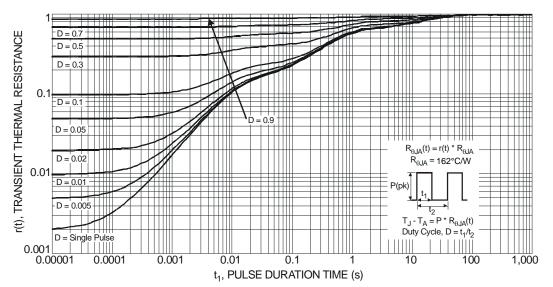


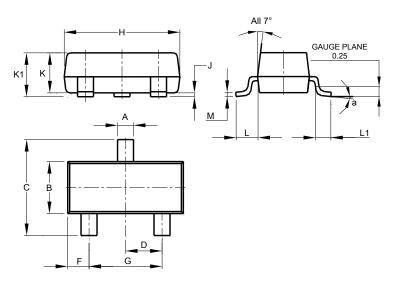
Fig. 12 Transient Thermal Response



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23

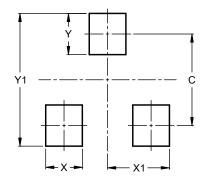


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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