



DMG4712SSS

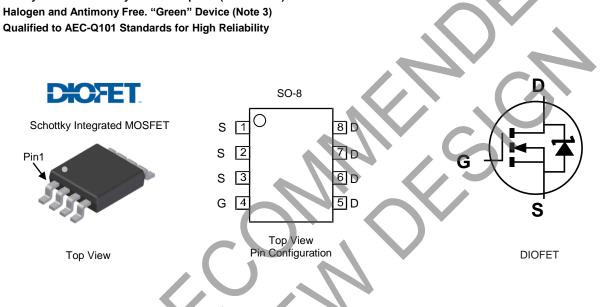
N-CHANNEL ENHANCEMENT MODE MOSFET WITH SCHOTTKY DIODE

Features

- High Density UMOS with Schottky Barrier Diode
- Low Leakage Current at High Temperature
- High Conversion Efficiency
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Utilizes Diodes Incorporated's Monolithic DIOFET Technology to Increase Conversion Efficiency
- UIS Tested, R_G Tested
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.074 grams (Approximate)



Ordering Information (Note 4)

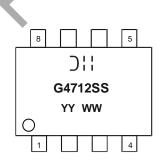
	Part Number	Case	Packaging	
	DMG4712SSS-13	SO-8	2,500 / Tape & Reel	
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.				

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and 1000ppm antimony compounds.

For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



) | | = Manufacturer's Marking G4712SS = Product Type Marking Code YYWW = Date Code Marking YY or \overline{YY} = Year (ex: 18 = 2018) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Char	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	30	V		
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 5)	Steady State	TA = +25°C TA = +85°C	Ι _D	11.2 6.6	А
Pulsed Drain Current (Note 6)	I _{DM}	63	A		
Avalanche Current (Notes 6 & 7)			I _{AR}	30	A
Repetitive Avalanche Energy (Notes 6 & 7) L = 0.1mH			E _{AR}	45	mJ

Thermal Characteristics

Symbol	Value	Unit
PD	1.55	W
Reja	81.3	°C/W
TJ, TSTG	-55 to +150	°C
	PD Reja	P _D 1.55 R _{0JA} 81.3

Electrical Characteristics (@ T_A = +25°C, unless otherwise stated.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage		30	- 🔨	-	V	$V_{GS} = 0V, I_D = 1mA$	
Zero Gate Voltage Drain Current	BV _{DSS}	-		100	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	-	-	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)				Ť		·	
Gate Threshold Voltage	V _{GS(TH)}	1.0		2.2	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)} -	10	14.0	mΩ	$V_{GS} = 10V, I_D = 11.2A$		
Encoded Transfor Astrolutions			11	15.4		$V_{GS} = 4.5V, I_D = 10A$	
Forward Transfer Admittance	Y _{fs}	-	23	-	S	$V_{DS} = 5V, I_D = 11.2A$	
Diode Forward Voltage	V _{SD}	-	0.37	0.55	V	$V_{GS} = 0V, I_S = 1A$	
Maximum Body-Diode + Schottky Continuous Current	ls		-	5	A	-	
DYNAMIC CHARACTERISTICS (Note 9)			1			1	
Input Capacitance	Ciss	-	2,296	-	pF		
Output Capacitance	Coss	-	164	-	рF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	-	120	-	рF		
Gate Resistance	Rg	-	1.3	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qq	-	45.7	-	nC		
Total Gate Charge (V _{GS} = 4.5V)	Q _q	-	19.3	-	nC		
Gate-Source Charge	Q _{gs}	-	5.0	-	nC	V _{DS} = 15V, I _D = 11.2A	
Gate-Drain Charge	Q _{qd}	-	2.9	-	nC		
Turn-On Delay Time	t _{D(ON)}	-	5.5	-	ns		
Turn-On Rise Time	t _R	-	24.4	-	ns	V _{GS} = 10V, V _{DS} = 15V,	
Turn-Off Delay Time	tD(OFF)	-	33.1	-	ns	$R_G = 3\Omega, R_L = 1.2\Omega$	
Turn-Off Fall Time	tF	-	6.6	-	ns		

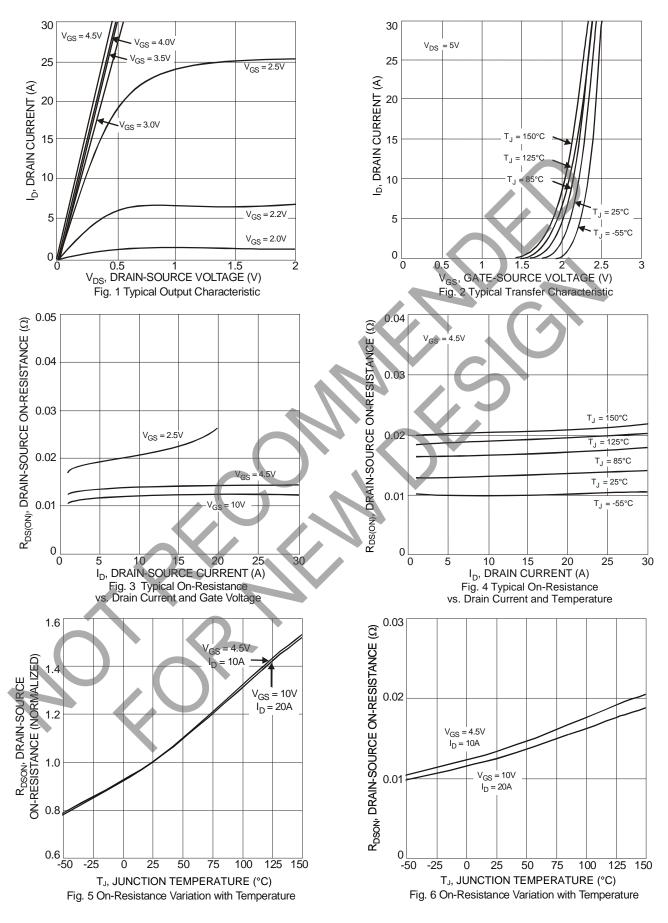
Notes:

5. Device mounted on FR-4 PCB with minimum recommended pad layout. The value in any given application depends on the user's specific board design. 5. Device mounted on PA-4 PCB with minimum recommended particle avoit. The value in any given application depends on the user's specific board designed. 6. Repetitive rating, pulse width limited by junction temperature. 7. I_{AR} and E_{AR} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}$ C. L = 0.1mH, $V_{DD} = 0V$, $R_G = 0\Omega$, rated $V_{DS} = 30V$, and $V_{GS} = 10V$. 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.



NOT RECOMMENDED FOR NEW DESIGN -NO ALTERNATE PART

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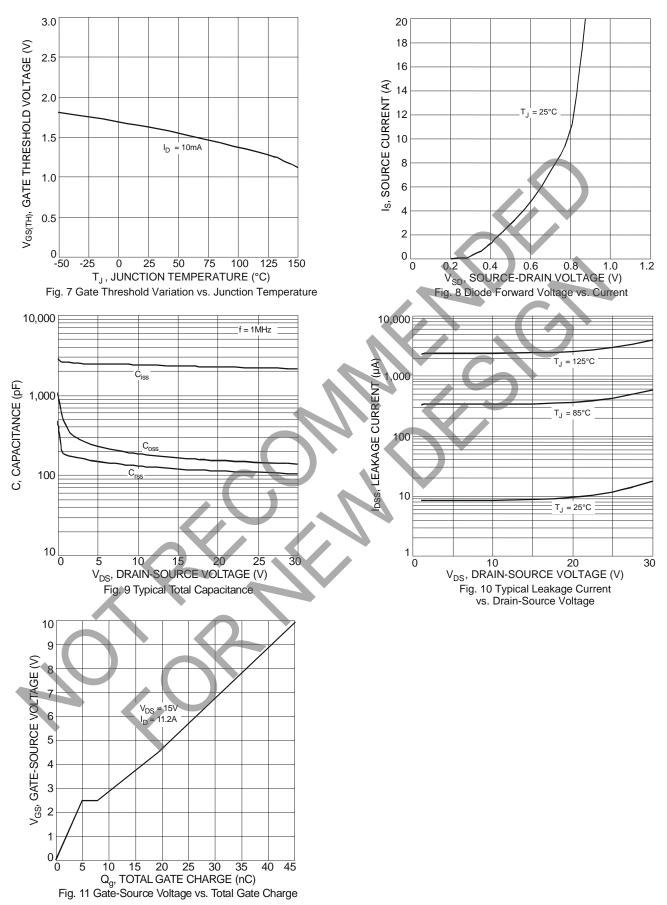


DMG4712SSS Document number: DS32040 Rev. 8 - 3



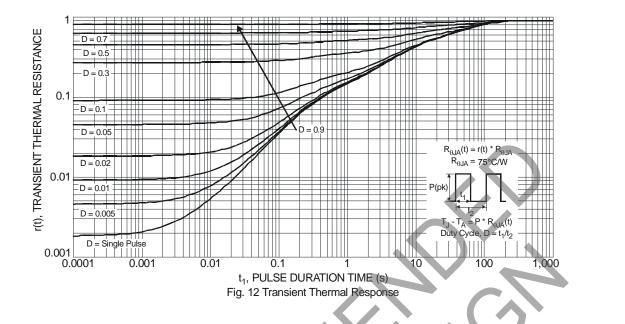
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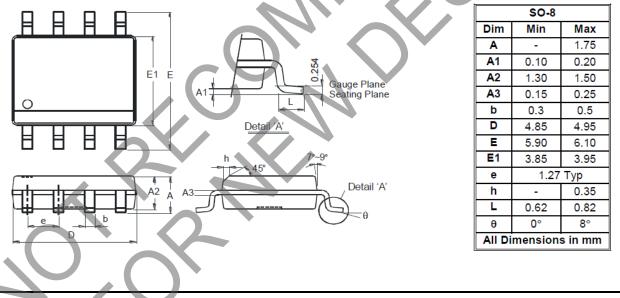


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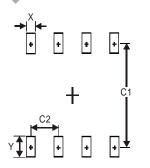
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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