



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	Rds(on)	Ι _D T _A = +25°C
30V	20mΩ @ V _{GS} = 10V	8.0 A
	27mΩ @ V _{GS} = 4.5V	6.5 A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

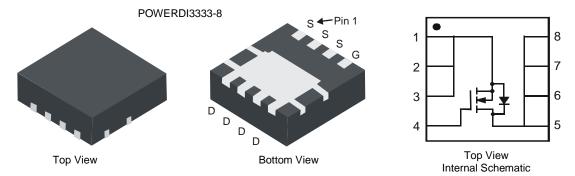
- Backlighting
- **DC-DC Converters**
- **Power Management Functions**

Features

- Low RDS(ON) ensures on state losses are minimized
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8 enabling smaller end product
- 100% UIS (Avalanche) rated
- 100% Rg tested
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: POWERDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 3
- Weight: 0.072 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMG7410SFG-7	POWERDI3333-8	2,000/Tape & Reel
DMG7410SFG-13	POWERDI3333-8	3,000/Tape & Reel

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied. 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

Notes:

and Lead-free 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



G74 = Product Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 10 for 2010) WW = Week Code (01 - 53)



N39 = Product Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 10 for 2010) WW = Week Code (01 - 53)

POWERDI is a registered trademark of Diodes Incorporated.



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteri	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	30	V		
Gate-Source Voltage	V _{GSS}	±25	V		
Continuous Drain Current (Note 5) V_{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Ι _D	5.3 4.2	А
Continuous Drain Current (Note 6) V_{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	8.0 6.3	А
Continuous Drain Current (Note 6) V _{GS} = 10V	t ≤ 10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Ι _D	9.5 7.7	A
Continuous Drain Current (Note 6) V_{GS} = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	6.5 4.9	A
Continuous Drain Current (Note 6) V_{GS} = 4.5V	t ≤ 10s	T _A = +25°C T _A = +70°C	Ι _D	7.8 6.2	A
Pulsed Drain Current (Note 7)			I _{DM}	70	A
Avalanche Current (Notes 7 & 8)	I _{AR}	18	А		
Repetitive Avalanche Energy (Notes 7 & 8) L = 0.1mH			E _{AR}	16	mJ

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	PD	1.0	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	R _{0JA}	130.6	°C/W
Power Dissipation (Note 6)	PD	2.07	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 6)	R _{0JA}	62.5	°C/W
Power Dissipation (Note 6) t \leq 10s	PD	3.0	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 6) t \leq 10s	R _{0JA}	43.8	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

 Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
Device mounted on 2" x 2" FR-4 PCB with high coverage 2 oz. Copper, single sided.
Repetitive rating, pulse width limited by junction temperature. Notes:

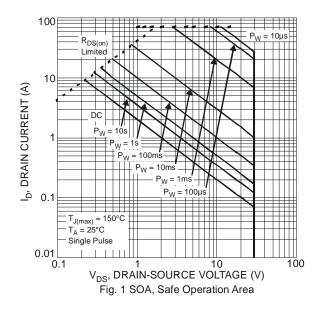
8. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep T_J = +25°C.

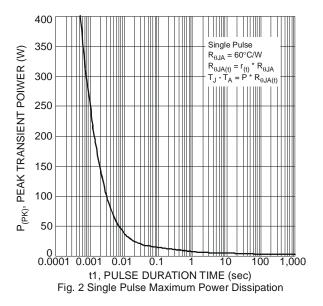


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

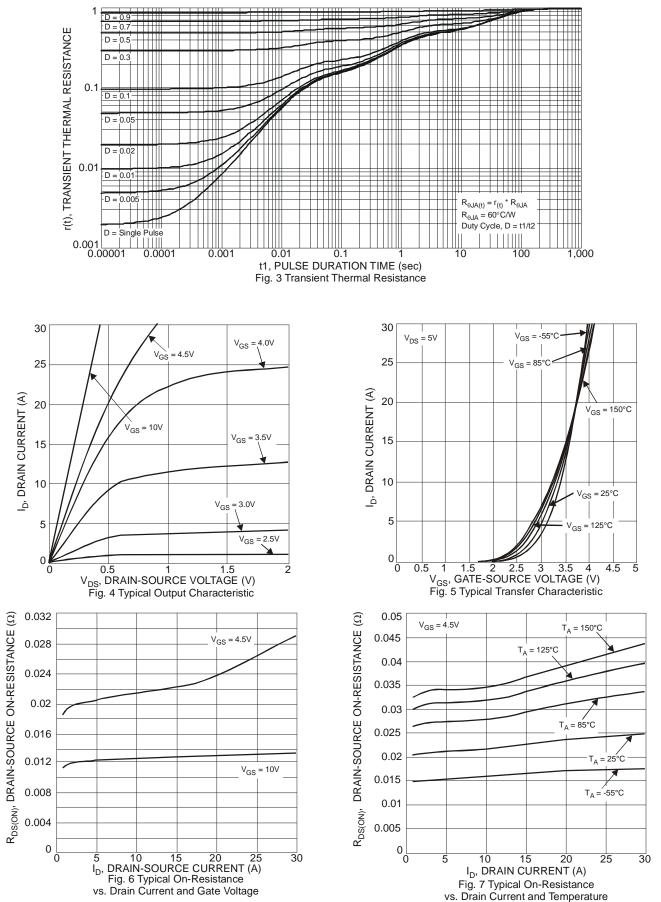
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	-	0.1	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	-	-	±100	nA	$V_{GS} = \pm 25V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V _{GS(th)}	0.8	1.2	2.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	D	-	13.5	20	mΩ	$V_{GS} = 10V, I_D = 10A$	
	R _{DS (ON)}	-	22	27	11122	$V_{GS} = 4.5V, I_D = 7.5A$	
Forward Transfer Admittance	Y _{fs}	-	13.0	-	S	$V_{DS} = 5V, I_D = 10A$	
Diode Forward Voltage	V _{SD}	-	0.7	1.0	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	Ciss	-	580	-		V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	-	110	-	pF		
Reverse Transfer Capacitance	Crss	-	70	-			
Gate Resistance	Rg	-	2.0	3.0	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge V _{GS} = 4.5V	Qg	-	5.3	-		$\label{eq:VGS} \begin{split} & V_{GS} = 4.5V, V_{DS} = 15V, I_{D} = 10A \\ & V_{GS} = 10V, V_{DS} = 15V, \\ & I_{D} = 10A \end{split}$	
Total Gate Charge V _{GS} = 10V	Qg	-	11.3	-	nC		
Gate-Source Charge	Q _{gs}	-	1.9	-	nc		
Gate-Drain Charge	Q _{gd}	-	1.9	-			
Turn-On Delay Time	t _{D(on)}	-	4.4	-	ns		
Turn-On Rise Time	tr	-	4.6	-	ns	$\label{eq:VGS} \begin{array}{l} V_{\text{GS}} = 10V, \ V_{\text{DS}} = 15V, \\ R_{\text{L}} = 15\Omega, \ R_{\text{G}} = 6\Omega \end{array}$	
Turn-Off Delay Time	t _{D(off)}	-	19.5	-	ns		
Turn-Off Fall Time	t _f	-	5.8	-	ns		
Bodyy Diode Reverse Recovery Time	t _{rr}	-	12.6	-	ns		
Body Diode Reverse Recovery Charge	Q _{rr}	-	10.5	-	nC	−IF=8A, di/dt=500A/µs	

Notes: 9. Short duration pulse test used to minimize self-heating effect. 10. Guaranteed by design. Not subject to production testing.

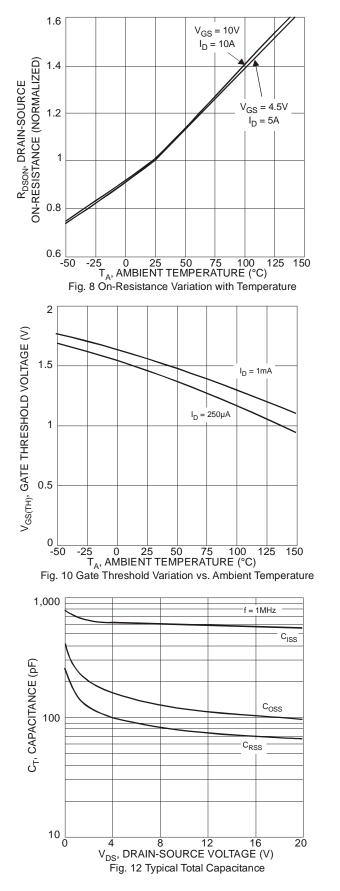


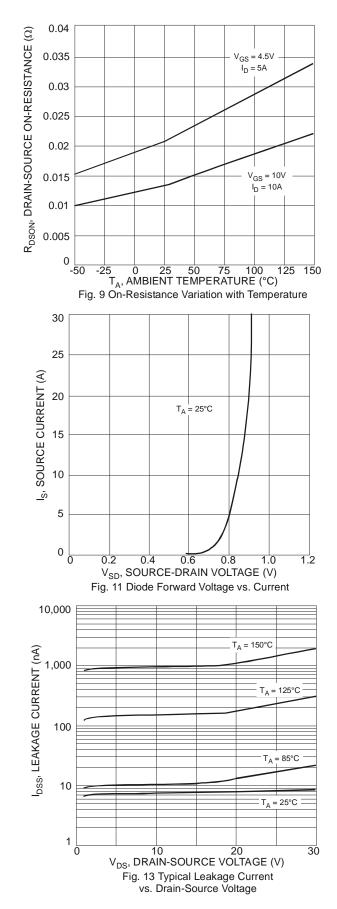








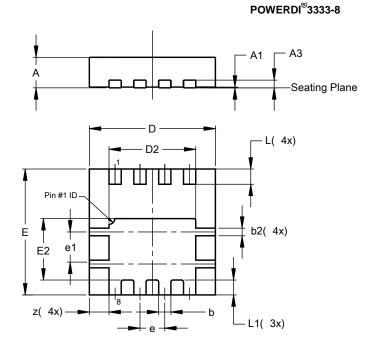






Package Outline Dimensions

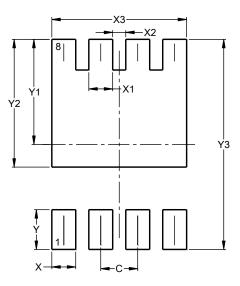
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



P	POWERDI [®] 3333-8					
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	-	-	0.203			
b	0.27	0.37	0.32			
b2	_	_	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
Е	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
е	-	-	0.65			
e1	0.79	0.89	0.84			
L	0.35	0.45	0.40			
L1	-	-	0.39			
Z	_	-	0.515			
	All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



POWERDI[®]3333-8

Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
X3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700



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