



450V DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON) MAX}	I _D T _A = +25°C
450V	4Ω @ V _{GS} = 10V	0.85A

Description

This new generation complementary MOSFET features low onresistance and fast switching, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

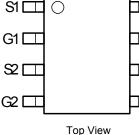
Features

- Low Input Capacitance
- High BVDss Rating for Power Application
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

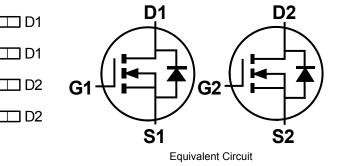
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See diagram below
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.074 grams (approximate)





Pin Configuration



Ordering Information (Note 4)

Top View

Part Number	Compliance	Case	Packaging
DMGD7N45SSD-13	Standard	SO-8	2,500/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

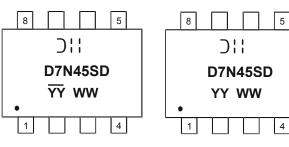
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

Notes:



Chengdu A/T Site

Shanghai A/T Site

) ¦ | = Manufacturer's Marking
D7N45SD = Product Type Marking Code
YYWW = Date Code Marking
YY or YY = Year (ex: 14 = 2014)
WW = Week (01 - 53)
YY = Date Code Marking for SAT (Shanghai Assembly/ Test site)
YY = Date Code Marking for CAT (Chengdu Assembly/ Test site)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	450	V
Gate-Source Voltage			V _{GSS}	±30	V
	Steady State			0.5	٨
Continuous Drain Current (Note 5) V _{GS} = 10V		t < 10s	I _D	0.62	A
		t < 1s		0.85	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	2.2	A
Maximum Body Diode Forward Current (Note 5)			I _S	1.7	A
Avalanche Current (Note 6)		L = 60mH	l	1.4	А
	L =	10mH (Note 8)	I _{AS}	2.2	
Avalanche Energy (Note 6)		L = 60mH	F	56	mJ
Avalatione Energy (Note 0)	L =	10mH (Note 8)	E _{AS}	25	IIIJ

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		PD	1.64	W
Thermal Decisionan Junction to Ambient (Note 5)	Steady state	D	78	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	R _{OJA}	20.2	°C/W
Thermal Resistance, Junction to Case (Note 5)		R _{ØJC}	13.3	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

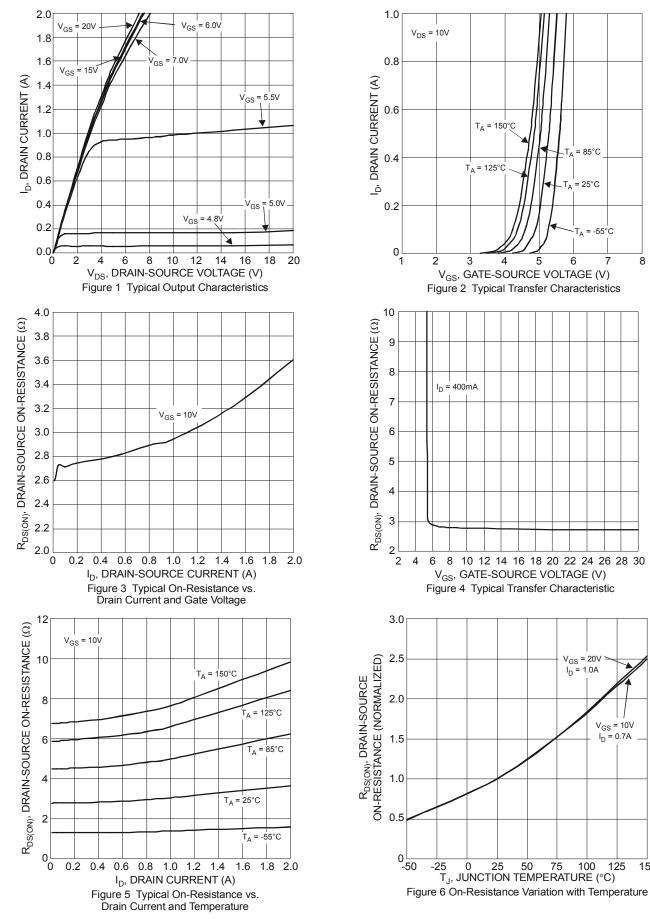
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	<u> </u>						
Drain-Source Breakdown Voltage	BV _{DSS}	450	_	—	V	$V_{GS} = 0V, I_D = 10mA$	
Zero Gate Voltage Drain Current	I _{DSS}		—	1	μA	V_{DS} = 450V, V_{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	V_{GS} = ±30V, V_{DS} = 0V	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	3.5		4.5	V	V _{DS} =10V I _D = 1mA	
Static Drain-Source On-Resistance	R _{DS (ON)}	_	3	4	Ω	V _{GS} = 10V, I _D = 0.4A	
Forward Transfer Admittance	Y _{fs}	0.55	1.1	—	S	V _{DS} = 10V, I _D =0.4A	
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 0.7A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}		256	—		V _{DS} = 25V, V _{GS} = 0V f = 1MHz	
Output Capacitance	C _{oss}	_	22.5	—	pF		
Reverse Transfer Capacitance	C _{rss}	_	0.83	—			
Gate Resistance	R _G	_	2.3	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz	
Total Gate Charge (V _{GS} = 10V)	Qg	_	6.9	—			
Gate-Source Charge	Q _{gs}	_	1.4	—	nC	V _{DS} = 360V,I _D = 0.7A, V _{GS} = 10V	
Gate-Drain Charge	Q _{gd}	_	3.4	_			
Turn-On Delay Time	t _{D(on)}	_	7	_			
Turn-On Rise Time	tr	_	6.4	—	nS	V_{GS} = 10V, R _L = 562 Ω , R _G = 10 Ω , I _D = 0.4A	
Turn-Off Delay Time	t _{D(off)}	_	18.9	_	115		
Turn-Off Fall Time	tf	_	56.6	_]		
Body Diode Reverse Recovery Time	trr	_	103	_	nS	1 - 10 d / d = 1000 / u = 1000	
Body Diode Reverse Recovery Charge	Q _{rr}		314	_	nC	−I _F = 1A, dI/dt = 100A/μs	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

6. I_{AR} and E_{AR} rating are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$. 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.

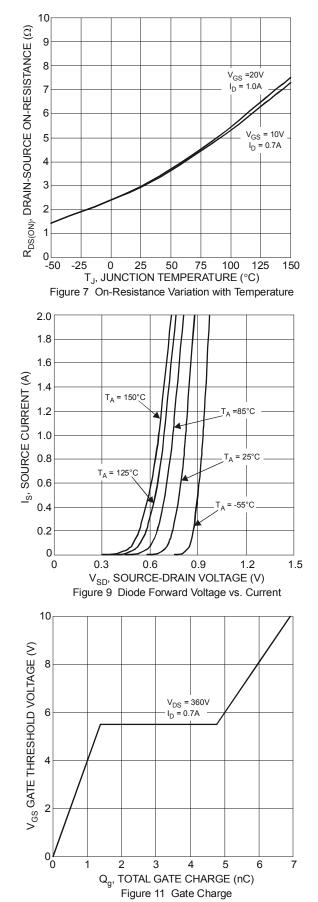


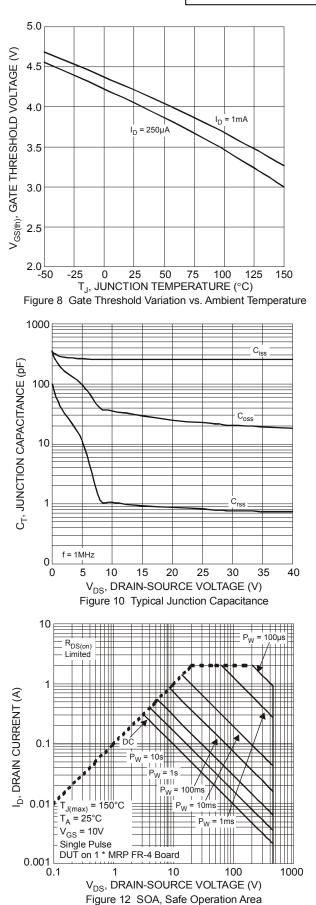
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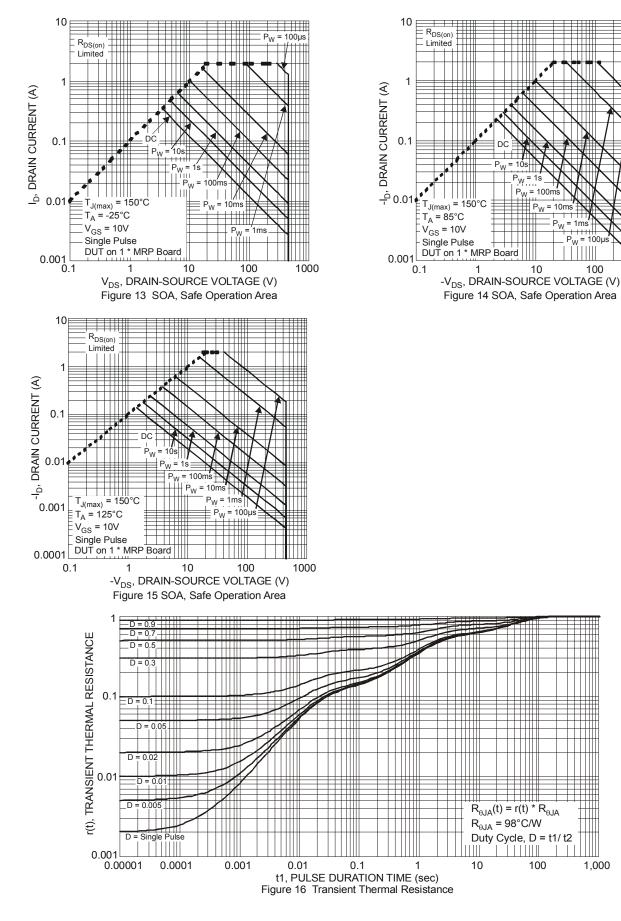








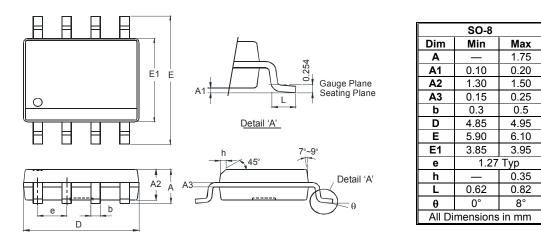
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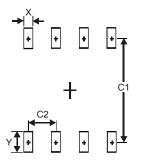
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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