

12V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8 (Type UX)

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C		
40)/	$3.8 m\Omega$ @ $V_{GS} = 4.5 V$	70A		
12V	$5.1 \text{m}\Omega$ @ $V_{GS} = 2.5 \text{V}$	55A		

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Power Management Functions
- DC-DC Converters
- Battery

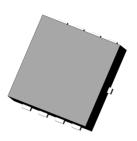
Features

- Low R_{DS(ON)} Ensures On-State Losses are Minimized
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

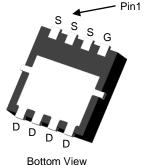
Mechanical Data

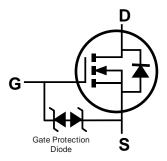
- Case: PowerDI[®]3333-8 (Type UX)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.072 grams (Approximate)





Top View





Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN1004UFV-7	PowerDI3333-8 (Type UX)	2,000/Tape & Reel
DMN1004UFV-13	PowerDI3333-8 (Type UX)	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



UF4 = Product Type Marking Code

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 16 = 2016)

WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	12	V	
Gate-Source Voltage		V_{GSS}	±8	V
Continuous Drain Current, V _{GS} = 4.5V (Note 7)	$T_{C} = +25^{\circ}C$	I _D	70	Α
Continuous Drain Current, V _{GS} = 4.5V (Note 7)	T _C = +70°C		50	
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)		I _{DM}	80	Α
Maximum Continuous Body Diode Forward Current (Note 7)		Is	70	Α
Avalanche Current, L = 0.1mH (Note 8)		I _{AS}	34	Α
Avalanche Energy, L = 0.1mH (Note 8)		E _{AS}	60	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		P_{D}	0.9	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	134	°C/W
Total Power Dissipation (Note 6)		P_D	1.9	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	66	°C/W
Thermal Resistance, Junction to Case (Note 7)		$R_{ heta JC}$	3.4	C/VV
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

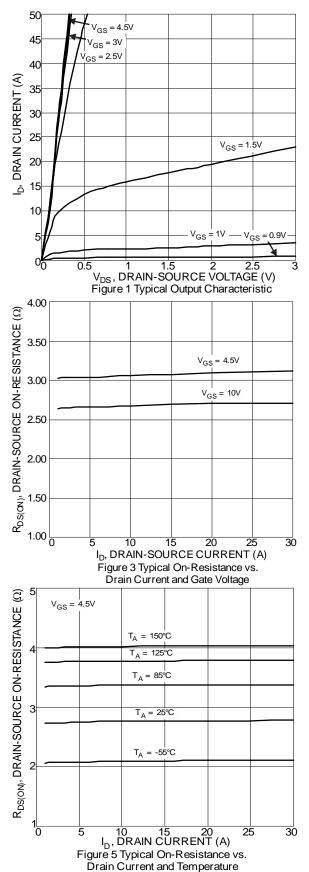
Characteristic	Symbol	Min	Typ Max Unit		Test Condition		
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV _{DSS}	12	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	V _{DS} = 9.6V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V _{GS(TH)}	0.3	_	1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	2.8	3.8	mΩ	$V_{GS} = 4.5V, I_D = 15A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	3.2	5.1	11177	$V_{GS} = 2.5V, I_D = 10A$	
Diode Forward Voltage	V_{SD}	_	0.75	1.2	V	$V_{GS} = 0V, I_S = 3.2A$	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C _{iss}	_	2,385		pF)/ 0)/)/ 0)/	
Output Capacitance	Coss		678	_	pF	$V_{DS} = 6V$, $V_{GS} = 0V$, - $f = 1MHz$	
Reverse Transfer Capacitance	C _{rss}	_	520	_	pF		
Gate Resistance	R_{G}	_	2.2	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Q_{G}	_	26		nC		
Total Gate Charge (V _{GS} = 8V)	Q_{G}	_	47	_	nC	6V I 10A	
Gate-Source Charge	Q _{GS}	_	2.8	_	nC	$V_{DS} = 6V, I_{D} = 10A$	
Gate-Drain Charge	Q_{GD}	_	5.3	_	nC	1	
Turn-On Delay Time	t _{D(ON)}	_	5.3		ns		
Turn-On Rise Time	t _R	_	10.7	_	ns	$V_{DD} = 6V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t _{D(OFF)}	_	31.6	_	ns	$R_G = 1\Omega$, $I_D = 5A$	
Turn-Off Fall Time	t _F	_	16.9	-	ns	1	
Reverse Recovery Time	t _{RR}	_	24.3	_	ns	1 00 1:/14 1000/	
Reverse Recovery Charge	Q_{RR}	_	7.4	_	nC	I _F = 2A, di/dt = 100A/μs	

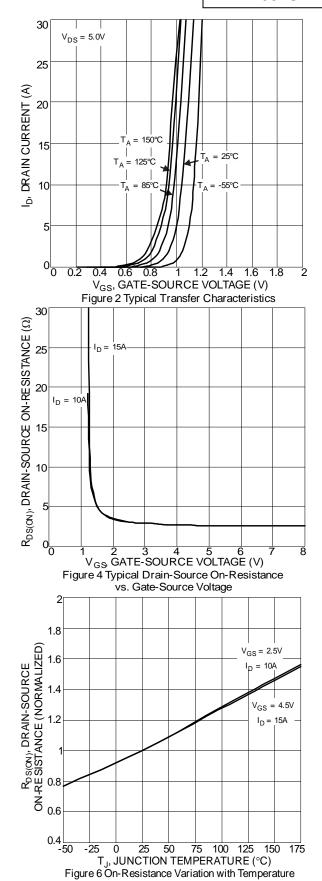
Notes:

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
- 7. Thermal resistance from junction to soldering point (on the exposed drain pad).
- 8. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_{J} = +25°C.
- 9. Short duration pulse test used to minimize self-heating effect.
- 10. Guaranteed by design. Not subject to product testing.



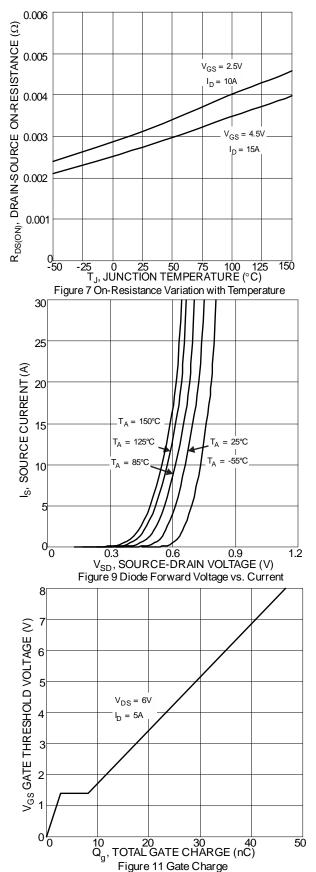


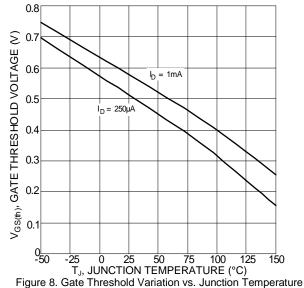


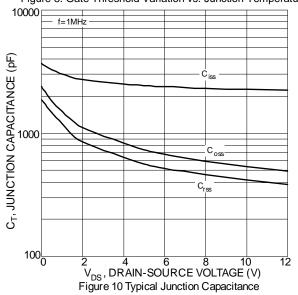


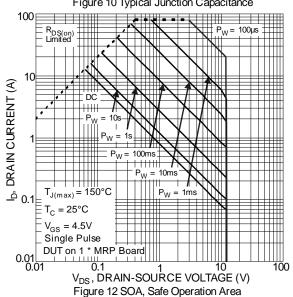




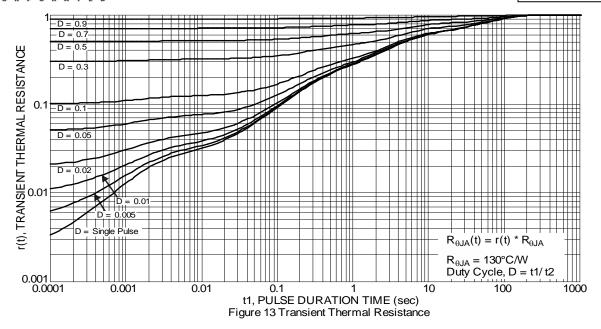










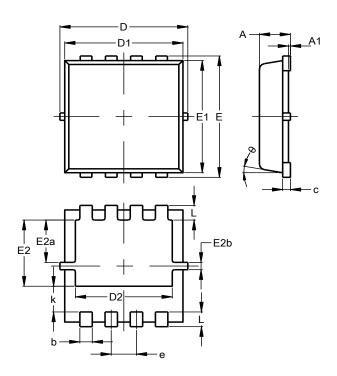




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type UX)

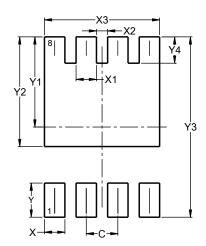


PowerDI3333-8						
(Type UX)						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05				
b	0.25	0.40	0.32			
С	0.10	0.25	0.15			
D	3.20	3.40	3.30			
D1	2.95	3.15	3.05			
D2	2.30	2.70	2.50			
Е	3.20	3.40	3.30			
E1	2.95	3.15	3.05			
E2	1.60	2.00	1.80			
E2a	0.95	1.35	1.15			
E2b	0.10	0.30	0.20			
е	0.65 BSC					
k	0.50	0.90	0.70			
L	0.30	0.50	0.40			
θ	0°	12°	10°			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (Type UX)



Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
Х3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540



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