

#### 100V N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

BV <sub>DSS</sub>	RDS(ON) max	I <sub>D max</sub> T <sub>A</sub> = +25°C
100V	220mΩ @ V <sub>GS</sub> = 10V	1.6A
100 V	250mΩ @ V <sub>GS</sub> = 4.5V	1.3A

## **Description and Applications**

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

Load Switch

## **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

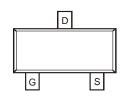
#### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.009 grams (Approximate)

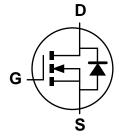
SOT23







Pin Configuration



**Equivalent Circuit** 

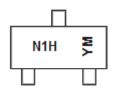
### **Ordering Information** (Note 5)

Part Number	Case	Packaging
DMN10H220LQ-7	SOT23	3,000/Tape & Reel
DMN10H220LQ-13	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product\_compliance\_definitions.html.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**



N1H = Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Key

Year	201	6	2017		2018	20	19	2020		2021	- 2	2022
Code	D		E		F	(	3	Н		l		J
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
0 - 1 -		_	_			_	_			_		



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Units		
Drain-Source Voltage		$V_{DSS}$	100	V		
Gate-Source Voltage		$V_{GSS}$	±16	V		
Continuous Prain Current (Note 6) / 40/	(Note 7)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	1.6 1.3	А	
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	(Note 6)	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	1.4 1.1	А	
Maximum Continuous Body Diode Forward Current	(Note 7)	Is	0.6	Α		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	8	А			

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 7)	$T_A = +25^{\circ}C$	C	1.3	W	
Total Power Dissipation (Note 7)	$T_A = +70^{\circ}C$	$P_{D}$	0.8		
Thermal Resistance, Junction to Ambient	(Note 6)	D	94	°C/W	
Thermal Resistance, Junction to Ambient	(Note 7)	$R_{\theta JA}$	177		
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C	

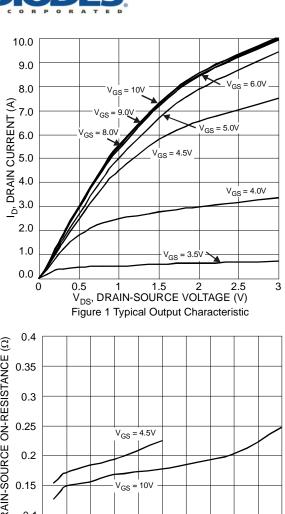
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

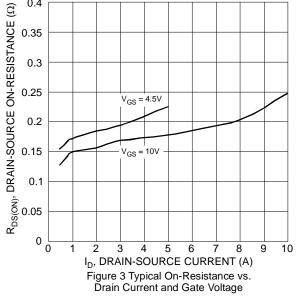
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	100	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 100V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	_	2.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance			_	220	mΩ	$V_{GS} = 10V, I_D = 1.6A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	_	250	11177	$V_{GS} = 4.5V, I_D = 1.3A$	
Diode Forward Voltage	V <sub>SD</sub>		0.7	1.2	V	$V_{GS} = 0V, I_{S} = 1.1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C <sub>iss</sub>		401	_		.,	
Output Capacitance	Coss		22	_	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	17	_		1 = 1101112	
Gate Resistance	$R_g$	_	2.1	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	$Q_g$	_	4.1	_			
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	8.3	_	nC	V 50V L 4.0A	
Gate-Source Charge	Qgs	_	1.5	_	IIC	$V_{DS} = 50V, I_D = 1.6A$	
Gate-Drain Charge	$Q_{gd}$	_	2	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	6.8	_			
Turn-On Rise Time	t <sub>R</sub>	-	8.2	_		$V_{DS} = 50V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>		7.9	_	ns	$R_G = 6.8\Omega, I_D = 1A$	
Turn-Off Fall Time	t <sub>F</sub>		3.6	_			
Reverse Recovery Time	t <sub>RR</sub>		17	_	ns	1 4 4 4 4 11/11 4 400 4 / 1 -	
Reverse Recovery Charge	Q <sub>RR</sub>	_	9.8	_	$I_F = 1.1A$ , di/dt =100A/ $\mu$ s		

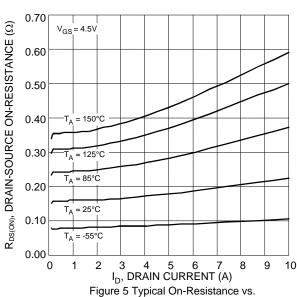
Notes:

- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
  8. Short duration pulse test used to minimize self-heating effect.
  9. Guaranteed by design. Not subject to production testing.

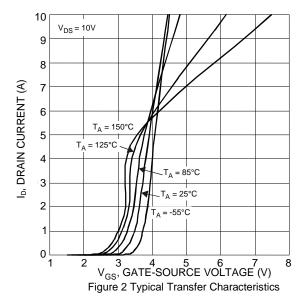


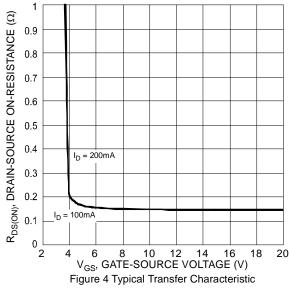






Drain Current and Temperature





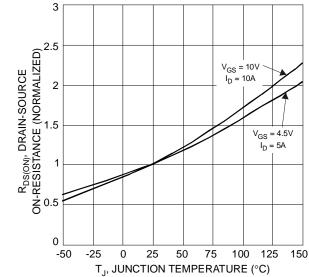
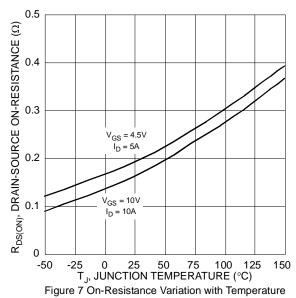
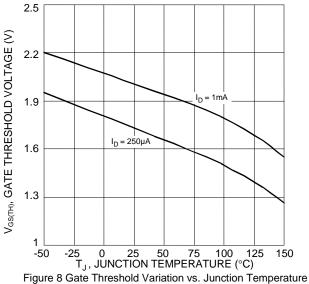


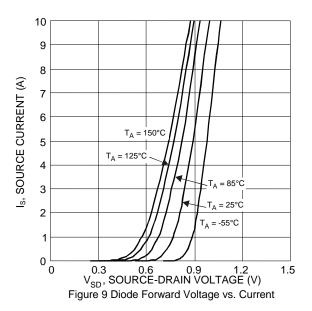
Figure 6 On-Resistance Variation with Temperature

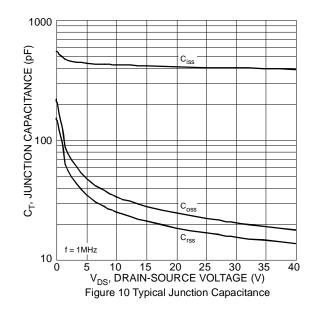
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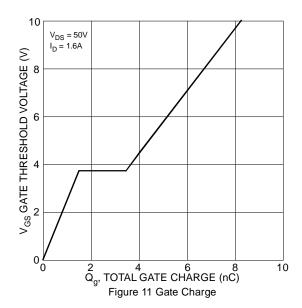




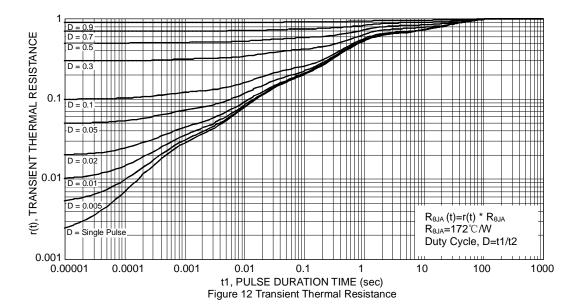










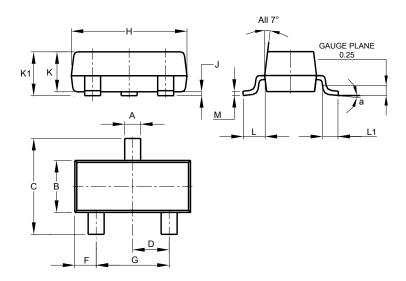




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23

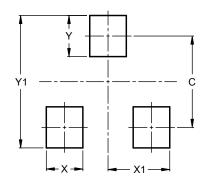


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All Dimensions in mm							

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23



Dimensions	Value (in mm)
С	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9



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