



Product Summary

| Device | BV _{DSS} | R _{DS(ON)} | Ι _D T _A = +25°C |
|-----------|-------------------|-------------------------------|--|
| N-Channel | 20V | 35mΩ @ V _{GS} = 4.5V | 4.6A |
| N-Channel | 200 | 43mΩ @ V _{GS} = 2.5V | 4.2A |

Description

This new generation MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Backlighting
- DC-DC Converters
- Power Management Functions



| G1 1 | • | 6 D1 |
|------|--------|------|
| S2 2 | | 5 S1 |
| G2 3 | | 4 D2 |
| ' | TSOT26 | 1 |

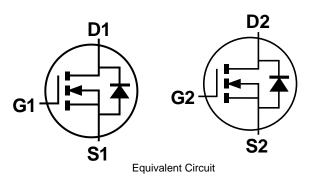
N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.013 grams (Approximate)



Ordering Information (Note 4)

| Part Number | Case | Packaging |
|---------------|--------|----------------------|
| DMN2053UVT-7 | TSOT26 | 3000 / Tape & Reel |
| DMN2053UVT-13 | TSOT26 | 10,000 / Tape & Reel |

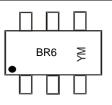
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



BR6= Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: F = 2018) M or \overline{M} = Month (ex: 9 = September)

Date Code Key

Notes:

| Duie Obue hey | | | | | | | | | | | | |
|---------------|------|-----|------|------|-----|------|------|-----|------|------|-----|------|
| Year | 2018 | | 2019 | 2020 | | 2021 | 2022 | | 2023 | 2024 | | 2025 |
| Code | F | | G | Н | | | J | | K | L | | М |
| Month | Jan | Feb | Mar | Apr | Мау | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D |



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

| Characteristic | Symbol | Q1 Value | Unit | | |
|--|------------------|----------|----------------|------------|---|
| Drain-Source Voltage | V _{DSS} | 20 | V | | |
| Gate-Source Voltage | V _{GSS} | ±12 | V | | |
| Continuous Drain Current (Note 6) $V_{GS} = 4.5V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$ | | | Ι _D | 4.6 3.7 | А |
| Maximum Continuous Body Diode Forward Current (Note | IS | 1.4 | А | | |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | IDM | 22 | A | | |

Thermal Characteristics

| Characteristic | | Symbol | Value | Unit |
|--|------------------------|----------------------------------|-------------|------|
| Total Power Dissipation (Note 5) | T _A = +25°C | PD | 0.7 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | Steady State | $R_{	heta JA}$ | 173 | °C/W |
| Total Power Dissipation (Note 6) | T _A = +25°C | PD | 1.1 | W |
| Thermal Resistance, Junction to Ambient (Note 6) | Steady State | $R_{	heta JA}$ | 108 | °C/W |
| Thermal Resistance, Junction to Case | | $R_{	ext{	heta}JC}$ | 37 | C/VV |
| Operating and Storage Temperature Range | | T _{J,} T _{STG} | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

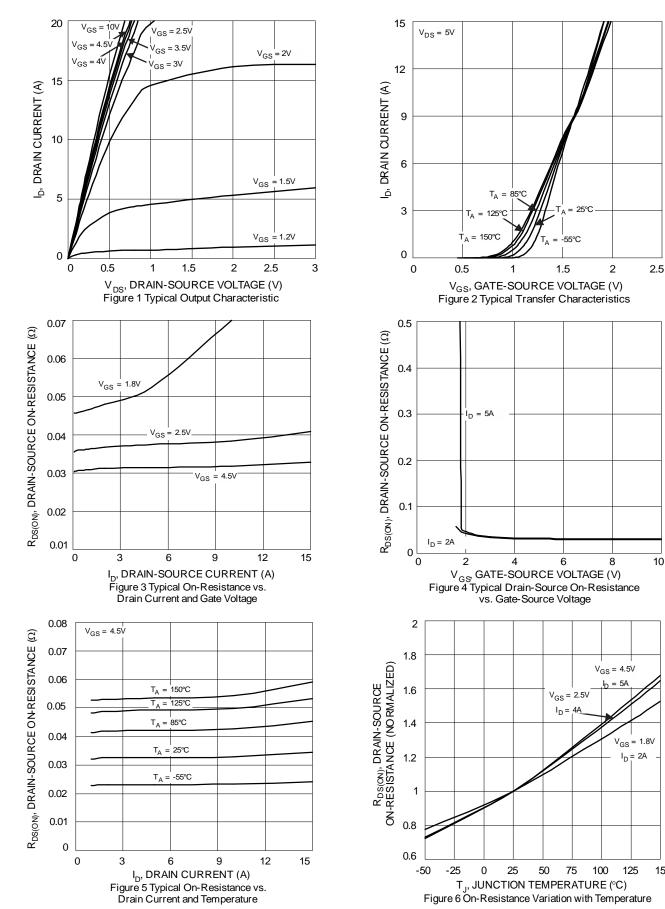
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | | | |
|---------------------------------------|---------------------|-----|------|------|------------------------------|---|--|--|--|
| OFF CHARACTERISTICS (Note 7) | | | | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 20 | — | | V | $V_{GS} = 0V, I_D = 250 \mu A$ | | | |
| Zero Gate Voltage Drain Current | I _{DSS} | _ | — | 1.0 | μA | $V_{DS} = 20V, V_{GS} = 0V$ | | | |
| Gate-Source Leakage | Igss | _ | _ | ±100 | nA | $V_{GS} = \pm 12V, V_{DS} = 0V$ | | | |
| ON CHARACTERISTICS (Note 7) | | | | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | 0.4 | _ | 1.0 | V | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | | | |
| | | | | 35 | | $V_{GS} = 4.5V, I_D = 5.0A$ | | | |
| Static Drain-Source On-Resistance | R _{DS(ON)} | — | — | 43 | mΩ | $V_{GS} = 2.5V, I_D = 4.0A$ | | | |
| | | | | 56 | | $V_{GS} = 1.8V, I_D = 2.0A$ | | | |
| Diode Forward Voltage | V _{SD} | — | 0.7 | 1.2 | V | $V_{GS} = 0V, I_{S} = 1A$ | | | |
| DYNAMIC CHARACTERISTICS (Note 8) | | | | | | | | | |
| Input Capacitance | Ciss | — | 369 | | | | | | |
| Output Capacitance | C _{oss} | — | 54 | _ | pF | $V_{DS} = 10V, V_{GS} = 0V$ f = 1.0MHz | | | |
| Reverse Transfer Capacitance | Crss | — | 32 | _ | | T = T.000172 | | | |
| Gate Resistance | Rg | _ | 4.1 | | Ω | $V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$ | | | |
| Total Gate Charge ($V_{GS} = 4.5V$) | Qg | — | 3.6 | _ | | | | | |
| Gate-Source Charge | Q _{gs} | _ | 0.4 | _ | nC | $V_{GS} = 4.5V$, $V_{DS} = 10V$, $I_D = 6A$ | | | |
| Gate-Drain Charge | Q _{gd} | _ | 1.0 | | | | | | |
| Turn-On Delay Time | t _{D(ON)} | _ | 2.6 | _ | | | | | |
| Turn-On Rise Time | t _R | _ | 3.0 | | $V_{DS} = 10V, V_{GS} = 5V,$ | | | | |
| Turn-Off Delay Time | t _{D(OFF)} | — | 12.5 | | ns | $R_G = 6\Omega$, $I_D = 6A$ | | | |
| Turn-Off Fall Time | tF | _ | 3.6 | | | | | | |
| Reverse Recovery Time | t _{RR} | — | 6.0 | | ns | | | | |
| Reverse Recovery Charge | Q _{RR} | _ | 0.9 | | nC | I _F = 1A, di/dt = 100A/μs | | | |

Notes:

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing.

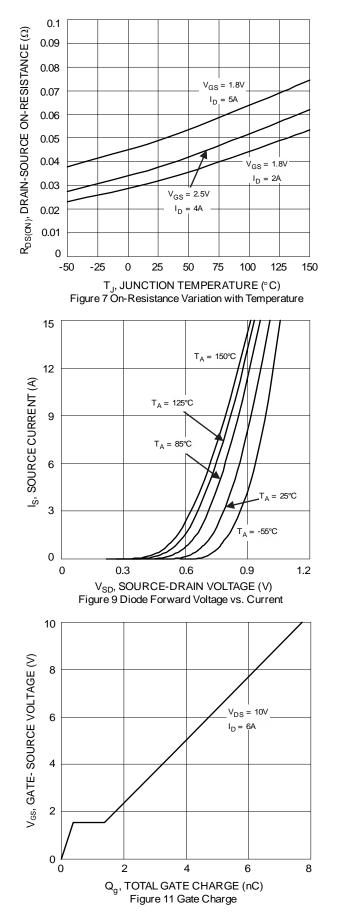


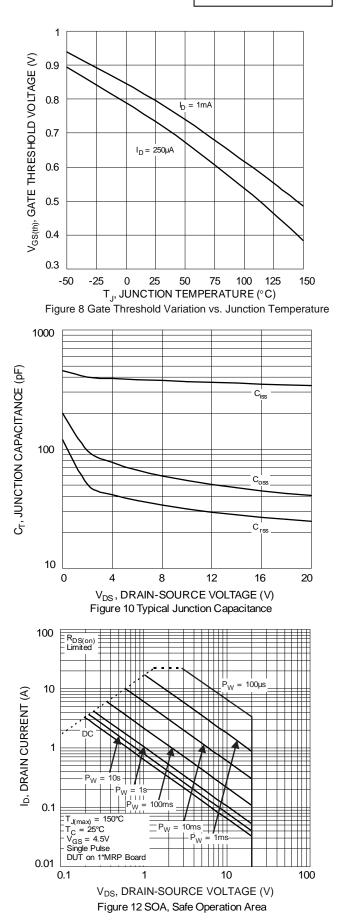


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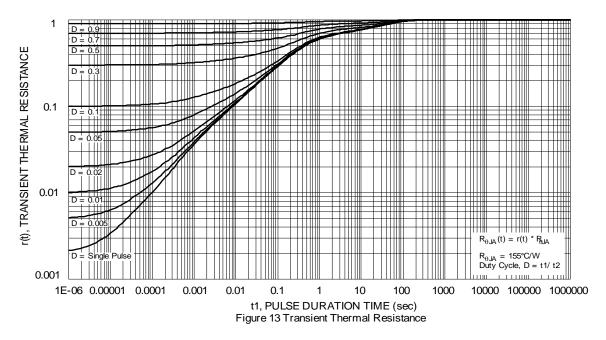






DMN2053UVT Document number: DS40904 Rev. 2 - 2

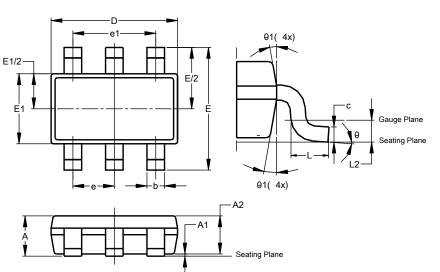






Package Outline Dimensions

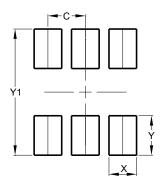
Please see http://www.diodes.com/package-outlines.html for the latest version.



| | TS | OT26 | | | |
|-----|-----------|-------------|-------|--|--|
| Dim | Min | Min Max Typ | | | |
| Α | | 1.00 | — | | |
| A1 | 0.010 | 0.100 | — | | |
| A2 | 0.840 | 0.900 | — | | |
| D | 2.800 | 3.000 | 2.900 | | |
| Е | 2 | .800 BS | С | | |
| E1 | 1.500 | 1.700 | 1.600 | | |
| b | 0.300 | 0.450 | _ | | |
| С | 0.120 | 0.200 | — | | |
| е | 0.950 BSC | | | | |
| e1 | 1 | .900 BS | С | | |
| L | 0.30 | 0.50 | — | | |
| L2 | 0 | .250 BS | С | | |
| θ | 0° | 8° | 4° | | |
| θ1 | 4° | 12° | _ | | |
| A | II Dimen | sions in | mm | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



TSOT26

| Dimensions | Value (in mm) |
|------------|---------------|
| С | 0.950 |
| Х | 0.700 |
| Y | 1.000 |
| Y1 | 3.199 |

TSOT26



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