



20V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
201/	0.45Ω @ V _{GS} = 4.5V	0.9A
20V	0.6Ω @ $V_{GS} = 2.5V$	0.8A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- **Power Management Functions**
- **DC-DC Converters**
- **Analog Switch**

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.027 grams (Approximate)

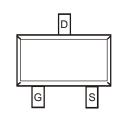




SOT323



Top View



Top View

Equivalent Circuit

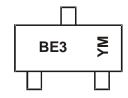
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2710UW-7	SOT323	3,000/Tape & Reel
DMN2710UW-13	SOT323	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



BE3 = Product Type Marking Code YM = Date Code Marking \overline{Y} = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Н	I	J	K	L	М	N	0	Р	R	S	T
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	20	V	
Gate-Source Voltage		V_{GSS}	±6	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	lo	0.9 0.7	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle=1%)	I _{DM}	5	Α	
Maximum Body Diode Forward Current (Note 5)		Is	0.6	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.47	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RеJA	268	°C/W
Total Power Dissipation (Note 6)		P _D	0.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	212	°C/W
Operating and Storage Temperature Range	·	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

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Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)				1	1		
Drain-Source Breakdown Voltage		BVDSS	20	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	$@T_C = +25^{\circ}C$	IDSS	_	_	100	nA	V _{DS} =20V, V _{GS} = 0V
Gate-Source Leakage		I _{GSS}	1	_	±1.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage		Vgs(TH)	0.5	_	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
			l	0.13	0.45		$V_{GS} = 4.5V, I_{D} = 600mA$
Static Drain-Source On-Resistance		RDS(ON)	-	0.16	0.6	Ω	$V_{GS} = 2.5V, I_{D} = 500mA$
				0.22	0.75		$V_{GS} = 1.8V, I_D = 350mA$
Diode Forward Voltage		VsD	_	0.7	1.2	V	V _G S = 0V, I _S = 150mA
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance		Ciss	_	42	_	pF	.,
Output Capacitance		Coss	_	13	_	pF	VDS = 16V, VGS = 0V f = 1.0MHz
Reverse Transfer Capacitance		Crss	_	6.5	_	pF	1 = 1.0WHZ
Total Gate Charge		Q_g	-	0.6	_	nC	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
Gate-Source Charge		Qgs	_	0.1	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge		Q_{gd}	_	0.1	_	nC	I _D = 250mA
Turn-On Delay Time		td(ON)	_	4.9	_	ns	101/1/ 451/
Turn-On Rise Time	,		_	3.1	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time		tD(OFF)		386	_	ns	$R_L = 47\Omega$, $R_g = 10\Omega$
Turn-Off Fall Time		t _F	_	174	_	ns	I _D = 200mA
Reverse Recovery Time		t _{RR}		88	_	ns	I _F = 1.0A, di/dt = 100A/μs
Reverse Recovery Charge		Qrr	_	29	_	nC	I _F = 1.0A, di/dt = 100A/μs

- Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.



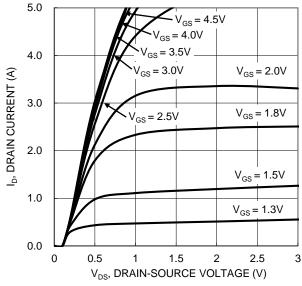


Figure 1. Typical Output Characteristic

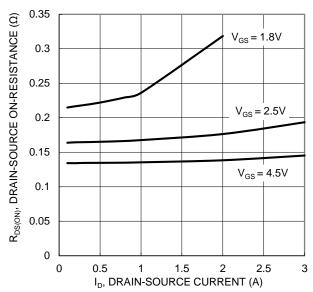


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

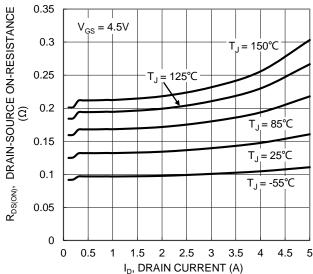
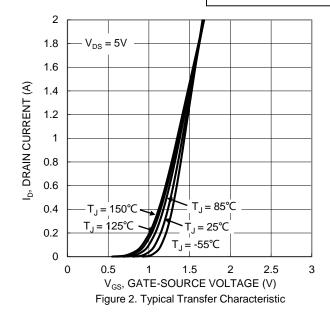
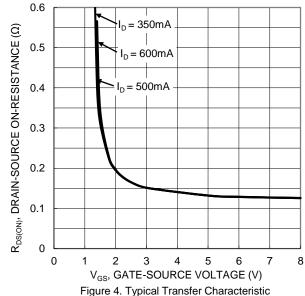


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





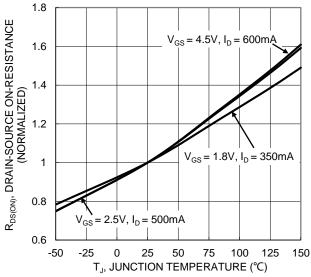


Figure 6. On-Resistance Variation with Temperature



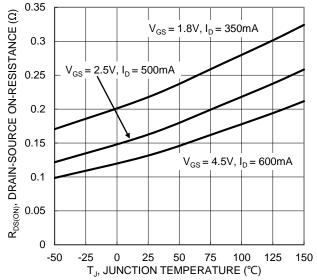
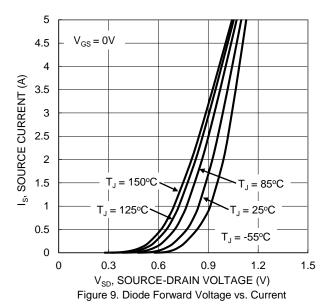


Figure 7. On-Resistance Variation with Temperature



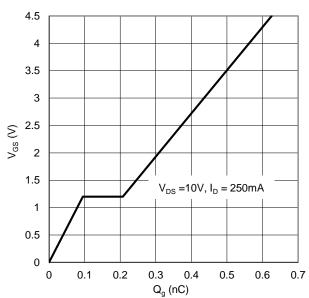


Figure 11. Gate Charge

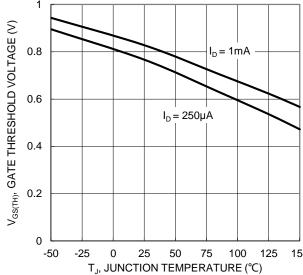
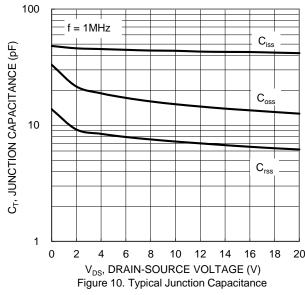


Figure 8. Gate Threshold Variation vs. Junction Temperature



10 $R_{\text{DS}(\text{ON})}$ _w = 100µs Limited = 1ms DRAIN CURRENT (A) $P_W = 100ms$ 0.1 $T_{J(Max)} = 150$ °C T_C = 25 °C Single Pulse DUT on 1*MRP $P_W = 10s$ board DC $V_{GS} = 4.5V$ 0.01 10 0.1 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V)

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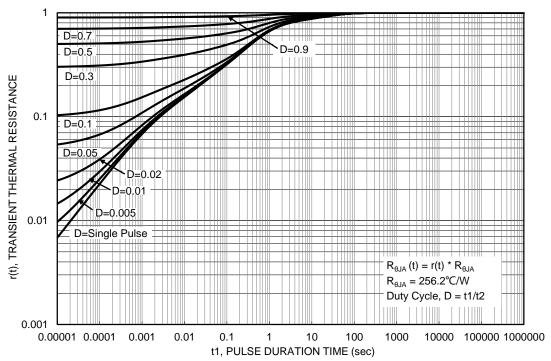


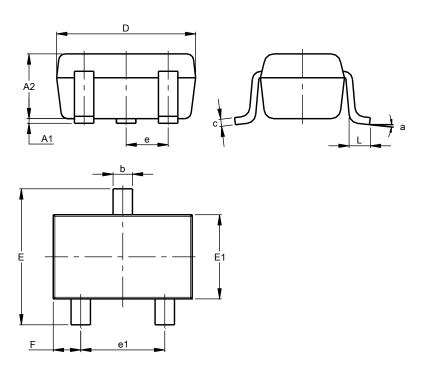
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

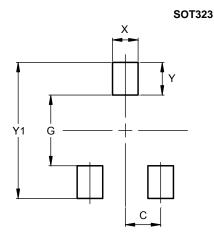
SOT323



SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
С	0.10	0.18	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C).650 B	SC				
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
V1	2 500



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