



Product Summary

BV _{DSS}	Rds(on) Max	I _D Max T _A = +25°C
2017	30mΩ @ V _{GS} = 10V	6.2A
30V	$42m\Omega @ V_{GS} = 4.5V$	5.2A

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Body Control Electronics
- Power Management Functions
- DC-DC Converters

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

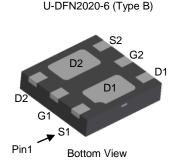
• This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

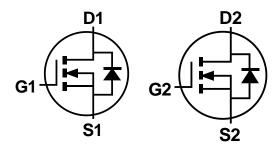
https://www.diodes.com/guality/product-definitions/

 An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DMN3032LFDBQ</u>)

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)





Internal Schematic

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3032LFDB-7	U-DFN2020-6 (Type B)	3,000/Tape & Reel
DMN3032LFDB-13	U-DFN2020-6 (Type B)	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

U-DFN2020-6 (Type B)



N5 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2012		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	Z		Н		J	К	L	М	Ν	0	Р	R
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	1	5	6	7	8	0	0	N	р

Site 2



N5 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2012	 2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	2	 0	1	2	3	4	5	6	7	8	9

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Γ	Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
	Code	Т	U	V	W	Х	Y	Z



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +75°C	ID	6.2 5.0	А
Maximum Continuous Body Diode Forward Curre	nt (Note 6)		ls	2	A
Pulsed Body Diode Forward Current (370µs Pulse	e, Duty Cycl	e = 1%)	I _{SM}	20	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1		ldм	25	A	
Avalanche Current (Note 7) L = 0.1mH		las	12	A	
Avalanche Energy (Note 7) L = 0.1mH		E _{AS}	10	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	1.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	D	127	°C/W
mermai Resistance, Junction to Ambient (Note 5)	Reja	75	C/W	
Total Power Dissipation (Note 6)		Po	1.7	W
Thermal Desistance, Junction to Ambient (Note C)	Steady state	P	72	
Thermal Resistance, Junction to Ambient (Note 6) t<10s		$R_{ heta JA}$	43	°C/W
Thermal Resistance, Junction to Case (Note 6)		Rejc	9	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

	I		· _			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BVDSS	30			V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_		1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$
Zero Gate Voltage Drain Current T _J = +150°C (Note 9)	IDSS	_		100	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	lgss	_	—	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	VGS(TH)	1.0	1.5	2.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
Static Drain-Source On-Resistance	Bacon	—	25	30	mΩ	$V_{GS} = 10V, I_{D} = 5.8A$
Static Drain-Source On-Resistance	RDS(ON)	_	30	42	11122	$V_{GS} = 4.5 V, I_D = 4.8 A$
Diode Forward Voltage	Vsd		0.75	1.2	V	$V_{GS} = 0V$, $I_S = 1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss		500	—	рF	
Output Capacitance	Coss	_	52		pF	V _{DS} = 15V, V _{GS} = 0V, - f = 1.0MHz
Reverse Transfer Capacitance	Crss		44	_	рF	1 - 1.00012
Gate Resistance	Rg	_	2.3	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg		5.0	—	nC	
Total Gate Charge (V _{GS} = 10V)	Qg		10.6	—	nC	V _{DS} = 15V, I _D = 5.8A
Gate-Source Charge	Qgs		1.3	—	nC	VDS = 15V, ID = 5.6A
Gate-Drain Charge	Q _{gd}		1.8	—	nC	
Turn-On Delay Time	tD(ON)		2.2	_	ns	
Turn-On Rise Time	t _R		2.6	_	ns	$V_{DD} = 15V, V_{GS} = 10V,$
Turn-Off Delay Time	tD(OFF)		9.7	_	ns	$R_L = 2.6\Omega, R_G = 3\Omega$
Turn-Off Fall Time	tF		2.0	_	ns	

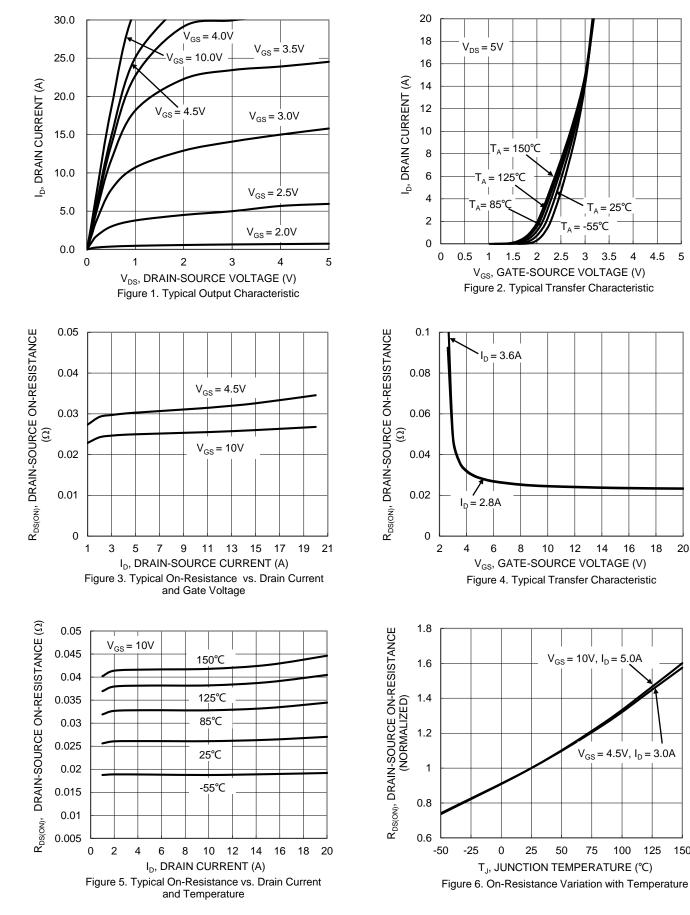
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Operating the plane in the product test in the product test into the plane. Notes:

9. Guaranteed by design. Not subject to product testing.



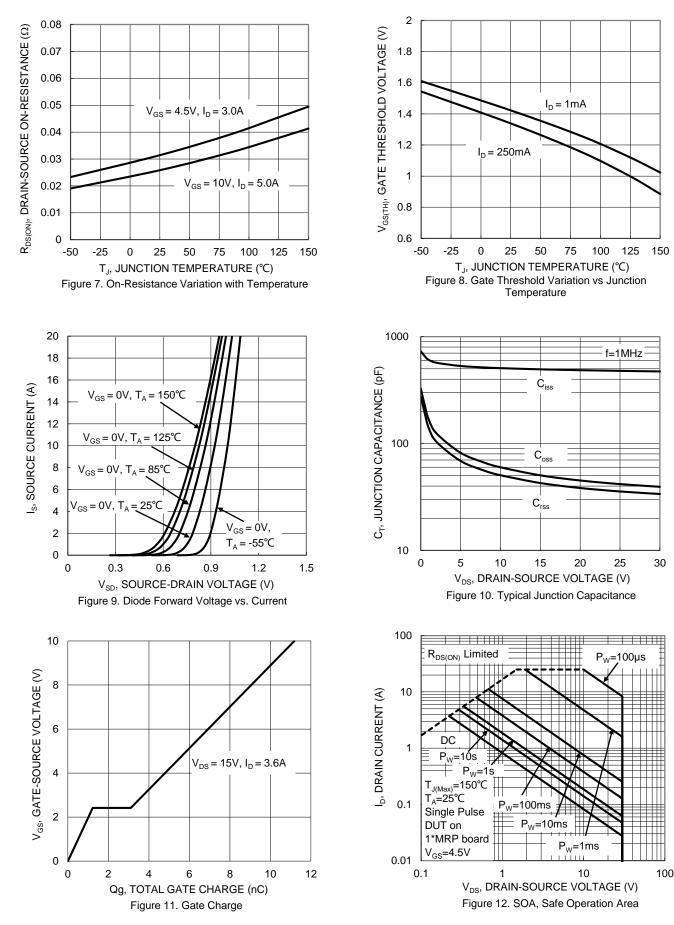
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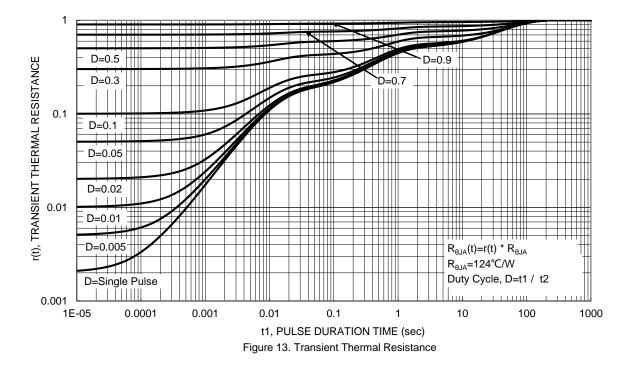
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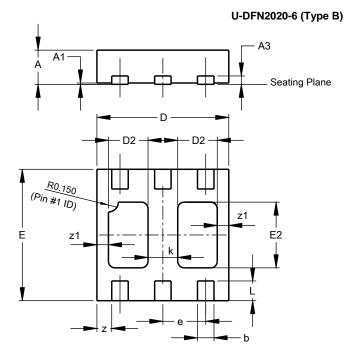






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

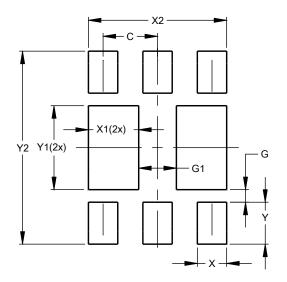


		2020-6 e B	
Dim	Min	Тур	
Α	0.545	0.605	0.575
A1	0.00	0.05	0.02
A3	-	-	0.13
b	0.20	0.30	0.25
D	1.95	2.075	2.00
D2	0.50	0.70	0.60
е	-	-	0.65
E	1.95	2.075	2.00
E2	0.90	1.10	1.00
k	-	-	0.45
L	0.25	0.35	0.30
z	-	-	0.225
z1	-	-	0.175
All	Dimens	ions in	mm

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type B)



Dimensions	Value (in mm)
С	0.650
G	0.150
G1	0.450
Х	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
Y2	2.300



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