

Product Summary

BV _{DSS}	R _{DS(ON)}	Package	I _D T _A = +25°C
30V	0.15Ω @ V _{GS} = 4.5V	SOT23	2A
	0.2Ω @ V _{GS} = 2.5V		1.6A
	0.25Ω @ V _{GS} = 1.8V		1.4A
	0.3Ω @ V _{GS} = 1.5V		1.2A

Description

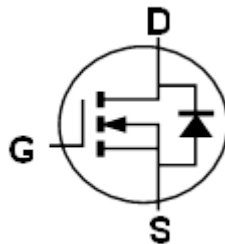
This new generation MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

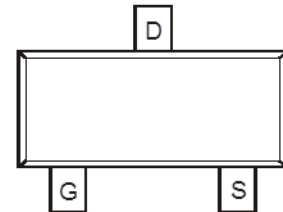
- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays
- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.



Top View



Equivalent Circuit



Top View

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen- and Antimony-Free. "Green" Device (Note 3)**
- **The DMN3300UQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

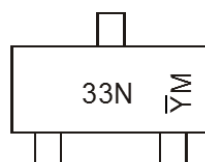
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Lead-Frame. Solderable per MIL-STD-202, Method 208 Ⓔ3
- Weight: 0.008 grams (Approximate)

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3300UQ-7	SOT23	3,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



SOT23

33N = Marking Code
 YM = Date Code Marking
 Y = Year (ex: H = 2020)
 M = Month (ex: 9 = September)

Date Code Key

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	H	I	J	K	L	M	N	O	P	R	S	T
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V_{GSS}	± 12	V
Continuous Drain Current (Note 5) $V_{GS} = 4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$	I_D	1.5	A
		$T_A = +70^\circ\text{C}$		1.2	
Continuous Drain Current (Note 6) $V_{GS} = 4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$	I_D	2.0	A
		$T_A = +70^\circ\text{C}$		1.6	
Pulsed Drain Current (10 μs Pulse, Duty Cycle = 1%)			I_{DM}	8	A
Maximum Body Diode Continuous Current (Note 6)			I_S	1.6	A

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation	(Note 5)	P_D	0.7	W
	(Note 6)		1.3	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	176	$^\circ\text{C/W}$
	(Note 6)		102	
Thermal Resistance, Junction to Case	(Note 6)	$R_{\theta JC}$	45	$^\circ\text{C/W}$
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	30	37	—	V	$V_{GS} = 0\text{V}, I_D = 100\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 12\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.5	—	1	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	100	150	m Ω	$V_{GS} = 4.5\text{V}, I_D = 4.5\text{A}$
			140	200		$V_{GS} = 2.5\text{V}, I_D = 3.5\text{A}$
			185	250		$V_{GS} = 1.8\text{V}, I_D = 1.5\text{A}$
			240	300		$V_{GS} = 1.5\text{V}, I_D = 0.5\text{A}$
Diode Forward Voltage	V_{SD}	—	0.8	1.1	V	$V_{GS} = 0\text{V}, I_S = 0.5\text{A}$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	193	—	pF	$V_{DS} = 10\text{V}, V_{GS} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	35	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	23	—	pF	
Turn-On Delay Time	$t_{D(ON)}$	—	7	—	ns	$V_{DD} = 10\text{V}, R_L = 10\Omega$ $I_D = 1\text{A}, V_{GEN} = 4.5\text{V}, R_G = 6\Omega$
Rise Time	t_R	—	24	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	24	—		
Fall Time	t_F	—	12	—		

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

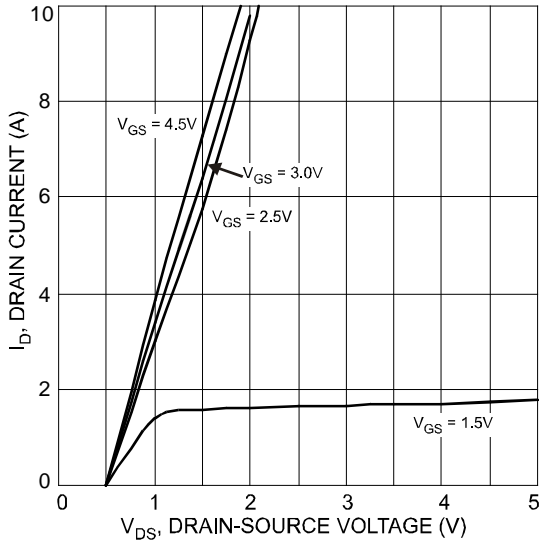


Fig. 1 Typical Output Characteristic

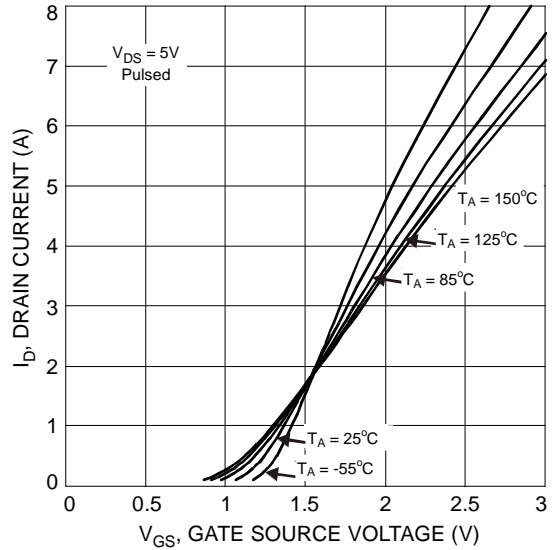


Fig. 2 Typical Transfer Characteristics

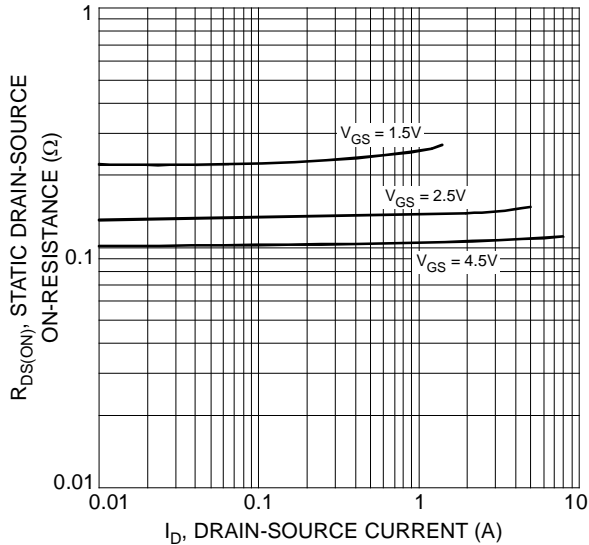


Fig. 3 On-Resistance vs. Drain Current & Gate Voltage

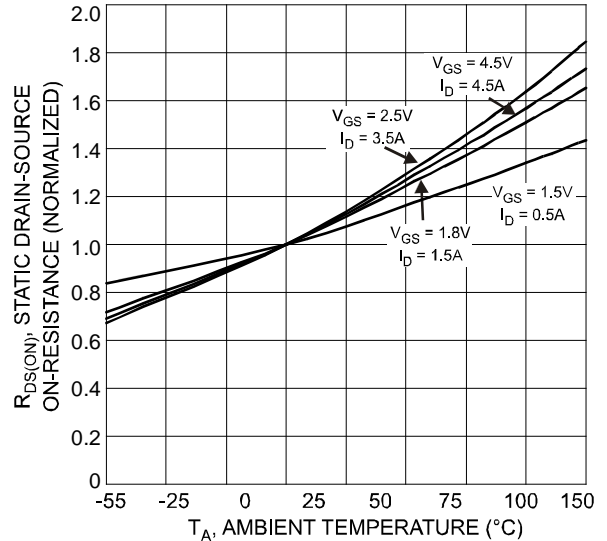


Fig. 4 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

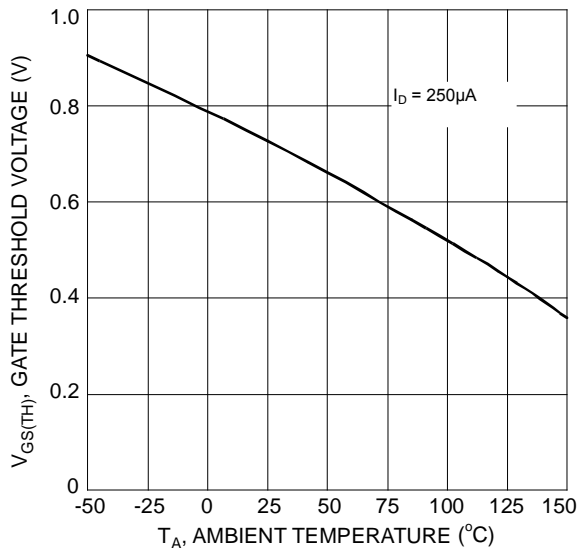


Fig. 5 Gate Threshold Variation vs. Ambient Temperature

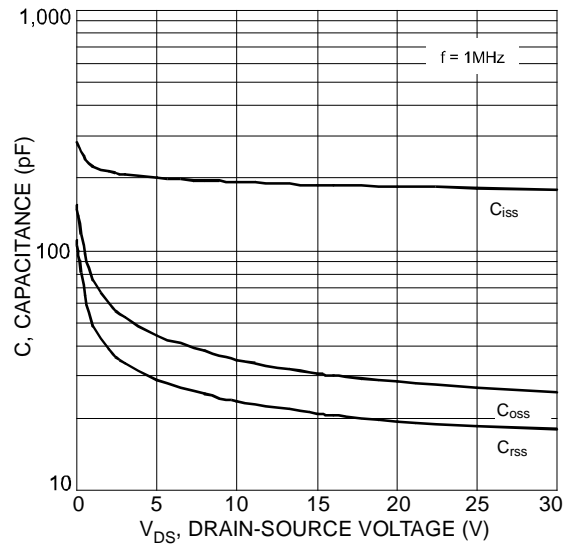


Fig. 6 Typical Total Capacitance

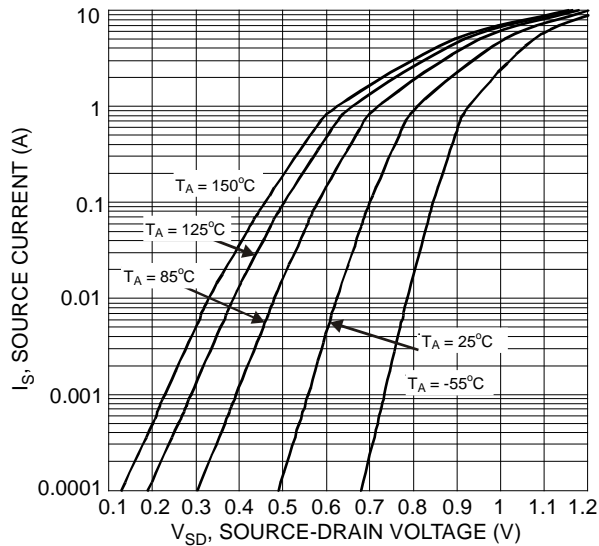


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

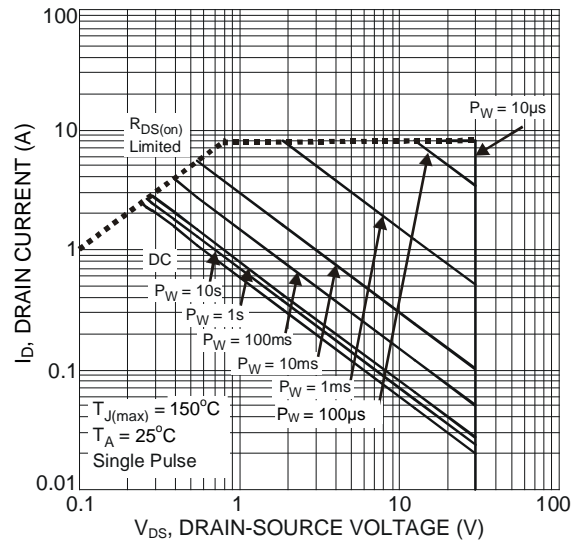


Fig. 8 SOA, Safe Operation Area

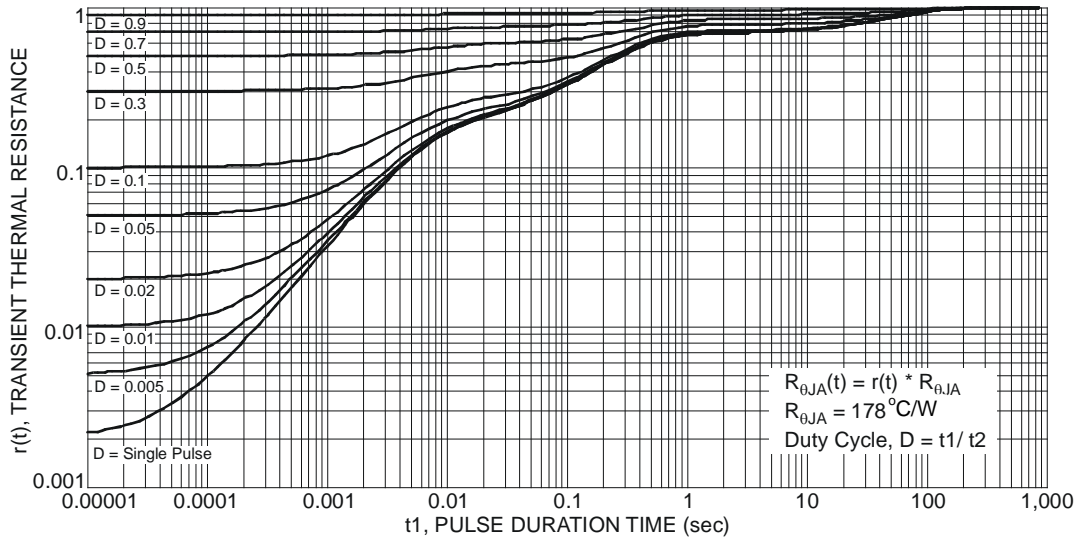
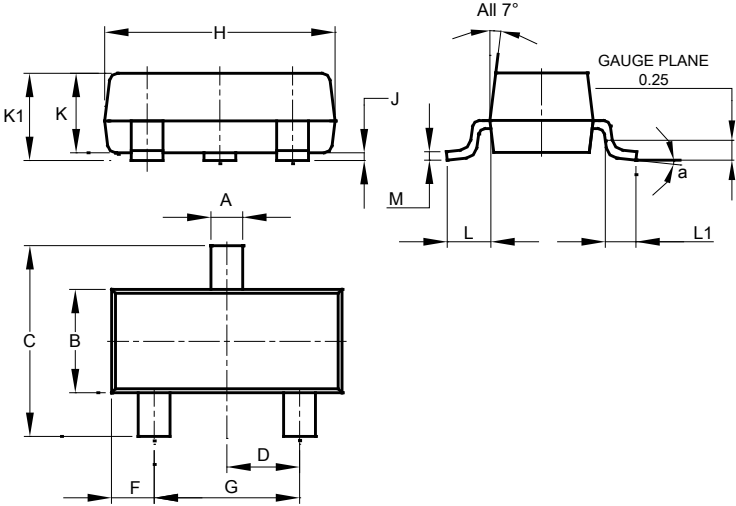


Fig. 9 Transient Thermal Resistance

Package Outline Dimensions

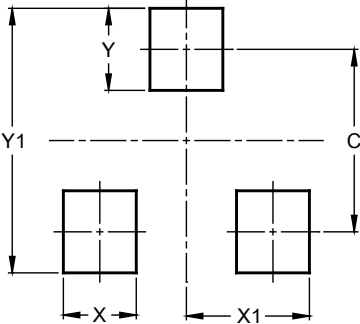
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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