





40V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	I _D T _A = 25°C		
40V	36mΩ @ V _{GS} = 10V	12.2A		
	61mΩ @ V _{GS} = 4.5V	9.4A		

Description and Applications

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- DC-DC Converters
- · Power management functions

Features and Benefits

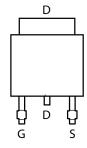
- · Low on-resistance
- Fast switching speed
- "Green" component and RoHS compliant (Note 1)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

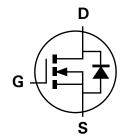
- Case: TO252-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0 (Note 1)
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Matte Tin Finish annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208
- Weight: 0.33 grams (approximate)







PIN OUT -TOP VIEW



Equivalent Circuit

Ordering Information (Note 1)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN4036LK3-13	N4036L	13	16	2,500

Note:

Marking Information



OH = Manufacturer's Marking
N4036L = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 09 = 2009)
WW = Week (01 - 53)

^{1.} Diodes, Inc. defines "Green" products as those which are RoHS compliant and contain no halogens or antimony compounds; further information about Diodes Inc.'s "Green" Policy can be found on our website. For packaging details, go to our website.





Maximum Ratings @T_A = 25°C unless otherwise specified

Ch	naracteristic		Symbol	Value	Unit
Drain-Source voltage			V_{DSS}	40	V
Gate-Source voltage		(Note 2)	V _{GS}	±20	V
		(Note 4)		12.2	
Continuous Drain current	$V_{GS} = 10V$	$T_A = 70^{\circ}C$ (Note 4)	I_{D}	9.7	А
		(Note 3)		8.5	
Pulsed Drain current V _{GS} = 10V		(Note 5)	I _{DM}	31.7	A
Continuous Source current (Body diode)	(Note 4)	Is	10.4	Α
Pulsed Source current (Body diode)		(Note 5)	I _{SM}	31.7	А

Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
	(Note 3)		4.12 33		
Power dissipation Linear derating factor	(Note 4)	P _D	8.49 67.9	W mW/°C	
	(Note 6)		2.12 16.9		
	(Note 3)		30.3		
Thermal Resistance, Junction to Ambient	(Note 4)	$R_{ hetaJA}$	R _θ JA 14.7		
	(Note 6)	Ī	59.0	°C/W	
Thermal Resistance, Junction to Lead	(Note 7)	$R_{ heta JL}$	3.1		
Operating and storage temperature range		T _J , T _{STG}	-55 to 150	°C	

Notes:

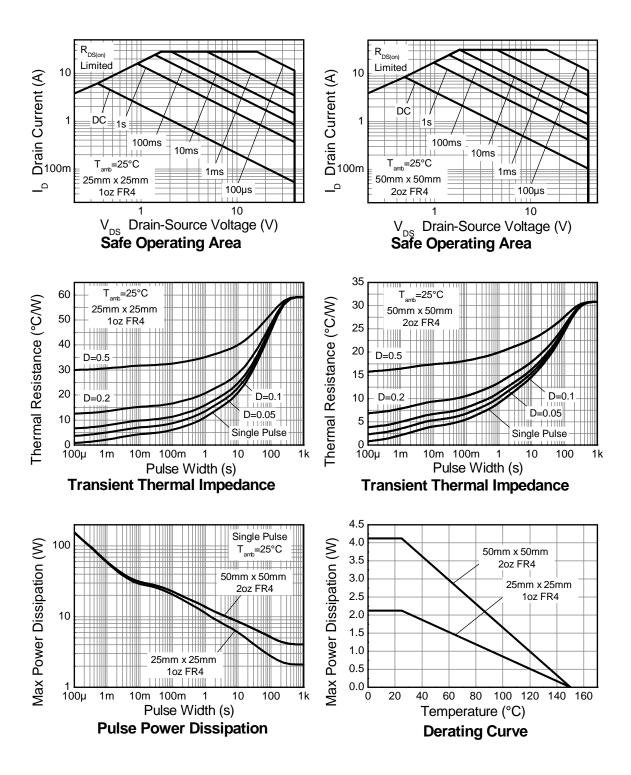
- 2. AEC-Q101 V_{GS} maximum is ±16V.
- 3. For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 4. Same as note 3, except the device is measured at $t \le 10$ sec.
- 5. Same as note 3, except the device is pulsed with D = 0.02 and pulse width 300 µs. The pulse current is limited by the maximum junction temperature.

 6. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 7. Thermal resistance from junction to solder-point (at the end of the drain lead).





Thermal Characteristics







Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Co	ondition	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$I_D = 250 \mu A, V_{GS}$	= 0V	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	0.5	μΑ	$V_{DS} = 40V, V_{GS} =$	= 0V	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS}$	= 0V	
ON CHARACTERISTICS				•	•	•		
Gate Threshold Voltage	V _{GS(th)}	1.0	_	3.0	V	$I_D = 250 \mu A, V_{DS} = V_{GS}$		
Static Drain Source On Registence (Note 9)	_		0.026	0.036	Ω	V _{GS} = 10V, I _D = 12A		
Static Drain-Source On-Resistance (Note 8)	R _{DS (ON)}		0.049	0.061	12	$V_{GS} = 4.5V, I_{D} =$	6A	
Forward Transconductance (Notes 8 & 9)	9fs		19.6	_	S	$V_{DS} = 15V, I_{D} = 1$	2A	
Diode Forward Voltage (Note 8)	V _{SD}	_	0.96	1.1	V	I _S = 12A, V _{GS} = 0)V	
Reverse recovery time (Note 9)	t _{rr}		112	_	ns	400 11/14 4000/ -		
Reverse recovery charge (Note 9)	Q _{rr}		926	_	nC	$I_S = 12A$, di/dt = $\frac{1}{2}$	υυΑ/μ\$	
DYNAMIC CHARACTERISTICS (Note 9)				<u>.</u>				
Input Capacitance	C _{iss}		453	_	pF	.,	0) (
Output Capacitance	Coss		79.1	_	pF	$V_{DS} = 20V, V_{GS} = 0V$ of = 1MHz		
Reverse Transfer Capacitance	C _{rss}	_	40.5	_	pF			
Total Gate Charge (Note 10)	Qg		4.9	_	nC	$V_{GS} = 4.5V$		
Total Gate Charge (Note 10)	Qg	_	9.2	_	nC		$V_{DS} = 20V$	
Gate-Source Charge (Note 10)	Q _{gs}	_	1.7	_	nC	$V_{GS} = 10V$ $I_D = 12A$		
Gate-Drain Charge (Note 10)	Q_{gd}	_	2.7	_	nC			
Turn-On Delay Time (Note 10)	t _{D(on)}	_	3.2	_	ns			
Turn-On Rise Time (Note 10)	t _r	_	11.7	_	ns	V _{DD} = 20V, V _{GS} = 10V		
Turn-Off Delay Time (Note 10)	t _{D(off)}	_	11.6	_	ns	$I_D = 12A, R_G \cong 6.0\Omega$		
Turn-Off Fall Time (Note 10)	t _f		9.5	_	ns	_ , 3		

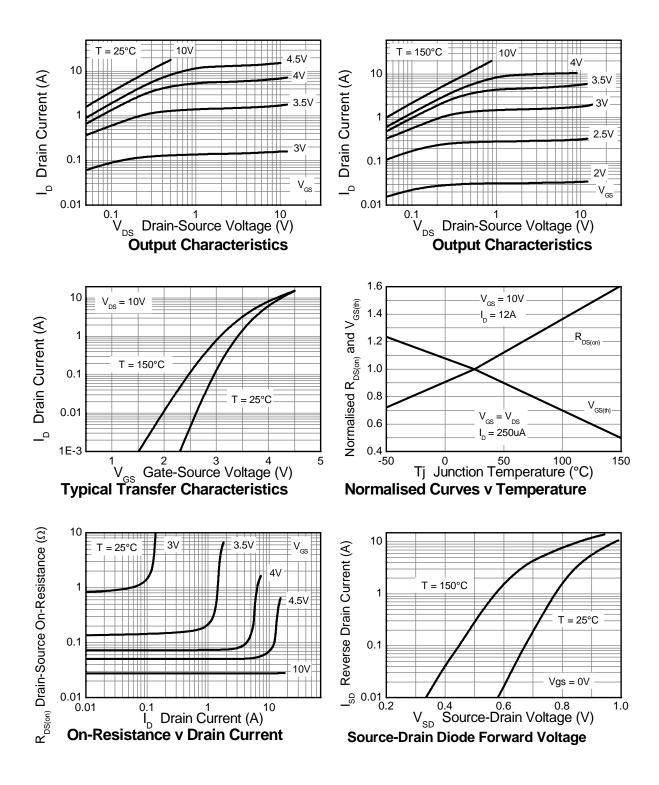
Notes:

- 8. Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2\%$
- Wedsdred what passed conditions. False what is sooned, and yeyline is 27%
 For design aid only, not subject to production testing.
 Switching characteristics are independent of operating junction temperatures.



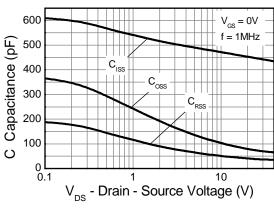


Typical Characteristics

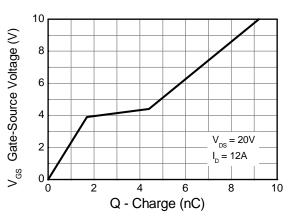




Typical Characteristics - continued

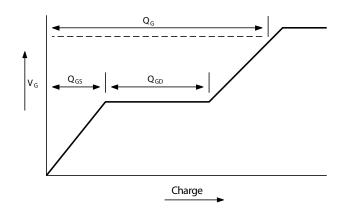


Capacitance v Drain-Source Voltage

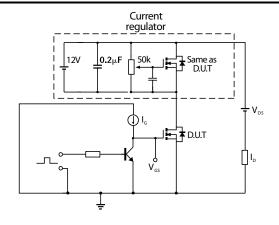


Gate-Source Voltage v Gate Charge

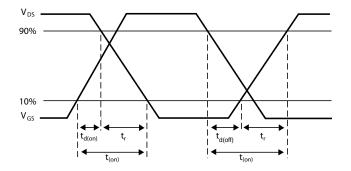
Test Circuits



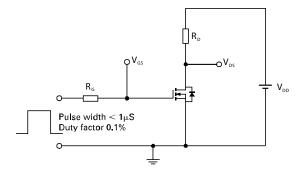
Basic gate charge waveform



Gate charge test circuit



Switching time waveforms

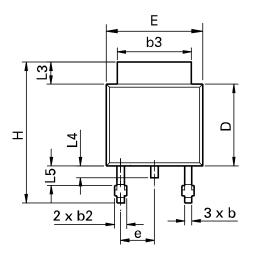


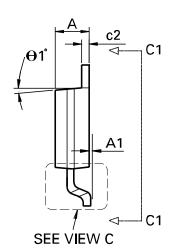
Switching time test circuit

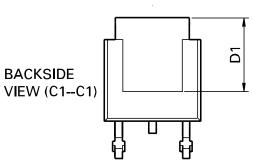


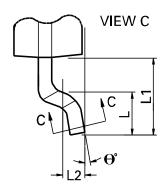


Package Outline Dimensions





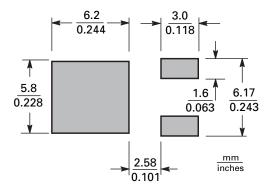




DIM	DIM Inches		Millimeters		DIM	Inches		Millimeters	
	Min	Max	Min	Max		Min	Max	Min	Max
Α	0.086	0.094	2.18	2.39	е	0.090 BSC		2.29 BSC	
A1	-	0.005	-	0.127	Н	0.370	0.410	9.40	10.41
b	0.020	0.035	0.508	0.89	L	0.055	0.070	1.40	1.78
b2	0.030	0.045	0.762	1.14	L1	0.108 REF		2.74 REF	
b3	0.205	0.215	5.21	5.46	L2	0.020 BSC		0.508 BSC	
С	0.018	0.024	0.457	0.61	L3	0.035	0.065	0.89	1.65
c2	0.018	0.023	0.457	0.584	L4	0.025	0.040	0.635	1.016
D	0.213	0.245	5.41	6.22	L5	0.045	0.060	1.14	1.52
D1	0.205	-	5.21	-	θ1°	0°	10°	0°	10°
E	0.250	0.265	6.35	6.73	θ°	0°	15°	0°	15°
E1	0.170	-	4.32	-	-	-	-	-	_



Suggested Pad Layout



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