

Product Summary

BV _{DSS}	Rds(on)	I _D TA = +25°C
	1.6Ω @ V _{GS} = 10V	0.46A
50V	2.5Ω @ V _{GS} = 4.5V	0.36A

Features

- **Dual N-Channel MOSFET** •
- Low On-Resistance •
- Low Input Capacitance •
- Fast Switching Speed •
- Small Surface Mount Package •
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMN53D0LDWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/guality/product-definitions/

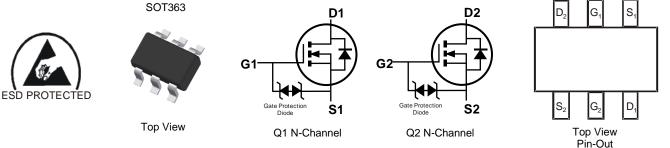
Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Backlighting
- **Power Management Functions**
- **DC-DC Converters**

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe . (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMN53D0LDWQ-7	SOT363	3000/Tape & Reel
DMN53D0LDWQ-13	SOT363	10000/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. Notes: 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and l ead-free

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

			Γ	٦
N	1M5		ΥM	
V	<u>۸۸</u>	5	MM	
			Τ	

MM5 = Product Type Marking Code YM = Date Code Marking \overline{Y} = Year (ex: H = 2020) M = Month (ex: 9 = September)

Bate Boad Hoy	Date	Code	Key
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Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Н		J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	Vdss	50	V		
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6), $V_{GS} = 10V$ Steady State $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$			ID	0.46 0.37	А
Pulsed Drain Current (10µs Pulse, 1% Duty Cyc	IDM	1	А		
Maximum Continuous Body Diode Forward Curr	ent (Note 6)		ls	0.46	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	0.4	W
Thermal Resistance, Junction to Ambient (Note 5)	Reja	317.6	°C/W	
Total Power Dissipation (Note 6)		PD	0.5	W
Thermal Resistance, Junction to Ambient (Note 6) Steady State		R _{0JA}	249.8	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

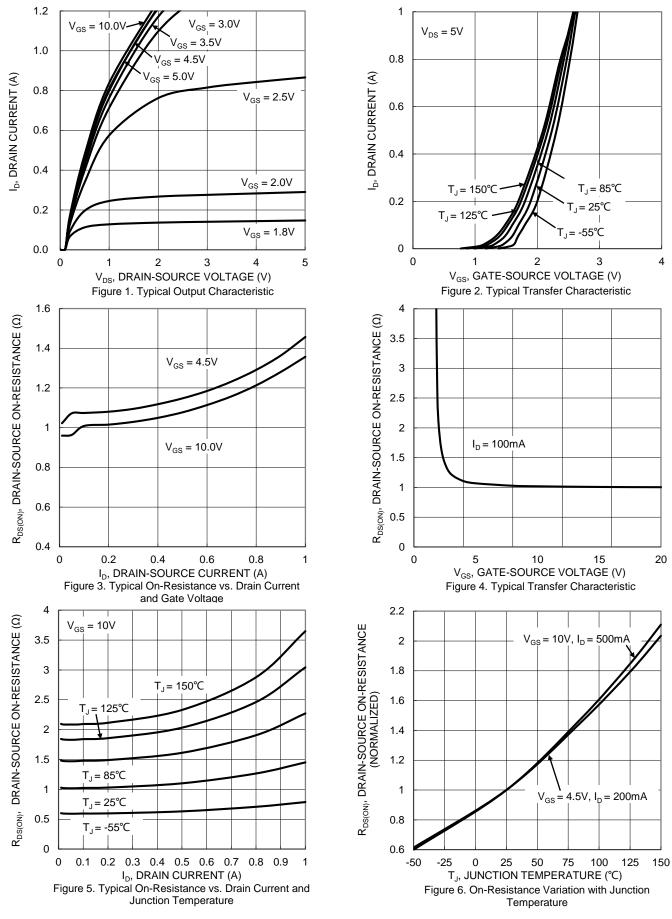
		r	r	r	r		
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)						-	
Drain-Source Breakdown Voltage	BV _{DSS}	50			V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	—	—	1.0	μA	$V_{DS} = 50V, V_{GS} = 0V$	
Gate-Body Leakage	lgss	_	_	10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(th)	0.8	—	1.5	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
		_	1.0	1.6		$V_{GS} = 10V, I_D = 500mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	—	1.0	2.5	Ω	$V_{GS} = 4.5V, I_D = 200mA$	
		—	1.4	4.5		$V_{GS} = 2.5V, I_D = 100mA$	
Source-Drain Diode Forward Voltage	Vsd	_	0.8	1.4	V	Vgs = 0V, Is = 500mA	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	49.5	—			
Output Capacitance	Coss	—	5.2	_	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	3.7	_		1 - 1.00012	
Gate Resistance	Rg	—	53	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$	
Total Gate Charge V _{GS} = 10V	Qg	_	1.4	—			
Total Gate Charge V _{GS} = 4.5V	Qg	_	0.7	—	nC	1/22 - 10/2 = -250mA	
Gate-Source Charge	Qgs	_	0.2	_	ne	$V_{DS} = 10V, I_{D} = 250mA$	
Gate-Drain Charge	Qgd	_	0.1	_			
Turn-On Delay Time	tD(ON)	—	3.7				
Turn-On Rise Time	tR	—	1.6	—		V _{DD} = 30V, V _{GS} = 10V,	
Turn-Off Delay Time	tD(OFF)	—	18.7	—	ns	$R_G = 25\Omega$, $I_D = 200mA$	
Turn-Off Fall Time	tF	_	7.0	_			

Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.

Device mounted on FR-4 FCB, with minimum recommended paid.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.



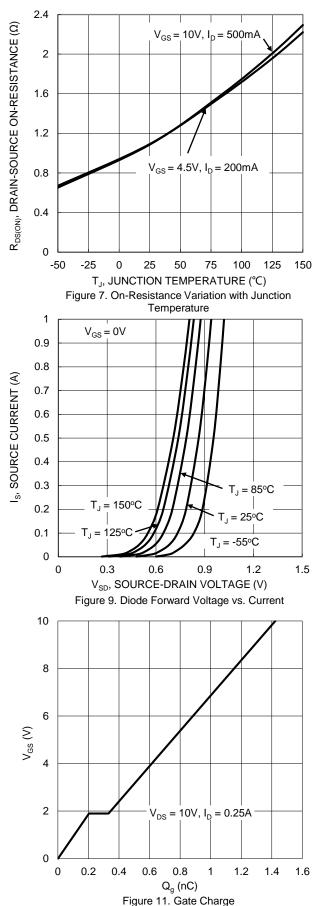
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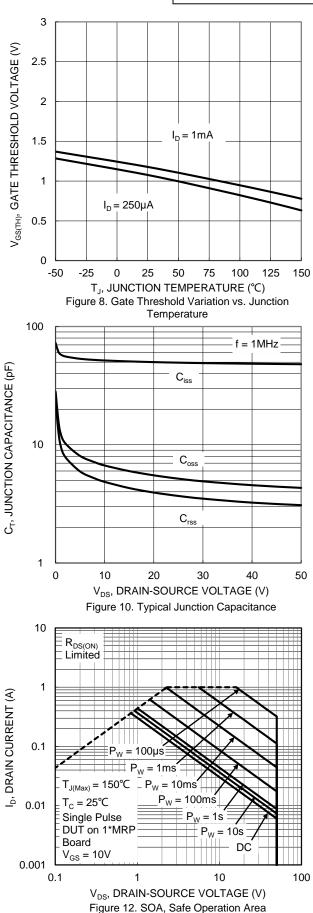


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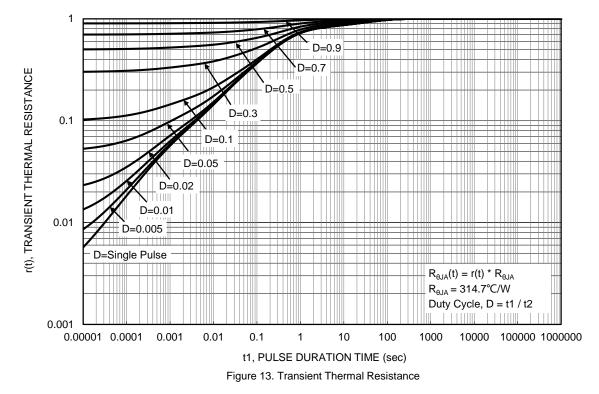






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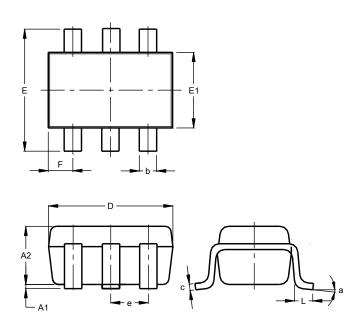






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



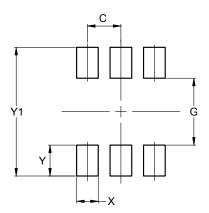
SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.10	0.30	0.25				
с	0.10	0.22	0.11				
D	1.80	2.20	2.15				
ш	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
e	e 0.650 BSC						
F	0.40	0.45	0.425				
1	0.25	0.40	0.30				
а	0°	8°					
All I	Dimen	sions	in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363

SOT363



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500



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